



SPEC CPU®2017 Floating Point Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen11

(2.10 GHz, Intel Xeon Platinum 8570)

SPECrate®2017_fp_base = 1110

SPECrate®2017_fp_peak = 1150

CPU2017 License: 3

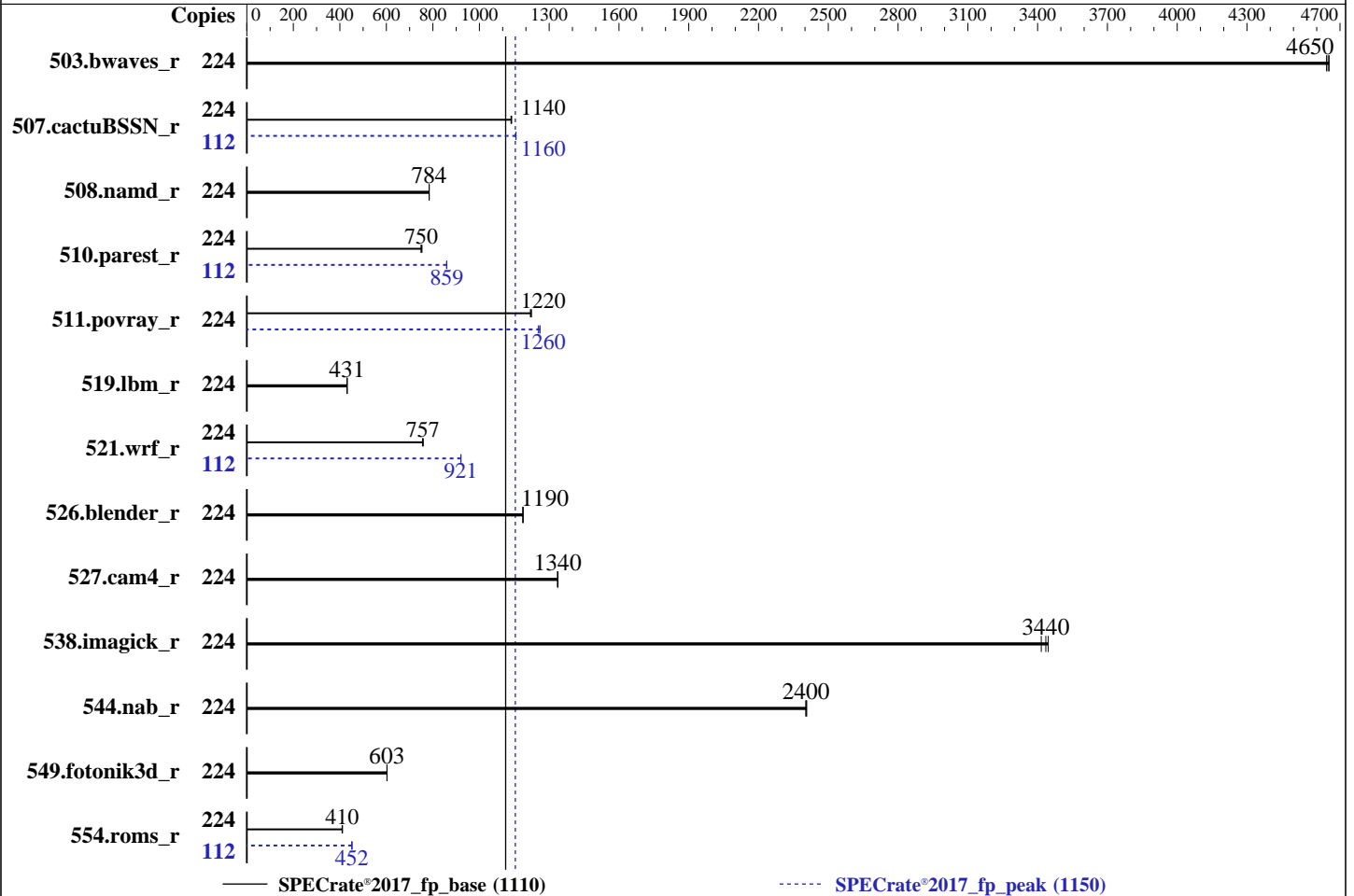
Test Sponsor: HPE

Tested by: HPE

Test Date: Mar-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023



Hardware

CPU Name: Intel Xeon Platinum 8570
 Max MHz: 4000
 Nominal: 2100
 Enabled: 112 cores, 2 chips, 2 threads/core
 Orderable: 1, 2 chip(s)
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 300 MB I+D on chip per chip
 Other: None
 Memory: 512 GB (16 x 32 GB 2Rx8 PC5-5600B-R)
 Storage: 1 x 480 GB SATA SSD
 Other: Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP5
 Kernel 5.14.21-150500.53-default
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
 Parallel: No
 Firmware: HPE BIOS Version v2.12 12/13/2023 released Dec-2023
 File System: btrfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	224	483	4650	483	4650	484	4640	224	483	4650	483	4650	484	4640
507.cactuBSSN_r	224	250	1140	249	1140	249	1140	112	123	1160	122	1160	123	1160
508.namd_r	224	271	784	272	784	271	784	224	271	784	272	784	271	784
510.parest_r	224	781	750	782	749	778	753	112	341	860	341	859	341	859
511.povray_r	224	427	1220	429	1220	428	1220	224	415	1260	415	1260	417	1250
519.lbm_r	224	548	431	547	431	548	431	224	548	431	547	431	548	431
521.wrf_r	224	663	757	664	755	662	758	112	273	920	273	921	272	921
526.blender_r	224	287	1190	287	1190	287	1190	224	287	1190	287	1190	287	1190
527.cam4_r	224	293	1340	293	1340	293	1340	224	293	1340	293	1340	293	1340
538.imagick_r	224	162	3440	163	3420	162	3450	224	162	3440	163	3420	162	3450
544.nab_r	224	157	2400	157	2400	157	2410	224	157	2400	157	2400	157	2410
549.fotonik3d_r	224	1449	603	1449	603	1447	603	224	1449	603	1449	603	1447	603
554.roms_r	224	865	412	868	410	869	410	112	394	452	394	451	394	452

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
tuned-adm profile was set to throughput-performance using "tuned-adm profile throughput-performance"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

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General Notes (Continued)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

The system ROM used for this result contains Intel microcode version 0x21000200 for the Intel Xeon Platinum 8570 processor.

BIOS Configuration:

Workload Profile set to General Throughput Compute

Memory Patrol Scrubbing set to Disabled

Last Level Cache (LLC) Dead Line Allocation set to Disabled

Enhanced Processor Performance Profile set to Aggressive

Thermal Configuration set to Maximum Cooling

Workload Profile set to Custom

DCU Stream Prefetcher set to Disabled

Adjacent Sector Prefetch set to Disabled

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Wed Mar 6 18:37:34 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
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1. uname -a

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Platform Notes (Continued)

Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux

2. w
18:37:34 up 3 min, 0 users, load average: 0.02, 0.04, 0.01

USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2062549
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2062549
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root@notty
bash -c cd \$SPEC/ && \$SPEC/fprateTP.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=224 -c
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=112 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=224 --configfile
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=112 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/cpu2017

6. /proc/cpuinfo
model name : INTEL(R) XEON(R) PLATINUM 8570
vendor_id : GenuineIntel
cpu family : 6
model : 207
stepping : 2
microcode : 0x21000200
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores : 56
siblings : 112
2 physical ids (chips)

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Platform Notes (Continued)

224 processors (hardware threads)
physical id 0: core ids 0-55
physical id 1: core ids 0-55
physical id 0: apicids 0-111
physical id 1: apicids 128-239

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Address sizes:        46 bits physical, 57 bits virtual
Byte Order:           Little Endian
CPU(s):               224
On-line CPU(s) list: 0-223
Vendor ID:            GenuineIntel
Model name:           INTEL(R) XEON(R) PLATINUM 8570
CPU family:           6
Model:                207
Thread(s) per core:  2
Core(s) per socket:  56
Socket(s):            2
Stepping:             2
BogoMIPS:             4200.00
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                    clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                    nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                    ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                    sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                    lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                    invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                    vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
                    bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                    avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                    xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                    cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                    avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                    avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                    cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                    arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
Virtualization:       VT-x
L1d cache:           5.3 MiB (112 instances)
L1i cache:           3.5 MiB (112 instances)
L2 cache:            224 MiB (112 instances)
L3 cache:            600 MiB (2 instances)
NUMA node(s):        4
NUMA node0 CPU(s):  0-27,112-139
NUMA node1 CPU(s):  28-55,140-167
NUMA node2 CPU(s):  56-83,168-195
NUMA node3 CPU(s):  84-111,196-223
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:  Not affected
Vulnerability Mds:   Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected

```

(Continued on next page)



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Platform Notes (Continued)

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBR SB-eIBRS SW sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	5.3M	12	Data	1	64	1	64
L1i	32K	3.5M	8	Instruction	1	64	1	64
L2	2M	224M	16	Unified	2	2048	1	64
L3	300M	600M	20	Unified	3	245760	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-27,112-139
node 0 size: 128703 MB
node 0 free: 128125 MB
node 1 cpus: 28-55,140-167
node 1 size: 129009 MB
node 1 free: 128449 MB
node 2 cpus: 56-83,168-195
node 2 size: 129009 MB
node 2 free: 128521 MB
node 3 cpus: 84-111,196-223
node 3 size: 128936 MB
node 3 free: 128398 MB
node distances:
node  0  1  2  3
 0:  10  20  30  30
 1:  20  10  30  30
 2:  30  30  10  20
 3:  30  30  20  10

```

9. /proc/meminfo

MemTotal: 528036232 kB

10. who -r

run-level 3 Mar 6 18:36

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	apparmor auditd cron getty@ irqbalance issue-generator kbdsettings lvm2-monitor postfix purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell grub2-once haveged haveged-switch-root issue-add-ssh-keys kexec-load lunmask rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-network-generator

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Platform Notes (Continued)

indirect systemd-sysexec systemd-time-wait-sync systemd-timesyncd tuned wickedd

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=930f8b53-ff67-403e-9053-e3aa4fef0050
splash=silent
mitigations=auto
quiet
security=apparmor

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. tuned-adm active
  Current active profile: throughput-performance

```

```

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness     20
vm.dirty_background_bytes       0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  20
vm.dirty_writeback_centisecs    500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold            500
vm.min_unmapped_ratio           1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy       0
vm.nr_overcommit_hugepages     0
vm.swappiness                    10
vm.watermark_boost_factor       15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            0

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64

```

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Platform Notes (Continued)

pages_to_scan 4096
scan_sleep_millisecs 10000

19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5

20. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 btrfs 446G 207G 240G 47% /home

21. /sys/devices/virtual/dmi/id
Vendor: HPE
Product: ProLiant ML350 Gen11
Product Family: ProLiant
Serial: CNX20800P4

22. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
16x Hynix HMCG88AGBRA193N 32 GB 2 rank 5600

23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: HPE
BIOS Version: 2.12
BIOS Date: 12/13/2023
BIOS Revision: 2.12
Firmware Revision: 1.54

Compiler Version Notes

=====
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

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Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
 Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
 Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
 Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
 C++, C, Fortran | 507.cactuBSSN_r(base, peak)
 =====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
 Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
 Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
 Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
 Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
 Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
 Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
 =====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
 Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
 Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
 =====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
 Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
 Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
 Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx



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Base Portability Flags

```

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

```

Base Optimization Flags

C benchmarks:

```

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

C++ benchmarks:

```

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

Fortran benchmarks:

```

-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

Benchmarks using both Fortran and C:

```

-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

```

Benchmarks using both C and C++:

```

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512

```

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SPEC CPU®2017 Floating Point Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen11

(2.10 GHz, Intel Xeon Platinum 8570)

SPECrate®2017_fp_base = 1110

SPECrate®2017_fp_peak = 1150

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Mar-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

```
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:

```
icx
```

C++ benchmarks:

```
icpx
```

Fortran benchmarks:

```
ifx
```

Benchmarks using both Fortran and C:

```
ifx icx
```

Benchmarks using both C and C++:

```
icpx icx
```

Benchmarks using Fortran, C, and C++:

```
icpx icx ifx
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
519.lbm_r: basepeak = yes
```

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Peak Optimization Flags (Continued)

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

521.wrf_r: -w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

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Test Date: Mar-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

Peak Optimization Flags (Continued)

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.xml>

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