SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR650 V3
(2.60 GHz, Intel Xeon Silver 4509Y)

SPECrate®2017_fp_base =   257

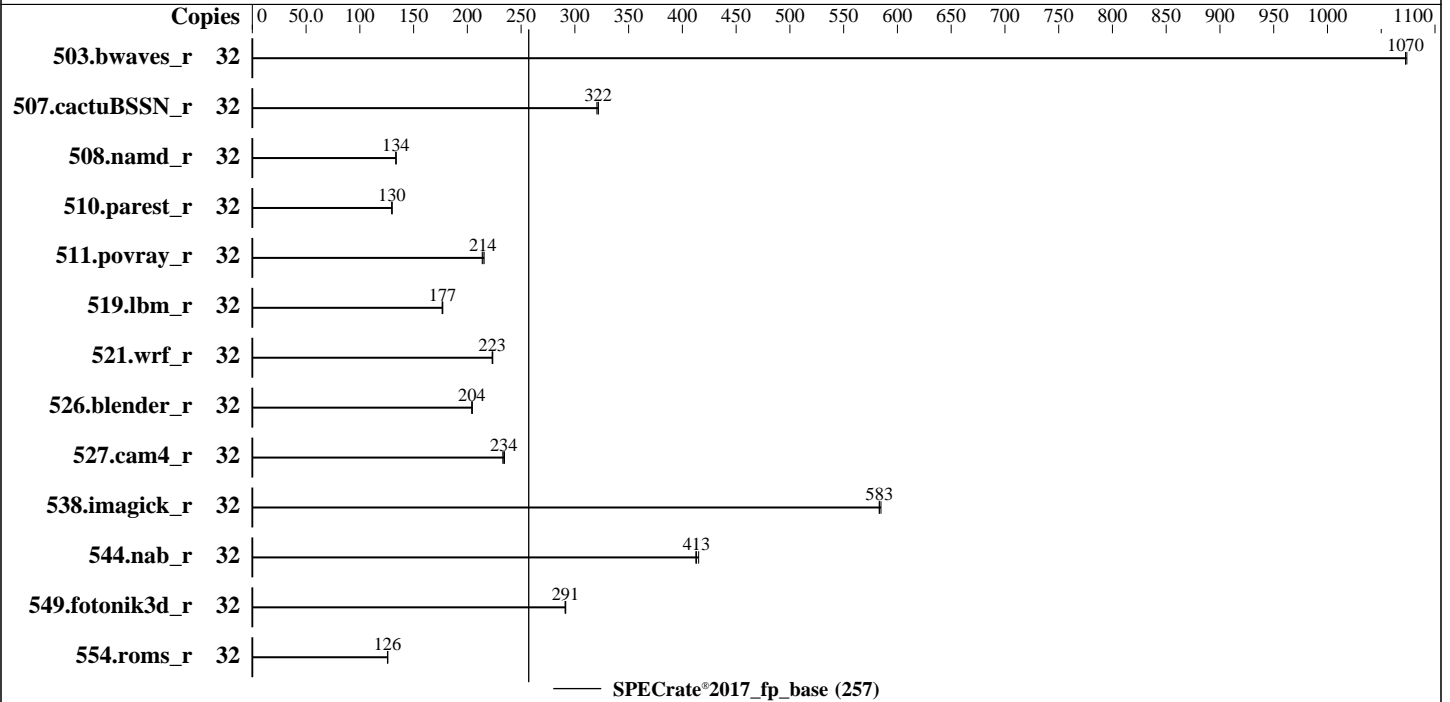SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2024
Hardware Availability: Feb-2024
Software Availability: Dec-2023

Benchmark	Copies	Result
503.bwaves_r	32	1070
507.cactuBSSN_r	32	322
508.namd_r	32	134
510.parest_r	32	130
511.povray_r	32	214
519.lbm_r	32	177
521.wrf_r	32	223
526.blender_r	32	204
527.cam4_r	32	234
538.imagick_r	32	583
544.nab_r	32	413
549.fotonik3d_r	32	291
554.roms_r	32	126

SPECrate®2017_fp_base (257)

Hardware

CPU Name: Intel Xeon Silver 4509Y
 Max MHz: 4100
 Nominal: 2600
Enabled: 16 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 22.5 MB I+D on chip per chip
 Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4400)
Storage: 1 x 960 GB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4
 Kernel 5.14.21-150400.22-default
Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Lenovo BIOS Version ESE121V 3.10 released Jan-2024
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	32	299	1070	**299**	**1070**	299	1070							
507.cactuBSSN_r	32	**126**	**322**	126	322	126	320							
508.namd_r	32	**227**	**134**	228	134	227	134							
510.parest_r	32	645	130	644	130	**645**	**130**							
511.povray_r	32	**348**	**214**	349	214	347	216							
519.lbm_r	32	**191**	**177**	191	177	190	177							
521.wrf_r	32	321	223	**321**	**223**	320	224							
526.blender_r	32	**238**	**204**	239	204	238	205							
527.cam4_r	32	239	235	240	233	**239**	**234**							
538.imagick_r	32	136	585	136	583	**136**	**583**							
544.nab_r	32	130	415	131	413	**130**	**413**							
549.fotonik3d_r	32	429	291	**428**	**291**	428	292							
554.roms_r	32	403	126	**404**	**126**	405	126							
SPECrate®2017_fp_base =	**257**													
SPECrate®2017_fp_peak =	**Not Run**													

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/home/cpu2017-1.1.9-ic2023.2.3/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>        /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
```

(Continued on next page)

Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

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Test Sponsor:　Lenovo Global Technology　　　　　　　　**Hardware Availability:** Feb-2024
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General Notes (Continued)

```
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS configuration:
Choose Operating Mode set to Maximum Performance
AMP Prefetch set to Enable

 Sysinfo program /home/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Mon Jan 22 09:27:54 2024

 SUT (System Under Test) info as seen by some common utilities.

 ----------------------------------------------------------
 Table of contents
 ----------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
 ----------------------------------------------------------


 ----------------------------------------------------------
 1. uname -a
   Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
   x86_64 x86_64 x86_64 GNU/Linux


 ----------------------------------------------------------
 2. w
    09:27:54 up 1 min,  1 user,  load average: 0.11, 0.04, 0.01
    USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
    root     tty1     -                09:27   10.00s  0.92s  0.01s -bash

 ----------------------------------------------------------
```

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Platform Notes (Continued)

```
  3. Username
    From environment variable $USER:  root


    ------------------------------------------------------------
  4. ulimit -a
    core file size          (blocks, -c) unlimited
    data seg size           (kbytes, -d) unlimited
    scheduling priority             (-e) 0
    file size               (blocks, -f) unlimited
    pending signals                 (-i) 4127074
    max locked memory       (kbytes, -l) 64
    max memory size         (kbytes, -m) unlimited
    open files                      (-n) 1024
    pipe size            (512 bytes, -p) 8
    POSIX message queues     (bytes, -q) 819200
    real-time priority              (-r) 0
    stack size              (kbytes, -s) unlimited
    cpu time               (seconds, -t) unlimited
    max user processes              (-u) 4127074
    virtual memory          (kbytes, -v) unlimited
    file locks                      (-x) unlimited


    ------------------------------------------------------------
  5. sysinfo process ancestry
   /usr/lib/systemd/systemd --switched-root --system --deserialize 30
   login -- root
   -bash
   -bash
   runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
     ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=16 --define physicalfirst
     --define invoke_with_interleave --define drop_caches --tune base -o all fprate
   runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
     ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=16 --define physicalfirst
     --define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
     rate --tune base --size refrate fprate --nopreenv --note-preenv --logfile
     $SPEC/tmp/CPU2017.033/templogs/preenv.fprate.033.0.log --lognum 033.0 --from_runcpu 2
   specperl $SPEC/bin/sysinfo
   $SPEC = /home/cpu2017-1.1.9-ic2023.2.3


    ------------------------------------------------------------
  6. /proc/cpuinfo
     model name       : INTEL(R) XEON(R) SILVER 4509Y
     vendor_id        : GenuineIntel
     cpu family       : 6
     model            : 143
     stepping         : 8
     microcode        : 0x2b000571
     bugs             : spectre_v1 spectre_v2 spec_store_bypass swapgs
     cpu cores        : 8
     siblings         : 16
     2 physical ids (chips)
     32 processors (hardware threads)
     physical id 0: core ids 0-7
     physical id 1: core ids 0-7
     physical id 0: apicids 0-15
     physical id 1: apicids 64-79
    Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
    virtualized systems.  Use the above data carefully.


    ------------------------------------------------------------
```

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Platform Notes (Continued)

```
  7. lscpu

From lscpu from util-linux 2.37.2:
  Architecture:            x86_64
  CPU op-mode(s):          32-bit, 64-bit
  Address sizes:           46 bits physical, 57 bits virtual
  Byte Order:              Little Endian
  CPU(s):                  32
  On-line CPU(s) list:     0-31
  Vendor ID:               GenuineIntel
  Model name:              INTEL(R) XEON(R) SILVER 4509Y
  CPU family:              6
  Model:                   143
  Thread(s) per core:      2
  Core(s) per socket:      8
  Socket(s):               2
  Stepping:                8
  BogoMIPS:                5200.00
  Flags:                   fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                           clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                           lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                           nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 ds_cpl
                           vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                           x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                           abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                           intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
                           flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
                           erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                           clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                           xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                           split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                           avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                           avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                           cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                           arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities
  Virtualization:          VT-x
  L1d cache:               768 KiB (16 instances)
  L1i cache:               512 KiB (16 instances)
  L2 cache:                32 MiB (16 instances)
  L3 cache:                45 MiB (2 instances)
  NUMA node(s):            2
  NUMA node0 CPU(s):       0-7,16-23
  NUMA node1 CPU(s):       8-15,24-31
  Vulnerability Itlb multihit:   Not affected
  Vulnerability L1tf:            Not affected
  Vulnerability Mds:             Not affected
  Vulnerability Meltdown:        Not affected
  Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
  Vulnerability Spectre v1:      Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:      Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
  Vulnerability Srbds:           Not affected
  Vulnerability Tsx async abort: Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE         LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     768K   12 Data             1    64        1             64
     L1i       32K     512K    8 Instruction      1    64        1             64
     L2         2M      32M   16 Unified          2  2048        1             64
     L3      22.5M      45M   15 Unified          3 24576        1             64
```

(Continued on next page)

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Platform Notes (Continued)

```
----------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0-7,16-23
  node 0 size: 515755 MB
  node 0 free: 514635 MB
  node 1 cpus: 8-15,24-31
  node 1 size: 516037 MB
  node 1 free: 515399 MB
  node distances:
  node   0   1
    0:  10  21
    1:  21  10


----------------------------------------------------------
9. /proc/meminfo
  MemTotal:       1056555608 kB


----------------------------------------------------------
10. who -r
  run-level 3 Jan 22 09:26


----------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
  Default Target  Status
  multi-user      running


----------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd chronyd cron getty@ haveged irqbalance
                   iscsi issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback
                   rsyslog smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                   console-getty cups cups-browsed debug-shell ebtables exchange-bmc-os-info firewalld gpm
                   grub2-once haveged-switch-root ipmi ipmievd iscsi-init iscsid iscsiuio issue-add-ssh-keys
                   kexec-load ksm kvm_stat lunmask man-db-create multipathd nfs nfs-blkmap nmb ntp-wait ntpd
                   rdisc rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts smb snmpd snmptrapd
                   svnserve systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                   systemd-time-wait-sync systemd-timesyncd udisks2
  indirect         wickedd


----------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
  root=UUID=d68daf13-caf1-4614-b0e5-f766f243d7c8
  splash=silent
  mitigations=auto
  quiet
  security=apparmor


----------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 0:
    Unable to determine current policy
    boost state support:
      Supported: yes
      Active: yes
```

(Continued on next page)

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Platform Notes (Continued)

```
    ----------------------------------------------------------
    15. sysctl
     kernel.numa_balancing            1
     kernel.randomize_va_space        2
     vm.compaction_proactiveness     20
     vm.dirty_background_bytes        0
     vm.dirty_background_ratio       10
     vm.dirty_bytes                   0
     vm.dirty_expire_centisecs     3000
     vm.dirty_ratio                  20
     vm.dirty_writeback_centisecs   500
     vm.dirtytime_expire_seconds  43200
     vm.extfrag_threshold           500
     vm.min_unmapped_ratio            1
     vm.nr_hugepages                  0
     vm.nr_hugepages_mempolicy        0
     vm.nr_overcommit_hugepages       0
     vm.swappiness                   60
     vm.watermark_boost_factor    15000
     vm.watermark_scale_factor       10
     vm.zone_reclaim_mode             0


    ----------------------------------------------------------
    16. /sys/kernel/mm/transparent_hugepage
     defrag          always defer defer+madvise [madvise] never
     enabled         [always] madvise never
     hpage_pmd_size  2097152
     shmem_enabled   always within_size advise [never] deny force


    ----------------------------------------------------------
    17. /sys/kernel/mm/transparent_hugepage/khugepaged
     alloc_sleep_millisecs   60000
     defrag                      1
     max_ptes_none             511
     max_ptes_shared           256
     max_ptes_swap              64
     pages_to_scan            4096
     scan_sleep_millisecs    10000


    ----------------------------------------------------------
    18. OS release
      From /etc/*-release /etc/*-version
      os-release SUSE Linux Enterprise Server 15 SP4


    ----------------------------------------------------------
    19. Disk information
    SPEC is set to: /home/cpu2017-1.1.9-ic2023.2.3
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda2      xfs   892G   84G  808G  10% /


    ----------------------------------------------------------
    20. /sys/devices/virtual/dmi/id
        Vendor:        Lenovo
        Product:       ThinkSystem SR650 V3 MB,EGS,DDR5,SH,2U
        Product Family: ThinkSystem
        Serial:        1234567890


    ----------------------------------------------------------
    21. dmidecode
```

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Platform Notes (Continued)

```
   Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     8x Samsung M321R8GA0PB0-CWMKH 64 GB 2 rank 5600, configured at 4400
     8x Samsung M321R8GA0PB0-CWMXH 64 GB 2 rank 5600, configured at 4400


   -------------------------------------------------------------
 22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Lenovo
    BIOS Version:      ESE121V-3.10
    BIOS Date:         01/09/2024
    BIOS Revision:     3.10
    Firmware Revision: 3.90
```

Compiler Version Notes

```
==============================================================================================
C             | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
C++           | 508.namd_r(base) 510.parest_r(base)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
C++, C        | 511.povray_r(base) 526.blender_r(base)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
Fortran       | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
----------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
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Compiler Version Notes (Continued)

```
--------------------------------------------------------------------------------

================================================================================
Fortran, C    | 521.wrf_r(base) 527.cam4_r(base)
--------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifx`

Benchmarks using both Fortran and C:
`ifx icx`

Benchmarks using both C and C++:
`icpx icx`

Benchmarks using Fortran, C, and C++:
`icpx icx ifx`

Base Portability Flags

503.bwaves_r: `-DSPEC_LP64`
507.cactuBSSN_r: `-DSPEC_LP64`
508.namd_r: `-DSPEC_LP64`
510.parest_r: `-DSPEC_LP64`
511.povray_r: `-DSPEC_LP64`
519.lbm_r: `-DSPEC_LP64`
521.wrf_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
526.blender_r: `-DSPEC_LP64 -DSPEC_LINUX -funsigned-char`
527.cam4_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
538.imagick_r: `-DSPEC_LP64`
544.nab_r: `-DSPEC_LP64`
549.fotonik3d_r: `-DSPEC_LP64`

(Continued on next page)

Lenovo Global Technology

ThinkSystem SR650 V3
(2.60 GHz, Intel Xeon Silver 4509Y)

SPECrate®2017_fp_base =   257

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2024
Hardware Availability: Feb-2024
Software Availability: Dec-2023

Base Portability Flags (Continued)

554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

C++ benchmarks:
```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:
```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:
```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:
```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:
```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

SPEC CPU®2017 Floating Point Rate Result

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Lenovo Global Technology

ThinkSystem SR650 V3
(2.60 GHz, Intel Xeon Silver 4509Y)

SPECrate®2017_fp_base = 257

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
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Test Date: Jan-2024
Hardware Availability: Feb-2024
Software Availability: Dec-2023

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html
http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml
http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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