



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System  
(2.10 GHz, Intel Xeon Silver 4310)

SPECspeed®2017\_fp\_base = 125

SPECspeed®2017\_fp\_peak = 126

CPU2017 License: 9016

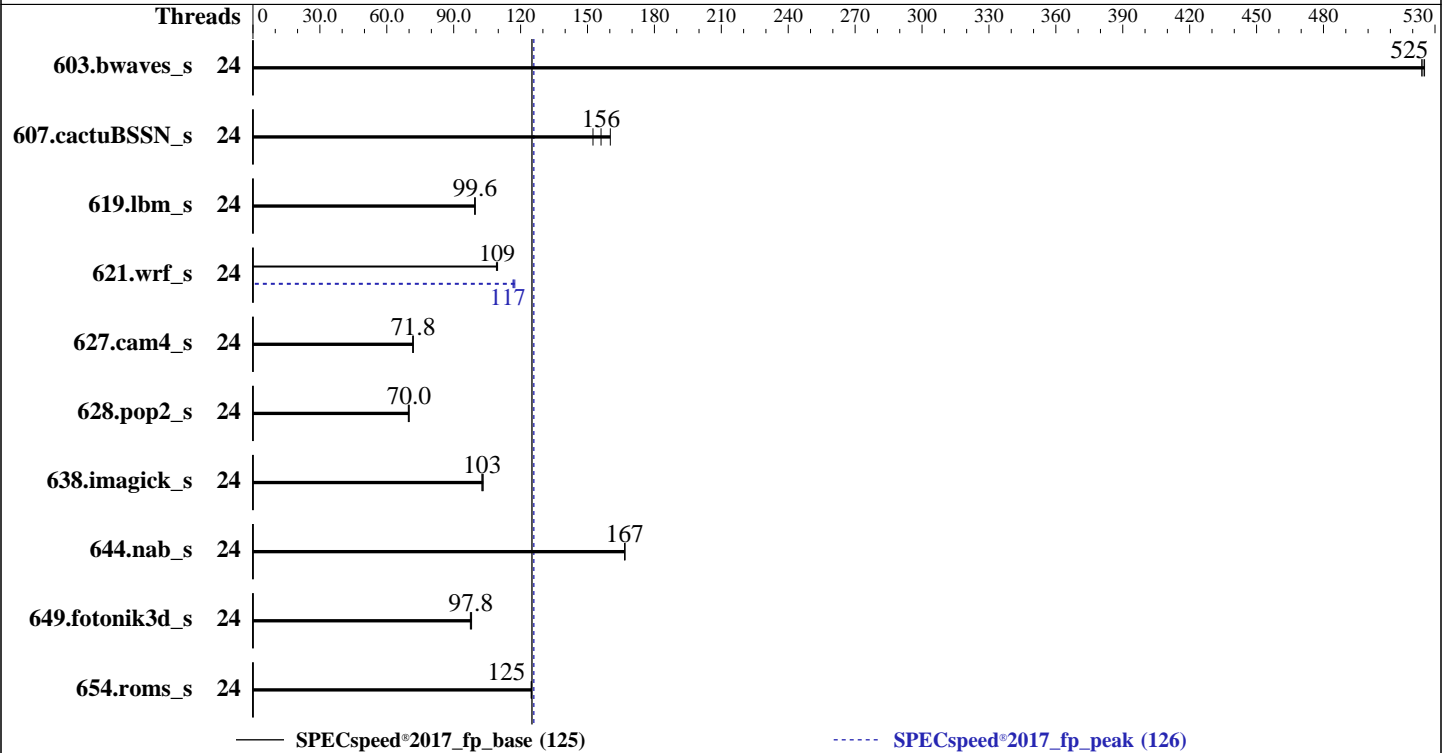
Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Jul-2022

Hardware Availability: Apr-2022

Software Availability: Oct-2021



### Hardware

CPU Name: Intel Xeon Silver 4310  
 Max MHz: 3300  
 Nominal: 2100  
 Enabled: 24 cores, 2 chips  
 Orderable: 1, 2 chip(s)  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1.25 MB I+D on chip per core  
 L3: 18 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R, running at 2666)  
 Storage: 1 x 1 TB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux release 8.4 (Ootpa) 4.18.0-305.25.1.el8\_4.x86\_64  
 Compiler: Fortran: Version 2021.4 of Intel Fortran Compiler Classic for Linux;  
 C/C++: Version 2021.4 of Intel C/C++ Compiler Classic for Linux  
 Parallel: Yes  
 Firmware: Version 0802 released Apr-2022  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System  
(2.10 GHz, Intel Xeon Silver 4310)

SPECSpeed®2017\_fp\_base = 125

SPECSpeed®2017\_fp\_peak = 126

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Jul-2022

Hardware Availability: Apr-2022

Software Availability: Oct-2021

## Results Table

Benchmark	Base						Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	24	112	525	113	524	<b>112</b>	<b>525</b>	24	112	525	113	524	<b>112</b>	<b>525</b>
607.cactuBSSN_s	24	<b>107</b>	<b>156</b>	109	152	104	160	24	<b>107</b>	<b>156</b>	109	152	104	160
619.lbm_s	24	52.7	99.4	52.6	99.6	<b>52.6</b>	<b>99.6</b>	24	52.7	99.4	52.6	99.6	<b>52.6</b>	<b>99.6</b>
621.wrf_s	24	121	109	121	110	<b>121</b>	<b>109</b>	24	113	117	<b>113</b>	<b>117</b>	113	117
627.cam4_s	24	123	71.8	<b>123</b>	<b>71.8</b>	124	71.6	24	123	71.8	<b>123</b>	<b>71.8</b>	124	71.6
628.pop2_s	24	<b>170</b>	<b>70.0</b>	170	70.0	170	69.7	24	<b>170</b>	<b>70.0</b>	170	70.0	170	69.7
638.imagick_s	24	140	103	141	103	<b>140</b>	<b>103</b>	24	140	103	141	103	<b>140</b>	<b>103</b>
644.nab_s	24	<b>105</b>	<b>167</b>	105	167	105	167	24	<b>105</b>	<b>167</b>	105	167	105	167
649.fotonik3d_s	24	<b>93.3</b>	<b>97.8</b>	93.5	97.5	93.0	98.0	24	<b>93.3</b>	<b>97.8</b>	93.5	97.5	93.0	98.0
654.roms_s	24	<b>126</b>	<b>125</b>	126	125	126	125	24	<b>126</b>	<b>125</b>	126	125	126	125

SPECSpeed®2017\_fp\_base = **125**

SPECSpeed®2017\_fp\_peak = **126**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
OS set to performance mode via cpupower frequency-set -g performance

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact"  
LD\_LIBRARY\_PATH = "/home/spec2017/lib/intel64:/home/spec2017/je5.0.1-64"  
MALLOCONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System  
(2.10 GHz, Intel Xeon Silver 4310)

SPECspeed®2017\_fp\_base = 125

SPECspeed®2017\_fp\_peak = 126

**CPU2017 License:** 9016

**Test Sponsor:** ASUSTeK Computer Inc.

**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Jul-2022

**Hardware Availability:** Apr-2022

**Software Availability:** Oct-2021

### General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS Configuration:

VT-d = Disabled

Patrol Scrub = Disabled

Hyper-Threading = Disable

Engine Boost = Aggressive

SR-IOV Support = Disabled

BMC Configuration:

Fan mode = Full speed mode

Sysinfo program /home/spec2017/bin/sysinfo

Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d

running on localhost.localdomain Fri Jul 29 06:48:17 2022

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Silver 4310 CPU @ 2.10GHz

2 "physical id"s (chips)

24 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 12

siblings : 12

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11

physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11

From lscpu from util-linux 2.32.1:

Architecture: x86\_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 24

On-line CPU(s) list: 0-23

Thread(s) per core: 1

Core(s) per socket: 12

Socket(s): 2

NUMA node(s): 2

Vendor ID: GenuineIntel

BIOS Vendor ID: Intel

CPU family: 6

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System  
(2.10 GHz, Intel Xeon Silver 4310)

SPECspeed®2017\_fp\_base = 125

SPECspeed®2017\_fp\_peak = 126

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Jul-2022

Hardware Availability: Apr-2022

Software Availability: Oct-2021

### Platform Notes (Continued)

```

Model: 106
Model name: Intel(R) Xeon(R) Silver 4310 CPU @ 2.10GHz
BIOS Model name: Intel(R) Xeon(R) Silver 4310 CPU @ 2.10GHz
Stepping: 6
CPU MHz: 940.260
CPU max MHz: 3300.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 18432K
NUMA node0 CPU(s): 0-11
NUMA node1 CPU(s): 12-23
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a
avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local split_lock_detect wbnoinvd dtherm ida arat pln pts hwp hwp_act_window
hwp_epp hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid fsrm md_clear pconfig
flush_lld arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 18432 KB

```

```

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
node 0 size: 515632 MB
node 0 free: 509969 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23
node 1 size: 516090 MB
node 1 free: 512646 MB
node distances:
node 0 1
0: 10 20
1: 20 10

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System  
(2.10 GHz, Intel Xeon Silver 4310)

SPECspeed®2017\_fp\_base = 125

SPECspeed®2017\_fp\_peak = 126

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Jul-2022

Hardware Availability: Apr-2022

Software Availability: Oct-2021

### Platform Notes (Continued)

From /proc/meminfo

MemTotal: 1056484120 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

/sbin/tuned-adm active

Current active profile: throughput-performance

/sys/devices/system/cpu/cpu\*/cpufreq/scaling\_governor has  
performance

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux"

VERSION="8.4 (Ootpa)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="8.4"

PLATFORM\_ID="platform:el8"

PRETTY\_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"

ANSI\_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:8.4:ga

uname -a:

Linux localhost.localdomain 4.18.0-305.25.1.el8\_4.x86\_64 #1 SMP Mon Oct 18 14:34:11  
EDT 2021 x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):

Not affected

CVE-2018-3620 (L1 Terminal Fault):

Not affected

Microarchitectural Data Sampling:

Not affected

CVE-2017-5754 (Meltdown):

Not affected

CVE-2018-3639 (Speculative Store Bypass):

Mitigation: Speculative Store  
Bypass disabled via prctl and  
seccomp

CVE-2017-5753 (Spectre variant 1):

Mitigation: usercopy/swaps  
barriers and \_\_user pointer  
sanitization

CVE-2017-5715 (Spectre variant 2):

Mitigation: Enhanced IBRS, IBPB:  
conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling):

Not affected

CVE-2019-11135 (TSX Asynchronous Abort):

Not affected

run-level 3 Jul 28 10:48

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System  
(2.10 GHz, Intel Xeon Silver 4310)

SPECspeed®2017\_fp\_base = 125

SPECspeed®2017\_fp\_peak = 126

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Jul-2022

Hardware Availability: Apr-2022

Software Availability: Oct-2021

### Platform Notes (Continued)

SPEC is set to: /home/spec2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	878G	107G	772G	13%	/home

From /sys/devices/virtual/dmi/id

```
Vendor:      ASUSTeK COMPUTER INC.
Product:    RS720-E10-RS12
Product Family: Server
Serial:     012345678901
```

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200, configured at 2666

BIOS:

```
BIOS Vendor:   American Megatrends Inc.
BIOS Version:  0802
BIOS Date:     04/29/2022
BIOS Revision: 8.2
```

(End of data from sysinfo program)

### Compiler Version Notes

```
=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
  | 644.nab_s(base, peak)
=====
```

```
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.4.0 Build 20210910_000000
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
=====
```

```
=====
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
=====
```

```
Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.4.0 Build 20210910_000000
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.4.0 Build 20210910_000000
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System  
(2.10 GHz, Intel Xeon Silver 4310)

SPECspeed®2017\_fp\_base = 125

SPECspeed®2017\_fp\_peak = 126

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Jul-2022

Hardware Availability: Apr-2022

Software Availability: Oct-2021

### Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.4.0 Build 20210910\_000000  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak)  
| 654.roms\_s(base, peak)  
=====

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.4.0 Build 20210910\_000000  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

=====  
Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak)  
| 628.pop2\_s(base, peak)  
=====

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.4.0 Build 20210910\_000000  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.4.0 Build 20210910\_000000  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

### Base Compiler Invocation

C benchmarks:

icc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

### Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**

ASUS RS720-E10(Z12PP-D32) Server System  
(2.10 GHz, Intel Xeon Silver 4310)

SPECspeed®2017\_fp\_base = 125

SPECspeed®2017\_fp\_peak = 126

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Jul-2022

Hardware Availability: Apr-2022

Software Availability: Oct-2021

## Base Portability Flags (Continued)

```
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-m64 -std=c11 -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries
```

Fortran benchmarks:

```
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-nostandard-realloc-lhs -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:

icc

(Continued on next page)





# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**

ASUS RS720-E10(Z12PP-D32) Server System  
(2.10 GHz, Intel Xeon Silver 4310)

SPECspeed®2017\_fp\_base = 125

SPECspeed®2017\_fp\_peak = 126

**CPU2017 License:** 9016

**Test Sponsor:** ASUSTeK Computer Inc.

**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Jul-2022

**Hardware Availability:** Apr-2022

**Software Availability:** Oct-2021

## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: basepeak = yes

644.nab\_s: basepeak = yes

Fortran benchmarks:

603.bwaves\_s: basepeak = yes

649.fotonik3d\_s: basepeak = yes

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf\_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)  
-prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div  
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4  
-DSPEC\_SUPPRESS\_OPENMP -qopenmp -DSPEC\_OPENMP  
-mbranches-within-32B-boundaries -nostandard-realloc-lhs  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4\_s: basepeak = yes

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**

ASUS RS720-E10(Z12PP-D32) Server System  
(2.10 GHz, Intel Xeon Silver 4310)

SPECspeed®2017\_fp\_base = 125

SPECspeed®2017\_fp\_peak = 126

**CPU2017 License:** 9016

**Test Sponsor:** ASUSTeK Computer Inc.

**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Jul-2022

**Hardware Availability:** Apr-2022

**Software Availability:** Oct-2021

## Peak Optimization Flags (Continued)

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z12-V1.2.html>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z12-V1.2.xml>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.8 on 2022-07-29 06:48:16-0400.

Report generated on 2022-08-31 20:07:50 by CPU2017 PDF formatter v6442.

Originally published on 2022-08-30.