



SPEC CPU®2017 Floating Point Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

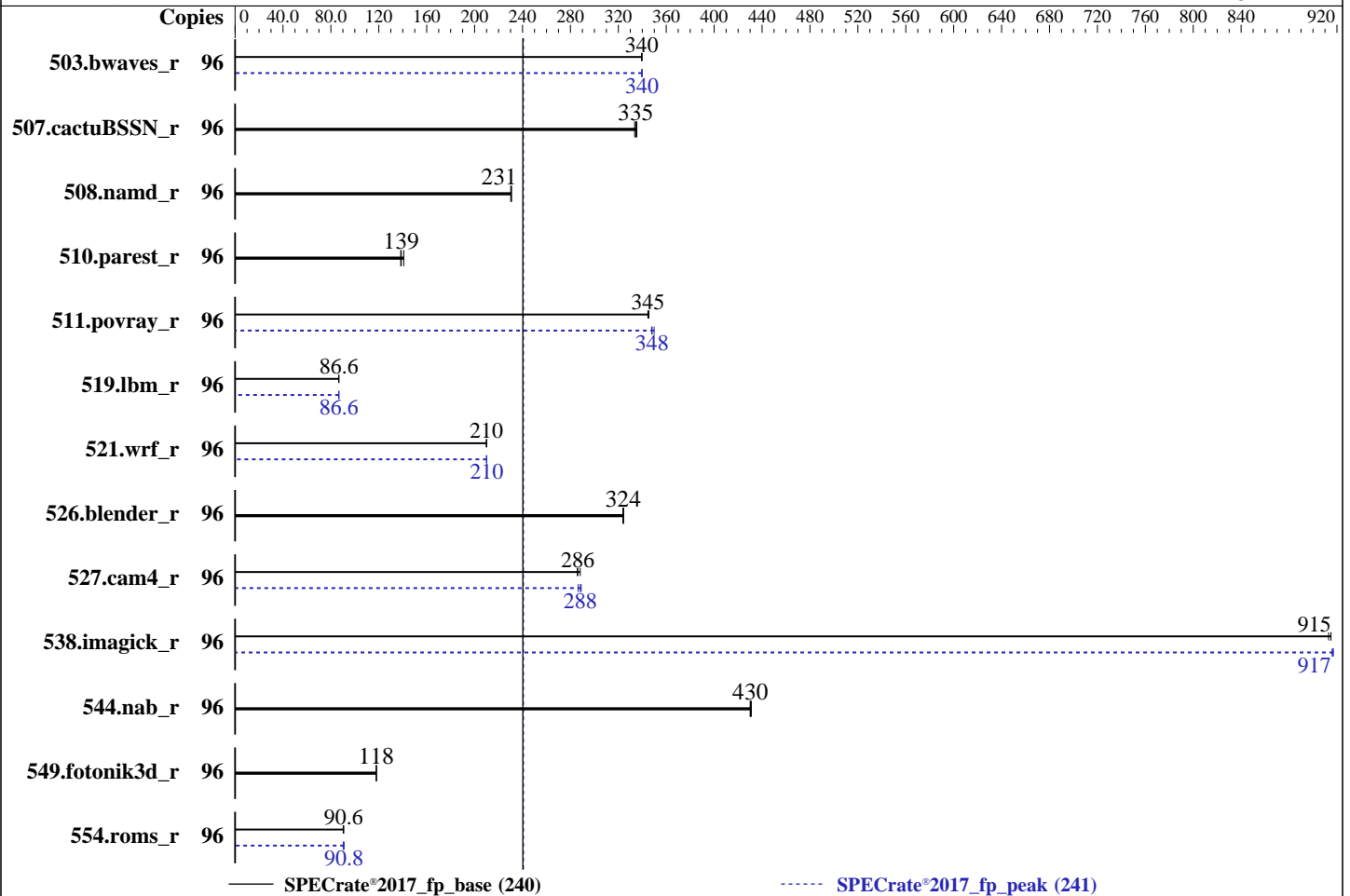
ProLiant DL325 Gen10 Plus
(2.30 GHz, AMD EPYC 7642)

SPECrate®2017_fp_base = 240

SPECrate®2017_fp_peak = 241

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Feb-2020
Hardware Availability: Dec-2019
Software Availability: Aug-2019



Hardware

CPU Name: AMD EPYC 7642
 Max MHz: 3300
 Nominal: 2300
 Enabled: 48 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 256 MB I+D on chip per chip,
 16 MB shared / 3 cores
 Other: None
 Memory: 512 GB (8 x 64 GB 2Rx4 PC4-3200AA-R)
 Storage: 1 x 480 GB SATA SSD, RAID 0
 Other: None

Software

OS: SUSE Linux Enterprise Server 15 (x86_64) SP1
 Kernel 4.12.14-195-default
 Compiler: C/C++/Fortran: Version 2.0.0 of AOCC
 Parallel: No
 Firmware: HPE BIOS Version A43 12/12/2019 released Dec-2019
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc: jemalloc memory allocator library v5.2.0
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	96	2834	340	2837	339	2834	340	96	2834	340	2834	340	2834	340
507.cactuBSSN_r	96	364	334	362	335	363	335	96	364	334	362	335	363	335
508.namd_r	96	395	231	396	230	395	231	96	395	231	396	230	395	231
510.parest_r	96	1816	138	1783	141	1809	139	96	1816	138	1783	141	1809	139
511.povray_r	96	649	346	650	345	650	345	96	645	348	644	348	641	350
519.lbm_r	96	1168	86.6	1168	86.6	1169	86.6	96	1168	86.6	1170	86.5	1167	86.7
521.wrf_r	96	1025	210	1024	210	1024	210	96	1024	210	1024	210	1024	210
526.blender_r	96	451	324	452	324	451	324	96	451	324	452	324	451	324
527.cam4_r	96	583	288	587	286	588	286	96	581	289	586	286	583	288
538.imagick_r	96	261	915	261	915	261	913	96	260	917	260	917	261	916
544.nab_r	96	375	430	375	431	376	430	96	375	430	375	431	376	430
549.fotonik3d_r	96	3171	118	3175	118	3172	118	96	3171	118	3175	118	3172	118
554.roms_r	96	1690	90.2	1684	90.6	1683	90.6	96	1689	90.3	1679	90.9	1680	90.8

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu

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Operating System Notes (Continued)

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/cpu2017/amd_rate_aocc200_rome_C_lib/64;/home/cpu2017/amd_rate_aoc  
    c200_rome_C_lib/32:"  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto
jemalloc 5.2.0 is available here:
<https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2>

Platform Notes

BIOS Configuration

Thermal Configuration set to Maximum Cooling
Performance Determinism set to Power Deterministic
Minimum Processor Idle Power core C-State set to C6 State
Memory Patrol Scrubbing set to Disabled
Workload Profile set to General Throughput Compute
NUMA memory domains per socket set to Four memory domains per socket
C-State Efficiency mode set to Enabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011

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Platform Notes (Continued)

running on linux-q10k Thu Feb 14 09:23:48 2019

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : AMD EPYC 7642 48-Core Processor

1 "physical id"s (chips)

96 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 48

siblings : 96

physical 0: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30
32 33 34 36 37 38 40 41 42 44 45 46 48 49 50 52 53 54 56 57 58 60 61 62

From lscpu:

Architecture: x86_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

Address sizes: 48 bits physical, 48 bits virtual

CPU(s): 96

On-line CPU(s) list: 0-95

Thread(s) per core: 2

Core(s) per socket: 48

Socket(s): 1

NUMA node(s): 4

Vendor ID: AuthenticAMD

CPU family: 23

Model: 49

Model name: AMD EPYC 7642 48-Core Processor

Stepping: 0

CPU MHz: 2295.780

BogoMIPS: 4591.56

Virtualization: AMD-V

L1d cache: 32K

L1i cache: 32K

L2 cache: 512K

L3 cache: 16384K

NUMA node0 CPU(s): 0-11,48-59

NUMA node1 CPU(s): 12-23,60-71

NUMA node2 CPU(s): 24-35,72-83

NUMA node3 CPU(s): 36-47,84-95

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf pni

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Platform Notes (Continued)

```
pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx fl6c
rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpeext perfctr_l2 mwaitx cpb
cat_l3 cdp_l3 hw_pstate ssbd ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2
cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr arat npt
lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmload vgif umip rdpid overflow_recov succor smca
```

```
/proc/cpuinfo cache data
cache size : 512 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 48 49 50 51 52 53 54 55 56 57 58 59
node 0 size: 128709 MB
node 0 free: 128415 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 60 61 62 63 64 65 66 67 68 69 70 71
node 1 size: 128988 MB
node 1 free: 128657 MB
node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 72 73 74 75 76 77 78 79 80 81 82 83
node 2 size: 129018 MB
node 2 free: 128780 MB
node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 84 85 86 87 88 89 90 91 92 93 94 95
node 3 size: 129004 MB
node 3 free: 128774 MB
node distances:
node  0  1  2  3
  0:  10  12  12  12
  1:  12  10  12  12
  2:  12  12  10  12
  3:  12  12  12  10
```

```
From /proc/meminfo
MemTotal:      528098988 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="SLES"
VERSION="15-SP1"
VERSION_ID="15.1"
PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
ID="sles"
ID_LIKE="suse"
```

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Platform Notes (Continued)

```
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp1"
```

```
uname -a:
Linux linux-q10k 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2018-3620 (L1 Terminal Fault):           Not affected
Microarchitectural Data Sampling:           Not affected
CVE-2017-5754 (Meltdown):                   Not affected
CVE-2018-3639 (Speculative Store Bypass):  Mitigation: Speculative Store Bypass disabled
                                              via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):           Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):           Mitigation: Full AMD retpoline, IBPB:
                                              conditional, IBRS_FW, STIBP: conditional, RSB
                                              filling
```

```
run-level 3 Feb 14 09:23
```

```
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       xfs   155G  9.7G  146G   7% /home
```

```
From /sys/devices/virtual/dmi/id
BIOS:      HPE A43 12/12/2019
Vendor:    HPE
Product:   ProLiant DL325 Gen10 Plus
Product Family: ProLiant
Serial:    CN792906TF
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
Memory:
 8x Micron 36ASF8G72PZ-3G2B2 64 GB 2 rank 3200
 8x UNKNOWN NOT AVAILABLE
```

(End of data from sysinfo program)

Compiler Version Notes

```
=====
C          | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
```

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Compiler Version Notes (Continued)

| 544.nab_r(base, peak)

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
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AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
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Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
| 554.roms_r(base, peak)

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
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InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

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Base Compiler Invocation (Continued)

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -D__BOOL_DEFINED -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc
-lflang
```

C++ benchmarks:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
```

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Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm
-ljemalloc -lflang
```

Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both C and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch -z muldefs
-lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
```

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SPEC CPU®2017 Floating Point Rate Result

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ProLiant DL325 Gen10 Plus
(2.30 GHz, AMD EPYC 7642)

SPECrate®2017_fp_base = 240

SPECrate®2017_fp_peak = 241

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Feb-2020
Hardware Availability: Dec-2019
Software Availability: Aug-2019

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
-lmvec -lamdlibm -ljemalloc -lflang
```

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
```

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Peak Optimization Flags (Continued)

519.lbm_r (continued):

```
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc
-lflang
```

538.imagick_r: Same as 519.lbm_r

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: basepeak = yes

Fortran benchmarks:

```
503.bwaves_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3
-march=znver2 -funroll-loops -Mrecursive
-mllvm -vector-library=LIBMVEC -Kieee
-fno-finite-math-only -lmvec -lamdlibm -ljemalloc
-lflang
```

549.fotonik3d_r: basepeak = yes

```
554.roms_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc
-lflang
```

Benchmarks using both Fortran and C:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively
```

(Continued on next page)



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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -O3 -funroll-loops -Mrecursive -Kieee
-fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both C and C++:

```
511.povray_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm
-ljemalloc -lflang
```

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-B1.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-B1.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revH.xml>



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Tested with SPEC CPU®2017 v1.1.0 on 2019-02-14 09:23:47-0500.

Report generated on 2020-04-14 14:06:53 by CPU2017 PDF formatter v6255.

Originally published on 2020-04-14.