SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042	**Test Date:** Jan-2020
Test Sponsor: Netweb Pte Ltd	**Hardware Availability:** Aug-2019
Tested by: Netweb	**Software Availability:** Dec-2019

Copies

Benchmark	Copies	base	peak
503.bwaves_r	128	584	
	32		622
507.cactuBSSN_r	128	542	
	64		532
508.namd_r	128	343	
			329
510.parest_r	128	183	
	64		310
511.povray_r	128	502	
			512
519.lbm_r	128	152	
	32		152
521.wrf_r	128	344	
	64		393
526.blender_r	128	481	
			480
527.cam4_r	128	405	
			405
538.imagick_r	128	1380	
			1380
544.nab_r	128	636	
			637
549.fotonik3d_r	128	208	
	64		211
554.roms_r	128	123	
	32		174

—— SPECrate®2017_fp_base (368)

----- SPECrate®2017_fp_peak (399)

Hardware

CPU Name: AMD EPYC 7542
 Max MHz: 3400
 Nominal: 2900
Enabled: 64 cores, 2 chips, 2 threads/core
Orderable: 1,2 (chip)s
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 128 MB I+D on chip per chip, 16 MB shared / 4 cores
 Other: None
Memory: 512 GB (16 x 32 GB 4Rx4 PC4-3200V-L)
Storage: 1 x 480 GB SSD
Other: None

Software

OS: Ubuntu 19.04
 kernel version
 5.0.0-38-generic
Compiler: C/C++/Fortran: Version 2.0.0 of AOCC
Parallel: No
Firmware: Version 1.0b released Dec-2019
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc: jemalloc memory allocator library v5.2.0
Power Management: Default

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399
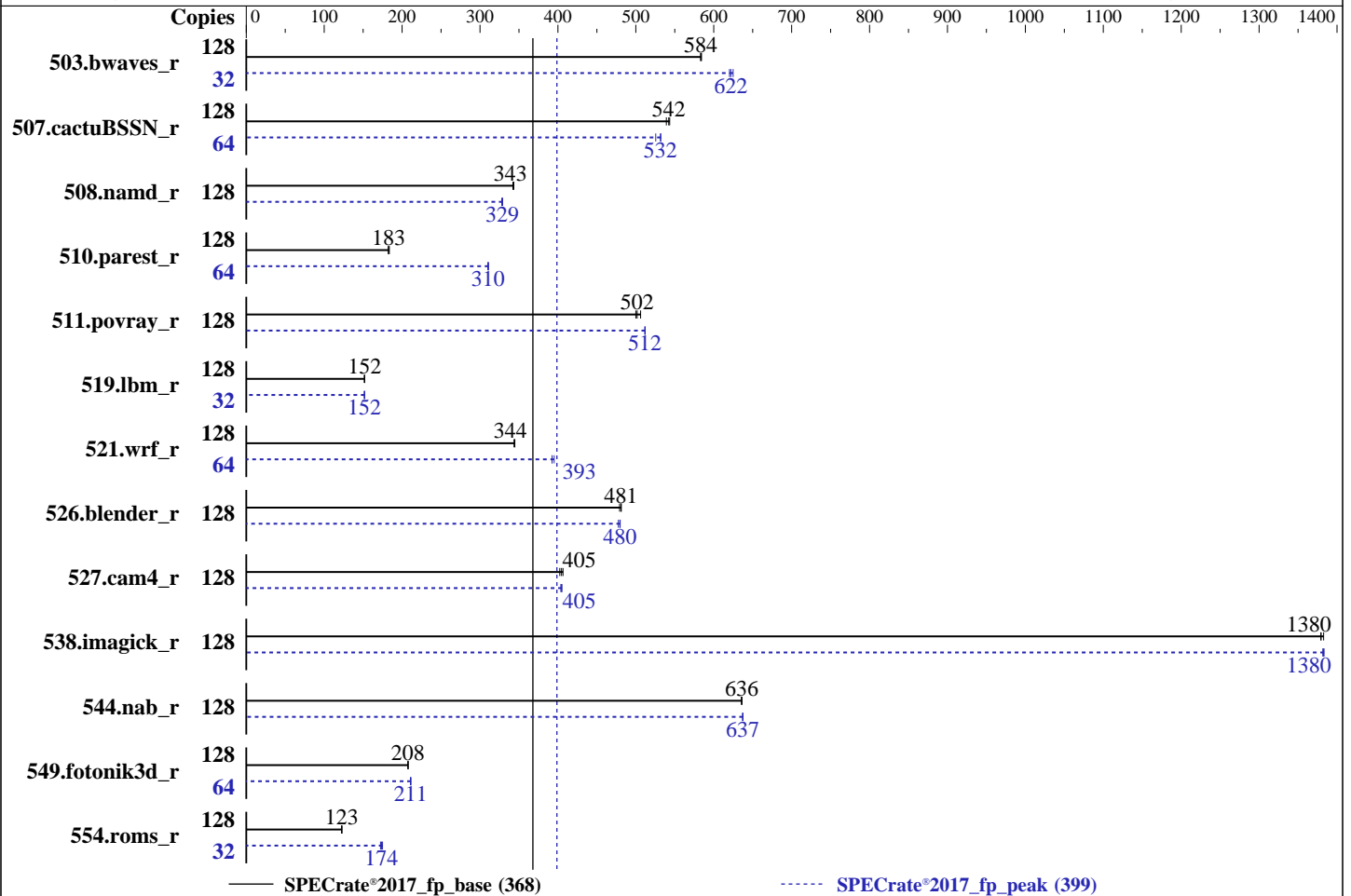
CPU2017 License: 006042	**Test Date:** Jan-2020
Test Sponsor: Netweb Pte Ltd	**Hardware Availability:** Aug-2019
Tested by: Netweb	**Software Availability:** Dec-2019

Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
503.bwaves_r	128	**2200**	**584**	2197	584	2201	583	32	517	620	**516**	**622**	514	625
507.cactuBSSN_r	128	**299**	**542**	298	543	301	539	64	154	526	**152**	**532**	152	532
508.namd_r	128	355	342	**355**	**343**	354	343	128	371	328	**370**	**329**	370	329
510.parest_r	128	1837	182	**1833**	**183**	1824	184	64	539	311	**539**	**310**	540	310
511.povray_r	128	**596**	**502**	591	506	598	500	128	**584**	**512**	584	512	584	512
519.lbm_r	128	**888**	**152**	887	152	891	151	32	**222**	**152**	222	152	223	152
521.wrf_r	128	834	344	**833**	**344**	832	345	64	363	395	**365**	**393**	365	392
526.blender_r	128	407	479	**405**	**481**	405	481	128	406	480	**407**	**480**	408	478
527.cam4_r	128	556	402	551	407	**553**	**405**	128	553	405	554	404	**553**	**405**
538.imagick_r	128	230	1380	231	1380	**231**	**1380**	128	**230**	**1380**	230	1380	230	1380
544.nab_r	128	**339**	**636**	339	636	339	636	128	338	637	**338**	**637**	338	638
549.fotonik3d_r	128	2402	208	2404	207	**2402**	**208**	64	**1182**	**211**	1183	211	1180	211
554.roms_r	128	**1659**	**123**	1662	122	1658	123	32	295	172	291	175	**293**	**174**
SPECrate®2017_fp_base =		**368**												
SPECrate®2017_fp_peak =		**399**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu
```

(Continued on next page)

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042

Test Sponsor: Netweb Pte Ltd

Tested by: Netweb

Test Date: Jan-2020

Hardware Availability: Aug-2019

Software Availability: Dec-2019

Operating System Notes (Continued)

```
dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto
jemalloc 5.2.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2
```

Platform Notes

```
 Sysinfo program /cpu2017/bin/sysinfo
 Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
 running on user Thu Jan 23 21:22:42 2020

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : AMD EPYC 7542 32-Core Processor
       2  "physical id"s (chips)
       128 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 32
```

(Continued on next page)

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

Platform Notes (Continued)

```
      siblings  : 64
     physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
     25 26 27 28 29 30 31
     physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
     25 26 27 28 29 30 31

 From lscpu:
     Architecture:        x86_64
     CPU op-mode(s):      32-bit, 64-bit
     Byte Order:          Little Endian
     Address sizes:       43 bits physical, 48 bits virtual
     CPU(s):              128
     On-line CPU(s) list: 0-127
     Thread(s) per core:  2
     Core(s) per socket:  32
     Socket(s):           2
     NUMA node(s):        8
     Vendor ID:           AuthenticAMD
     CPU family:          23
     Model:               49
     Model name:          AMD EPYC 7542 32-Core Processor
     Stepping:            0
     CPU MHz:             1812.288
     CPU max MHz:         2900.0000
     CPU min MHz:         1500.0000
     BogoMIPS:            5799.71
     Virtualization:      AMD-V
     L1d cache:           32K
     L1i cache:           32K
     L2 cache:            512K
     L3 cache:            16384K
     NUMA node0 CPU(s):   0-7,64-71
     NUMA node1 CPU(s):   8-15,72-79
     NUMA node2 CPU(s):   16-23,80-87
     NUMA node3 CPU(s):   24-31,88-95
     NUMA node4 CPU(s):   32-39,96-103
     NUMA node5 CPU(s):   40-47,104-111
     NUMA node6 CPU(s):   48-55,112-119
     NUMA node7 CPU(s):   56-63,120-127
     Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
     pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
     constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf pni
     pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
     rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
     osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
     cat_l3 cdp_l3 hw_pstate sme ssbd mba sev ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2
     smep bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1
```

(Continued on next page)

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042		**Test Date:**	Jan-2020
Test Sponsor:	Netweb Pte Ltd	**Hardware Availability:**	Aug-2019
Tested by:	Netweb	**Software Availability:**	Dec-2019

Platform Notes (Continued)

```
     xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr
     wbnoinvd arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid
     decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif umip rdpid
     overflow_recov succor smca

 /proc/cpuinfo cache data
    cache size : 512 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 8 nodes (0-7)
   node 0 cpus: 0 1 2 3 4 5 6 7 64 65 66 67 68 69 70 71
   node 0 size: 64371 MB
   node 0 free: 63820 MB
   node 1 cpus: 8 9 10 11 12 13 14 15 72 73 74 75 76 77 78 79
   node 1 size: 64504 MB
   node 1 free: 64023 MB
   node 2 cpus: 16 17 18 19 20 21 22 23 80 81 82 83 84 85 86 87
   node 2 size: 64504 MB
   node 2 free: 63980 MB
   node 3 cpus: 24 25 26 27 28 29 30 31 88 89 90 91 92 93 94 95
   node 3 size: 64492 MB
   node 3 free: 63974 MB
   node 4 cpus: 32 33 34 35 36 37 38 39 96 97 98 99 100 101 102 103
   node 4 size: 64504 MB
   node 4 free: 63966 MB
   node 5 cpus: 40 41 42 43 44 45 46 47 104 105 106 107 108 109 110 111
   node 5 size: 64504 MB
   node 5 free: 63955 MB
   node 6 cpus: 48 49 50 51 52 53 54 55 112 113 114 115 116 117 118 119
   node 6 size: 64504 MB
   node 6 free: 64036 MB
   node 7 cpus: 56 57 58 59 60 61 62 63 120 121 122 123 124 125 126 127
   node 7 size: 64479 MB
   node 7 free: 64009 MB
   node distances:
   node   0    1    2    3    4    5    6    7
     0:  10   12   12   12   32   32   32   32
     1:  12   10   12   12   32   32   32   32
     2:  12   12   10   12   32   32   32   32
     3:  12   12   12   10   32   32   32   32
     4:  32   32   32   32   10   12   12   12
     5:  32   32   32   32   12   10   12   12
     6:  32   32   32   32   12   12   10   12
     7:  32   32   32   32   12   12   12   10

  From /proc/meminfo
```

(Continued on next page)

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Test Date: Jan-2020
Hardware Availability: Aug-2019
Software Availability: Dec-2019

Platform Notes (Continued)

```
     MemTotal:       528249004 kB
     HugePages_Total:        0
     Hugepagesize:        2048 kB

 /usr/bin/lsb_release -d
    Ubuntu 19.04

 From /etc/*release* /etc/*version*
    debian_version: buster/sid
    os-release:
       NAME="Ubuntu"
       VERSION="19.04 (Disco Dingo)"
       ID=ubuntu
       ID_LIKE=debian
       PRETTY_NAME="Ubuntu 19.04"
       VERSION_ID="19.04"
       HOME_URL="https://www.ubuntu.com/"
       SUPPORT_URL="https://help.ubuntu.com/"

 uname -a:
    Linux user 5.0.0-38-generic #41-Ubuntu SMP Tue Dec 3 00:27:35 UTC 2019 x86_64 x86_64
    x86_64 GNU/Linux

 Kernel self-reported vulnerability status:

 itlb_multihit:                         Not affected
 CVE-2018-3620 (L1 Terminal Fault):     Not affected
 Microarchitectural Data Sampling:      Not affected
 CVE-2017-5754 (Meltdown):              Not affected
 CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                        via prctl and seccomp
 CVE-2017-5753 (Spectre variant 1):     Mitigation: usercopy/swapgs barriers and __user
                                        pointer sanitization
 CVE-2017-5715 (Spectre variant 2):     Mitigation: Full AMD retpoline, IBPB:
                                        conditional, IBRS_FW, STIBP: conditional, RSB
                                        filling
 tsx_async_abort:                       Not affected

 run-level 5 Jan 23 12:48

 SPEC is set to: /cpu2017
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda2      ext4  439G   22G  396G   6% /

 From /sys/devices/virtual/dmi/id
     BIOS:    American Megatrends Inc. 1.0b 12/12/2019
     Vendor:  Tyrone Systems
```

(Continued on next page)

Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042

Test Sponsor: Netweb Pte Ltd

Tested by: Netweb

Test Date: Jan-2020

Hardware Availability: Aug-2019

Software Availability: Dec-2019

Platform Notes (Continued)

```
      Product: DA720TB-224R
      Serial:  0123456789

 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
 this section. The 'dmidecode' program reads system data which is "intended to allow
 hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
     16x Samsung M393A4K40DB3-CWE 32 kB 2 rank 3200

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C                   | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
                    | 544.nab_r(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------


==============================================================================
C++                 | 508.namd_r(base, peak) 510.parest_r(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------


==============================================================================
C++, C              | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
```

(Continued on next page)

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042	**Test Date:**	Jan-2020
Test Sponsor: Netweb Pte Ltd	**Hardware Availability:**	Aug-2019
Tested by: Netweb	**Software Availability:**	Dec-2019

Compiler Version Notes (Continued)

```
Thread model: posix
InstalledDir: /opt/aocc-compiler-2.0.0/bin
--------------------------------------------------------------------------


==========================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
--------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/aocc-compiler-2.0.0/bin
--------------------------------------------------------------------------


==========================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
                | 554.roms_r(base, peak)
--------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/aocc-compiler-2.0.0/bin
--------------------------------------------------------------------------


==========================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
--------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
```

(Continued on next page)

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Test Date: Jan-2020
Hardware Availability: Aug-2019
Software Availability: Dec-2019

Compiler Version Notes (Continued)

```
InstalledDir: /opt/aocc-compiler-2.0.0/bin
----------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
```
clang
```

C++ benchmarks:
```
clang++
```

Fortran benchmarks:
```
flang
```

Benchmarks using both Fortran and C:
```
flang clang
```

Benchmarks using both C and C++:
```
clang++ clang
```

Benchmarks using Fortran, C, and C++:
```
clang++ clang flang
```

Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -D__BOOL_DEFINED -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Test Date: Jan-2020
Hardware Availability: Aug-2019
Software Availability: Dec-2019

Base Optimization Flags

C benchmarks:
```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc
-lflang
```

C++ benchmarks:
```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm
-ljemalloc -lflang
```

Fortran benchmarks:
```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:
```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both C and C++:
```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
```

(Continued on next page)

Standard Performance Evaluation Corporation (info@spec.org)

https://www.spec.org/

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042

Test Sponsor: Netweb Pte Ltd

Tested by: Netweb

Test Date: Jan-2020

Hardware Availability: Aug-2019

Software Availability: Dec-2019

Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):
```
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch -z muldefs
-lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:
```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
-lmvec -lamdlibm -ljemalloc -lflang
```

Peak Compiler Invocation

C benchmarks:
```
clang
```

C++ benchmarks:
```
clang++
```

Fortran benchmarks:
```
flang
```

Benchmarks using both Fortran and C:
```
flang clang
```

Benchmarks using both C and C++:
```
clang++ clang
```

Benchmarks using Fortran, C, and C++:
```
clang++ clang flang
```

Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Test Date: Jan-2020
Hardware Availability: Aug-2019
Software Availability: Dec-2019

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc -lflang
```

C++ benchmarks:

```
 508.namd_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -lmvec -lamdlibm -ljemalloc
-lflang
```

```
 510.parest_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -Ofast -march=znver2
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -lmvec -lamdlibm -ljemalloc
-lflang
```

Fortran benchmarks:

(Continued on next page)

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042		**Test Date:**	Jan-2020
Test Sponsor:	Netweb Pte Ltd	**Hardware Availability:**	Aug-2019
Tested by:	Netweb	**Software Availability:**	Dec-2019

Peak Optimization Flags (Continued)

503.bwaves_r: `-flto -Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-region-vectorize`
`-Wl,-mllvm -Wl,-vector-library=LIBMVEC`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3`
`-march=znver2 -funroll-loops -Mrecursive`
`-mllvm -vector-library=LIBMVEC -Kieee`
`-fno-finite-math-only -lmvec -lamdlibm -ljemalloc`
`-lflang`

549.fotonik3d_r: `Same as 503.bwaves_r`

554.roms_r: `-flto -Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-region-vectorize`
`-Wl,-mllvm -Wl,-vector-library=LIBMVEC`
`-Wl,-mllvm -Wl,-reduce-array-computations=3`
`-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver2`
`-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC`
`-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc`
`-lflang`

Benchmarks using both Fortran and C:
`-flto -Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2`
`-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively`
`-mllvm -function-specialize -mllvm -enable-gvn-hoist`
`-mllvm -unroll-threshold=50 -fremap-arrays`
`-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3`
`-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000`
`-flv-function-specialization -O3 -funroll-loops -Mrecursive -Kieee`
`-fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang`

Benchmarks using both C and C++:

511.povray_r: `-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-region-vectorize`
`-Wl,-mllvm -Wl,-vector-library=LIBMVEC`
`-Wl,-mllvm -Wl,-reduce-array-computations=3`
`-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast`
`-march=znver2 -mno-sse4a -fstruct-layout=5`
`-mllvm -vectorize-memory-aggressively`
`-mllvm -function-specialize -mllvm -enable-gvn-hoist`
`-mllvm -unroll-threshold=50 -fremap-arrays`
`-mllvm -vector-library=LIBMVEC`
`-mllvm -reduce-array-computations=3`
`-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000`

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Test Date: Jan-2020
Hardware Availability: Aug-2019
Software Availability: Dec-2019

Peak Optimization Flags (Continued)

511.povray_r (continued):
```
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm
-ljemalloc -lflang
```

526.blender_r: `-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize`
```
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm
-ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:
```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch -mllvm -loop-unswitch-threshold=200000
-O3 -funroll-loops -Mrecursive -Kieee -fno-finite-math-only -lmvec
-lamdlibm -ljemalloc -lflang
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/aocc200-flags-B1.html
http://www.spec.org/cpu2017/flags/Tyrone-platform_amd_rate_aocc200_rome_B1.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/aocc200-flags-B1.xml
http://www.spec.org/cpu2017/flags/Tyrone-platform_amd_rate_aocc200_rome_B1.xml

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DA720TB-224R
(2.90 GHz,AMD EPYC 7542)

SPECrate®2017_fp_base = 368

SPECrate®2017_fp_peak = 399

CPU2017 License: 006042	**Test Date:** Jan-2020
Test Sponsor: Netweb Pte Ltd	**Hardware Availability:** Aug-2019
Tested by: Netweb	**Software Availability:** Dec-2019

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-01-23 16:22:41-0500.
Report generated on 2020-02-18 18:05:11 by CPU2017 PDF formatter v6255.
Originally published on 2020-02-18.