SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 660 Gen10
(2.70 GHz, Intel Xeon Platinum 8280)

$SPECspeed®2017\_fp\_base =$ 216

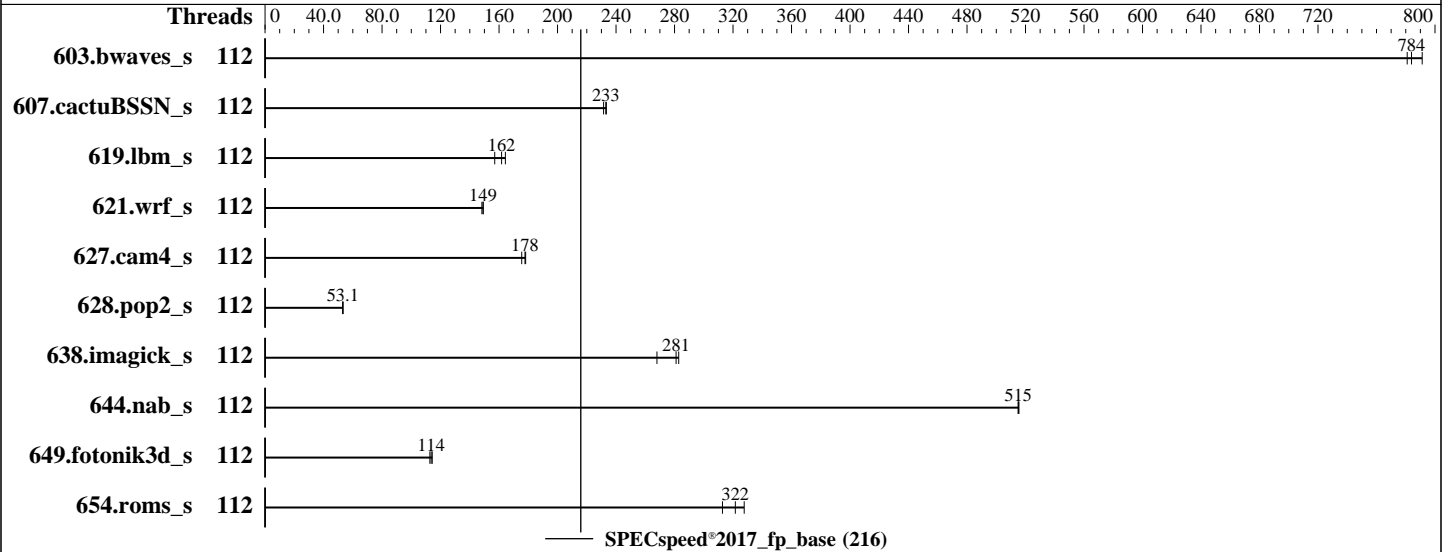$SPECspeed®2017_fp_peak = $ Not Run

CPU2017 License: 3　　　　　　　　　　　　　　　　　　　**Test Date:** Mar-2019
Test Sponsor: HPE　　　　　　　　　　　　　　　　　　　**Hardware Availability:** Apr-2019
Tested by: HPE　　　　　　　　　　　　　　　　　　　　**Software Availability:** Feb-2019

Threads

Benchmark	Threads	Result
603.bwaves_s	112	784
607.cactuBSSN_s	112	233
619.lbm_s	112	162
621.wrf_s	112	149
627.cam4_s	112	178
628.pop2_s	112	53.1
638.imagick_s	112	281
644.nab_s	112	515
649.fotonik3d_s	112	114
654.roms_s	112	322

SPECspeed®2017_fp_base (216)

Hardware

CPU Name: Intel Xeon Platinum 8280
 Max MHz: 4000
 Nominal: 2700
Enabled: 112 cores, 4 chips
Orderable: 2, 4 chip(s)
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 38.5 MB I+D on chip per chip
 Other: None
Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)
Storage: 1 x 480 GB SATA SSD, RAID 0
Other: None

Software

OS: SUSE Linux Enterprise Server 15 (x86_64)
　　　 Kernel 4.12.14-23-default
Compiler: C/C++: Version 19.0.2.187 of Intel C/C++
　　　 Compiler Build 20190117 for Linux;
　　　 Fortran: Version 19.0.2.187 of Intel Fortran
　　　 Compiler Build 20190117 for Linux
Parallel: Yes
Firmware: HPE BIOS Version I43 02/02/2019 released Apr-2019
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None
Power Management: --

SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 660 Gen10
(2.70 GHz, Intel Xeon Platinum 8280)

$SPECspeed®2017\_fp\_base =$ 216

$SPECspeed®2017_fp_peak = $ Not Run

CPU2017 License: 3		**Test Date:**	Mar-2019
Test Sponsor:	HPE	**Hardware Availability:**	Apr-2019
Tested by:	HPE	**Software Availability:**	Feb-2019

Results Table

Benchmark	Base						Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	112	75.5	781	74.6	791	**75.3**	**784**						
607.cactuBSSN_s	112	71.4	233	72.0	231	**71.5**	**233**						
619.lbm_s	112	31.9	164	33.4	157	**32.4**	**162**						
621.wrf_s	112	89.2	148	**88.7**	**149**	88.7	149						
627.cam4_s	112	50.5	176	**49.9**	**178**	49.7	178						
628.pop2_s	112	223	53.3	**223**	**53.1**	224	53.1						
638.imagick_s	112	51.0	283	53.8	268	**51.3**	**281**						
644.nab_s	112	**33.9**	**515**	33.9	516	33.9	515						
649.fotonik3d_s	112	**80.3**	**114**	79.7	114	80.9	113						
654.roms_s	112	50.3	313	**49.0**	**322**	48.1	328						
SPECspeed®2017_fp_base =	**216**												
SPECspeed®2017_fp_peak =	**Not Run**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 >        /proc/sys/vm/drop_caches
```

General Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017_u2/lib/ia32:/home/cpu2017_u2/lib/intel64"
OMP_STACKSIZE = "192M"

 Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.5
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
BIOS Configuration:
 Hyper-Threading set to Disabled
 Thermal Configuration set to Maximum Cooling
```

(Continued on next page)

Hewlett Packard Enterprise
(Test Sponsor: HPE)

Synergy 660 Gen10
(2.70 GHz, Intel Xeon Platinum 8280)

SPECspeed®2017_fp_base = 216

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Platform Notes (Continued)

```
Memory Patrol Scrubbing set to Disabled
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Enhanced Processor Performance set to Enabled
Workload Profile set to General Peak Frequency Compute
 Energy/Performance Bias set to Balanced Power
Workload Profile set to Custom
 Numa Group Size Optimization set to Flat
 Intel UPI Link Power Management set to Enabled
 Advanced Memory Protection set to Advanced ECC
Sysinfo program /home/cpu2017_u2/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on sy660-gen10 Wed Mar 13 23:55:03 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Platinum 8280 CPU @ 2.70GHz
      4  "physical id"s (chips)
      112 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 28
      siblings  : 28
      physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
      28 29 30
      physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
       28 29 30
      physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
       28 29 30
      physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
       28 29 30

From lscpu:
   Architecture:        x86_64
   CPU op-mode(s):      32-bit, 64-bit
   Byte Order:          Little Endian
   CPU(s):              112
   On-line CPU(s) list: 0-111
   Thread(s) per core:  1
   Core(s) per socket:  28
   Socket(s):           4
   NUMA node(s):        4
   Vendor ID:           GenuineIntel
   CPU family:          6
```

(Continued on next page)

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 660 Gen10
(2.70 GHz, Intel Xeon Platinum 8280)

SPECspeed®2017_fp_base = 216

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Platform Notes (Continued)

```
        Model:                  85
        Model name:             Intel(R) Xeon(R) Platinum 8280 CPU @ 2.70GHz
        Stepping:               7
        CPU MHz:                2700.000
        BogoMIPS:               5400.00
        Virtualization:         VT-x
        L1d cache:              32K
        L1i cache:              32K
        L2 cache:               1024K
        L3 cache:               39424K
        NUMA node0 CPU(s):      0-27
        NUMA node1 CPU(s):      28-55
        NUMA node2 CPU(s):      56-83
        NUMA node3 CPU(s):      84-111
        Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
    aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3
    sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
    tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault
    epb cat_l3 cdp_l3 invpcid_single intel_ppin mba tpr_shadow vnmi flexpriority ept
    vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a
    avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
    xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
    ibpb ibrs stibp dtherm ida arat pln pts pku ospke avx512_vnni arch_capabilities ssbd

 /proc/cpuinfo cache data
    cache size : 39424 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
    available: 4 nodes (0-3)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
    node 0 size: 193014 MB
    node 0 free: 192648 MB
    node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
    53 54 55
    node 1 size: 193530 MB
    node 1 free: 193317 MB
    node 2 cpus: 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80
    81 82 83
    node 2 size: 193530 MB
    node 2 free: 193026 MB
    node 3 cpus: 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105
    106 107 108 109 110 111
    node 3 size: 193321 MB
    node 3 free: 193136 MB
```

(Continued on next page)

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 660 Gen10
(2.70 GHz, Intel Xeon Platinum 8280)

$SPECspeed®2017\_fp\_base =$ 216

$SPECspeed®2017_fp_peak = $ Not Run

CPU2017 License: 3	**Test Date:**	Mar-2019
Test Sponsor: HPE	**Hardware Availability:**	Apr-2019
Tested by: HPE	**Software Availability:**	Feb-2019

Platform Notes (Continued)

```
  node distances:
  node   0   1   2   3
    0:  10  21  21  21
    1:  21  10  21  21
    2:  21  21  10  21
    3:  21  21  21  10

From /proc/meminfo
   MemTotal:       791958772 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="SLES"
      VERSION="15"
      VERSION_ID="15"
      PRETTY_NAME="SUSE Linux Enterprise Server 15"
      ID="sles"
      ID_LIKE="suse"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
   Linux sy660-gen10 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown):            Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation,
IBPB, IBRS_FW

run-level 3 Mar 13 23:54

SPEC is set to: /home/cpu2017_u2
   Filesystem      Type   Size  Used Avail Use% Mounted on
   /dev/sdb2       btrfs  445G  106G  339G  24% /home

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS HPE I43 02/02/2019
   Memory:
    24x UNKNOWN NOT AVAILABLE
```

(Continued on next page)

SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 660 Gen10
(2.70 GHz, Intel Xeon Platinum 8280)

SPECspeed®2017_fp_base = 216

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: Feb-2019

Platform Notes (Continued)

```
     24x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2933

  (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C                  | 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
Fortran         | 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
Fortran, C      | 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------
```

SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)

Synergy 660 Gen10
(2.70 GHz, Intel Xeon Platinum 8280)

SPECspeed®2017_fp_base =   216

SPECspeed®2017_fp_peak = Not Run

CPU2017 License:	3	**Test Date:**	Mar-2019
Test Sponsor:	HPE	**Hardware Availability:**	Apr-2019
Tested by:	HPE	**Software Availability:**	Feb-2019

Base Compiler Invocation

C benchmarks:
```
icc -m64 -std=c11
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

Base Portability Flags

603.bwaves_s: `-DSPEC_LP64`
607.cactuBSSN_s: `-DSPEC_LP64`
619.lbm_s: `-DSPEC_LP64`
621.wrf_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
627.cam4_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
628.pop2_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl`
638.imagick_s: `-DSPEC_LP64`
644.nab_s: `-DSPEC_LP64`
649.fotonik3d_s: `-DSPEC_LP64`
654.roms_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
```

Fortran benchmarks:
```
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-nostandard-realloc-lhs
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs
```

(Continued on next page)

Standard Performance Evaluation Corporation (info@spec.org)

https://www.spec.org/

Hewlett Packard Enterprise
(Test Sponsor: HPE)

Synergy 660 Gen10
(2.70 GHz, Intel Xeon Platinum 8280)

SPECspeed®2017_fp_base =   216

SPECspeed®2017_fp_peak = Not Run

CPU2017 License:	3	**Test Date:**	Mar-2019
Test Sponsor:	HPE	**Hardware Availability:**	Apr-2019
Tested by:	HPE	**Software Availability:**	Feb-2019

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs
```

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revA.html
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-03.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revA.xml
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-03.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.0.5 on 2019-03-14 01:55:02-0400.
Report generated on 2023-03-23 14:03:19 by CPU2017 PDF formatter v6442.
Originally published on 2019-04-03.