SPEC® CPU2017 Integer Rate Result

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Huawei

Huawei 2488H V5 (Intel Xeon Gold 6154)

SPECrate2017_int_base = 444

SPECrate2017_int_peak = 467
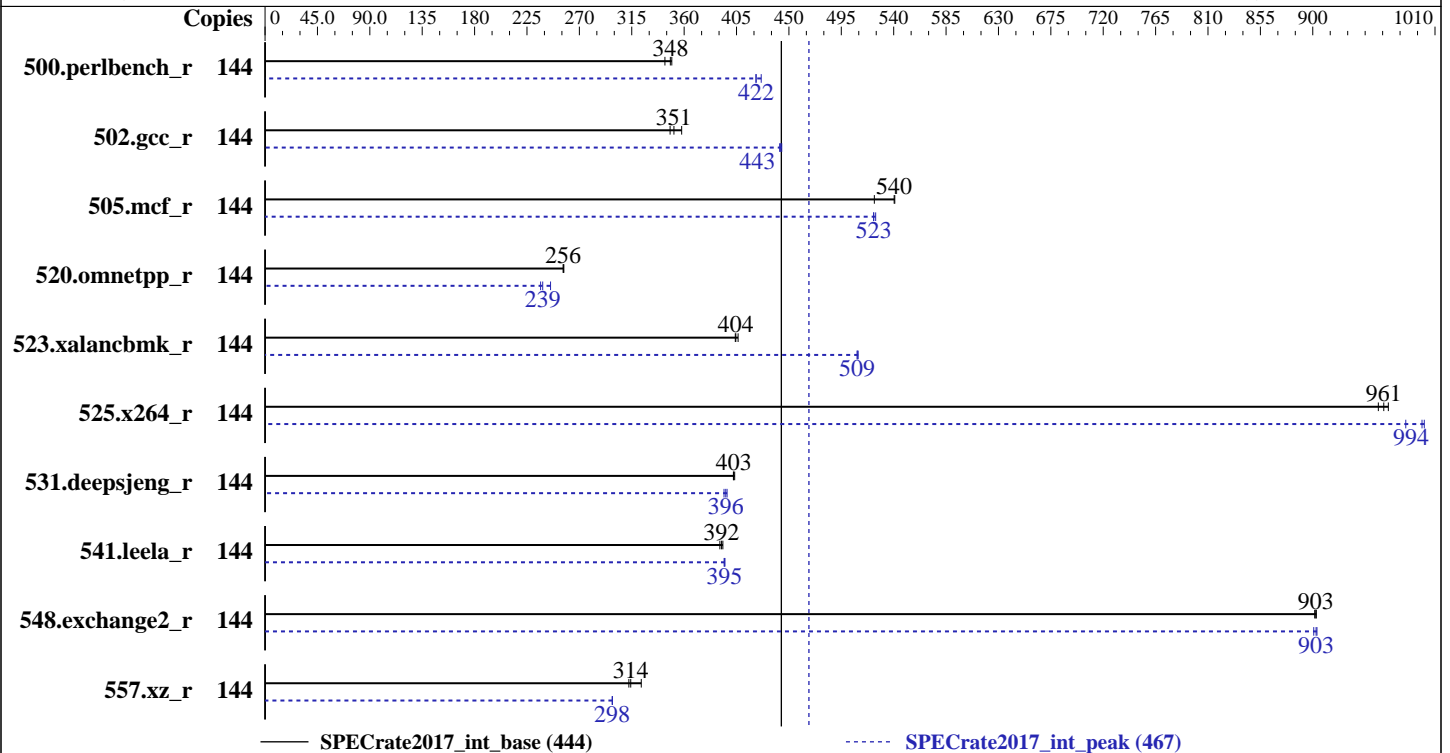
CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: May-2018

Software Availability: Mar-2018

Copies	
500.perlbench_r	144
	348
	422
502.gcc_r	144
	351
	443
505.mcf_r	144
	540
	523
520.omnetpp_r	144
	256
	239
523.xalancbmk_r	144
	404
	509
525.x264_r	144
	961
	994
531.deepsjeng_r	144
	403
	396
541.leela_r	144
	392
	395
548.exchange2_r	144
	903
	903
557.xz_r	144
	314
	298

—— **SPECrate2017_int_base (444)**   - - - - **SPECrate2017_int_peak (467)**

Hardware

CPU Name: Intel Xeon Gold 6154
 Max MHz.:   3700
 Nominal: 3000
Enabled: 72 cores, 4 chips, 2 threads/core
Orderable: 2,4 chips
Cache L1:   32 KB I + 32 KB D on chip per core
 L2:   1 MB I+D on chip per core
 L3:   24.75 MB I+D on chip per chip
 Other: None
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)
Storage: 1 x 900 GB SAS HDD 10K RPM, RAID 0
Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP2
              4.4.120-92.70-default
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
              Compiler for Linux;
              Fortran: Version 18.0.0.128 of Intel Fortran
              Compiler for Linux
Parallel: No
Firmware: Version 0.84 released Mar-2018
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc: jemalloc memory allocator library
              V5.0.1;

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	144	**658**	**348**	656	349	667	343	144	538	426	544	422	**543**	**422**
502.gcc_r	144	586	348	**581**	**351**	570	358	144	461	442	459	444	**460**	**443**
505.mcf_r	144	430	541	**431**	**540**	445	523	144	444	525	445	523	**445**	**523**
520.omnetpp_r	144	736	257	**737**	**256**	738	256	144	770	245	798	237	**792**	**239**
523.xalancbmk_r	144	**376**	**404**	374	406	376	404	144	299	509	298	509	**299**	**509**
525.x264_r	144	264	956	261	965	**262**	**961**	144	257	980	**254**	**994**	253	996
531.deepsjeng_r	144	410	402	409	403	**410**	**403**	144	**417**	**396**	416	397	419	394
541.leela_r	144	**608**	**392**	606	393	610	391	144	**604**	**395**	604	395	605	394
548.exchange2_r	144	**418**	**903**	418	903	418	902	144	419	901	**418**	**903**	418	904
557.xz_r	144	481	323	**495**	**314**	498	313	144	**521**	**298**	521	298	521	298
SPECrate2017_int_base =		**444**												
SPECrate2017_int_peak =		**467**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>   /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

jemalloc: configured and built at default for
32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4,

(Continued on next page)

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General Notes (Continued)

and the system compiler gcc 4.8.5;
jemalloc: sources available from jemalloc.net or
https://github.com/jemalloc/jemalloc/releases;
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Platform Notes

```
 BIOS configuration:
 Sub NUMA Cluster (SNC) set to enabled
 IMC (Integrated memory controller) Interleaving set to 1 way interleave
 Xtended Prediction Table (XPT) Prefetch set to Enable
 Memory Patrol Scrub set to Disable
 Last Level Cache (LLC) Prefetch set to Disable
 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
 running on linux-oyf8 Tue Jun 19 22:04:20 2018

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6154 CPU @ 3.00GHz
       4  "physical id"s (chips)
       144 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 18
       siblings  : 36
       physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
       physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
       physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
       physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

 From lscpu:
    Architecture:          x86_64
    CPU op-mode(s):        32-bit, 64-bit
    Byte Order:            Little Endian
    CPU(s):                144
    On-line CPU(s) list:   0-143
    Thread(s) per core:    2
```

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Platform Notes (Continued)

```
        Core(s) per socket:     18
        Socket(s):              4
        NUMA node(s):           8
        Vendor ID:              GenuineIntel
        CPU family:             6
        Model:                  85
        Model name:             Intel(R) Xeon(R) Gold 6154 CPU @ 3.00GHz
        Stepping:               4
        CPU MHz:                3001.000
        CPU max MHz:            3001.0000
        CPU min MHz:            1200.0000
        BogoMIPS:               5999.86
        Virtualization:         VT-x
        L1d cache:              32K
        L1i cache:              32K
        L2 cache:               1024K
        L3 cache:               25344K
        NUMA node0 CPU(s):      0-2,5,6,9,10,14,15,72-74,77,78,81,82,86,87
        NUMA node1 CPU(s):      3,4,7,8,11-13,16,17,75,76,79,80,83-85,88,89
        NUMA node2 CPU(s):      18-20,23,24,27,28,32,33,90-92,95,96,99,100,104,105
        NUMA node3 CPU(s):      21,22,25,26,29-31,34,35,93,94,97,98,101-103,106,107
        NUMA node4 CPU(s):      36-38,41,42,45,46,50,51,108-110,113,114,117,118,122,123
        NUMA node5 CPU(s):      39,40,43,44,47-49,52,53,111,112,115,116,119-121,124,125
        NUMA node6 CPU(s):      54-56,59,60,63,64,68,69,126-128,131,132,135,136,140,141
        NUMA node7 CPU(s):      57,58,61,62,65-67,70,71,129,130,133,134,137-139,142,143
        Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
    aperfmperf eagerfpu pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
    xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
    avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm
    intel_pt rsb_ctxsw spec_ctrl stibp retpoline kaiser tpr_shadow vnmi flexpriority ept
    vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx avx512f
    avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec
    xgetbv1 cqm_llc cqm_occup_llc

 /proc/cpuinfo cache data
    cache size : 25344 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
    available: 8 nodes (0-7)
    node 0 cpus: 0 1 2 5 6 9 10 14 15 72 73 74 77 78 81 82 86 87
    node 0 size: 191756 MB
    node 0 free: 190360 MB
    node 1 cpus: 3 4 7 8 11 12 13 16 17 75 76 79 80 83 84 85 88 89
    node 1 size: 193524 MB
```

(Continued on next page)

Huawei

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Test Sponsor:　Huawei　　　　　　　　　　　　　　　　　　**Hardware Availability:** May-2018
Tested by:　Huawei　　　　　　　　　　　　　　　　　　　**Software Availability:**　Mar-2018

Platform Notes (Continued)

```
   node 1 free: 193005 MB
   node 2 cpus: 18 19 20 23 24 27 28 32 33 90 91 92 95 96 99 100 104 105
   node 2 size: 193524 MB
   node 2 free: 192906 MB
   node 3 cpus: 21 22 25 26 29 30 31 34 35 93 94 97 98 101 102 103 106 107
   node 3 size: 193524 MB
   node 3 free: 192909 MB
   node 4 cpus: 36 37 38 41 42 45 46 50 51 108 109 110 113 114 117 118 122 123
   node 4 size: 193524 MB
   node 4 free: 193048 MB
   node 5 cpus: 39 40 43 44 47 48 49 52 53 111 112 115 116 119 120 121 124 125
   node 5 size: 193524 MB
   node 5 free: 192934 MB
   node 6 cpus: 54 55 56 59 60 63 64 68 69 126 127 128 131 132 135 136 140 141
   node 6 size: 193524 MB
   node 6 free: 193132 MB
   node 7 cpus: 57 58 61 62 65 66 67 70 71 129 130 133 134 137 138 139 142 143
   node 7 size: 193367 MB
   node 7 free: 192959 MB
   node distances:
   node   0   1   2   3   4   5   6   7
     0:  10  20  20  20  20  20  20  20
     1:  20  10  20  20  20  20  20  20
     2:  20  20  10  20  20  20  20  20
     3:  20  20  20  10  20  20  20  20
     4:  20  20  20  20  10  20  20  20
     5:  20  20  20  20  20  10  20  20
     6:  20  20  20  20  20  20  10  20
     7:  20  20  20  20  20  20  20  10

 From /proc/meminfo
    MemTotal:       1583380320 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 /usr/bin/lsb_release -d
    SUSE Linux Enterprise Server 12 SP2

 From /etc/*release* /etc/*version*
    SuSE-release:
       SUSE Linux Enterprise Server 12 (x86_64)
       VERSION = 12
       PATCHLEVEL = 2
       # This file is deprecated and will be removed in a future service pack or release.
       # Please check /etc/os-release for details about this release.
    os-release:
       NAME="SLES"
```

(Continued on next page)

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Platform Notes (Continued)

```
        VERSION="12-SP2"
        VERSION_ID="12.2"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp2"

 uname -a:
    Linux linux-oyf8 4.4.120-92.70-default #1 SMP Wed Mar 14 15:59:43 UTC 2018 (52a83de)
    x86_64 x86_64 x86_64 GNU/Linux

 run-level 5 Jun 19 11:46

 SPEC is set to: /home/cpu2017
    Filesystem      Type    Size   Used Avail Use% Mounted on
    /dev/sda4       btrfs   736G   23G   712G   4% /home

 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
 this section. The 'dmidecode' program reads system data which is "intended to allow
 hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS INSYDE Corp. 0.84 03/26/2018
    Memory:
     48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
 CC   500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
      525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
 CC   500.perlbench_r(peak) 502.gcc_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
 CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
```

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Compiler Version Notes (Continued)

```
    541.leela_r(base)
-------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
-------------------------------------------------------------------------


==========================================================================
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
     541.leela_r(peak)
-------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
-------------------------------------------------------------------------


==========================================================================
 FC  548.exchange2_r(base, peak)
-------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
-------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Base Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`

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Base Portability Flags (Continued)

557.xz_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:
```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:
```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

Base Other Flags

C benchmarks:
```
-m64 -std=c11
```

C++ benchmarks:
```
-m64
```

Fortran benchmarks:
```
-m64
```

Peak Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Fortran benchmarks:
```
ifort
```

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Peak Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-D_FILE_OFFSET_BITS=64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Peak Optimization Flags

C benchmarks:

500.perlbench_r: `-Wl,-z,muldefs` -prof-gen(pass 1) -prof-use(pass 2) `-ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`
`-fno-strict-overflow -L/usr/local/je5.0.1-64/lib`
`-ljemalloc`

502.gcc_r: `-L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32`
`-Wl,-z,muldefs` -prof-gen(pass 1) -prof-use(pass 2) `-ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`
`-L/usr/local/je5.0.1-32/lib -ljemalloc`

505.mcf_r: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib`
`-ljemalloc`

525.x264_r: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -fno-alias`
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

557.xz_r: `Same as 505.mcf_r`

C++ benchmarks:

520.omnetpp_r: `-Wl,-z,muldefs` -prof-gen(pass 1) -prof-use(pass 2) `-ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

523.xalancbmk_r: `-L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32`
`-Wl,-z,muldefs` -prof-gen(pass 1) -prof-use(pass 2) `-ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`

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Peak Optimization Flags (Continued)

523.xalancbmk_r (continued):
```
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

531.deepsjeng_r: `Same as 520.omnetpp_r`

541.leela_r: `Same as 520.omnetpp_r`

Fortran benchmarks:
```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

Peak Other Flags

C benchmarks (except as noted below):
```
-m64 -std=c11
```

502.gcc_r: `-m32 -std=c11`

C++ benchmarks (except as noted below):
```
-m64
```

523.xalancbmk_r: `-m32`

Fortran benchmarks:
```
-m64
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-06-19 10:04:19-0400.
Report generated on 2018-10-31 18:27:47 by CPU2017 PDF formatter v6067.
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