



# SPEC® CFP2006 Result

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## Lenovo Group Limited

Lenovo Flex System x240 M5  
(2.40 GHz, Intel Xeon E5-2699A v4)

**SPECfp®\_rate2006 = 1140**

**SPECfp\_rate\_base2006 = 1100**

CPU2006 license: 9017

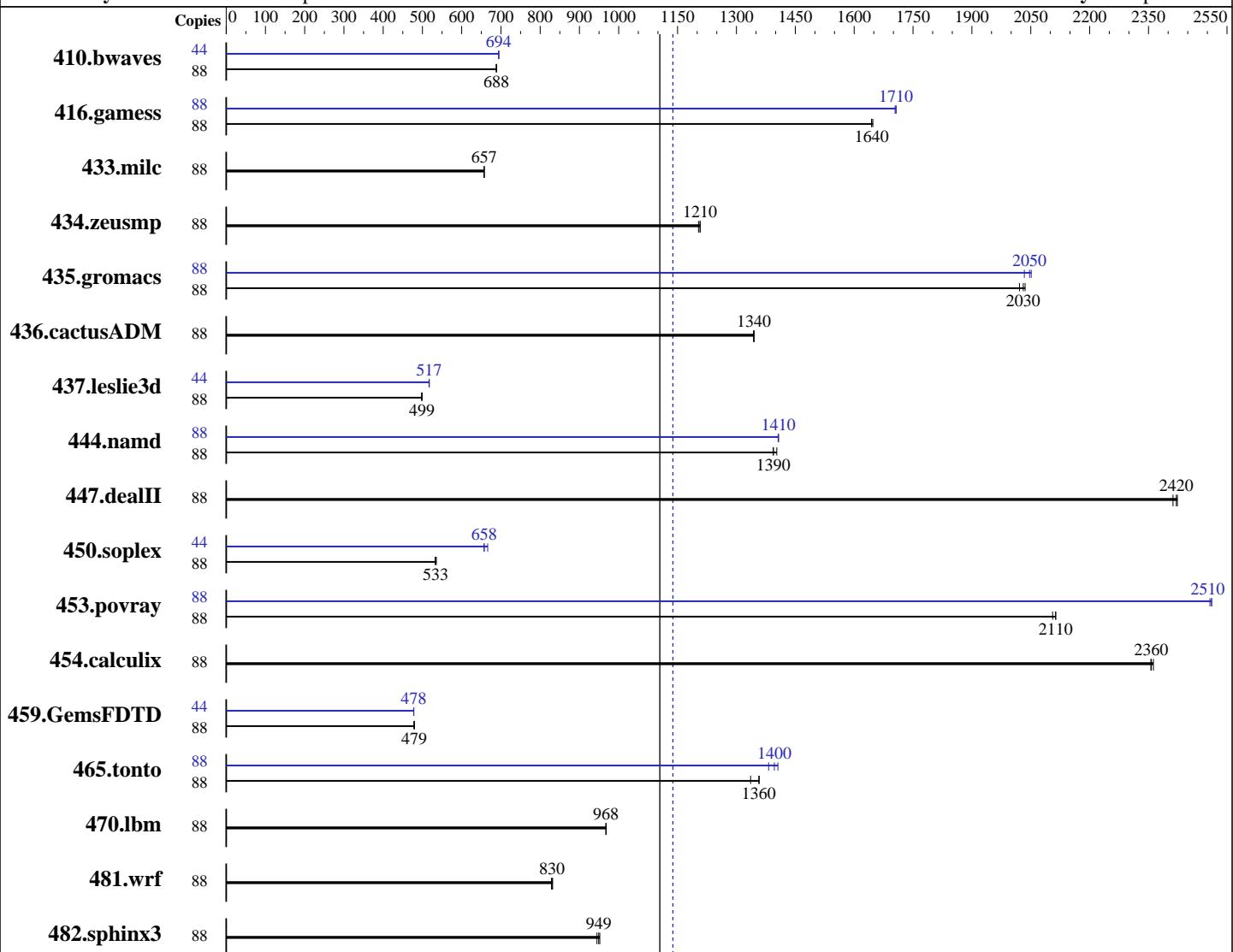
Test date: Nov-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Sep-2016



**SPECfp\_rate\_base2006 = 1100:**

**SPECfp\_rate2006 = 1140**

### Hardware

CPU Name: Intel Xeon E5-2699A v4  
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
CPU MHz: 2400  
FPU: Integrated  
CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
Compiler: Kernel 3.12.49-11-default  
C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;  
Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux  
Auto Parallel: No  
File System: btrfs  
System State: Run level 3 (multi-user)

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L3 Cache: 55 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 800 GB SATA SSD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	88	1738	688	<b>1737</b>	<b>688</b>	1736	689	44	861	695	861	694	<b>861</b>	<b>694</b>
416.gamess	88	<b>1048</b>	<b>1640</b>	1046	1650	1048	1640	88	1010	1710	<b>1010</b>	<b>1710</b>	1011	1700
433.milc	88	<b>1229</b>	<b>657</b>	1229	657	1229	657	88	<b>1229</b>	<b>657</b>	1229	657	1229	657
434.zeusmp	88	665	1200	663	1210	<b>663</b>	<b>1210</b>	88	665	1200	663	1210	<b>663</b>	<b>1210</b>
435.gromacs	88	309	2040	311	2020	<b>309</b>	<b>2030</b>	88	309	2030	<b>307</b>	<b>2050</b>	306	2050
436.cactusADM	88	783	1340	<b>782</b>	<b>1340</b>	782	1340	88	783	1340	<b>782</b>	<b>1340</b>	782	1340
437.leslie3d	88	1662	498	1658	499	<b>1659</b>	<b>499</b>	44	<b>800</b>	<b>517</b>	800	517	799	518
444.namd	88	503	1400	<b>506</b>	<b>1390</b>	506	1390	88	<b>502</b>	<b>1410</b>	502	1410	501	1410
447.dealII	88	417	2410	415	2420	<b>416</b>	<b>2420</b>	88	417	2410	415	2420	<b>416</b>	<b>2420</b>
450.soplex	88	1379	532	<b>1376</b>	<b>533</b>	1371	535	44	<b>551</b>	666	<b>558</b>	<b>658</b>	559	657
453.povray	88	<b>222</b>	<b>2110</b>	221	2110	222	2110	88	187	2510	<b>186</b>	<b>2510</b>	186	2510
454.calculix	88	308	2360	307	2360	<b>308</b>	<b>2360</b>	88	308	2360	307	2360	<b>308</b>	<b>2360</b>
459.GemsFDTD	88	<b>1948</b>	<b>479</b>	1953	478	1947	480	44	<b>977</b>	<b>478</b>	977	478	977	478
465.tonto	88	638	1360	<b>638</b>	<b>1360</b>	648	1340	88	627	1380	<b>620</b>	<b>1400</b>	616	1410
470.lbm	88	1250	968	<b>1249</b>	<b>968</b>	1249	968	88	1250	968	<b>1249</b>	<b>968</b>	1249	968
481.wrf	88	1182	832	1186	829	<b>1184</b>	<b>830</b>	88	1182	832	1186	829	<b>1184</b>	<b>830</b>
482.sphinx3	88	1816	945	1802	952	<b>1807</b>	<b>949</b>	88	1816	945	1802	952	<b>1807</b>	<b>949</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```



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## Platform Notes

BIOS configuration:

Operating Mode set to Maximum Performance

COD Preference set to Enable

Sysinfo program /home/cpu2006-1.2-ic17.0/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on x240 Sat Nov 19 04:05:33 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2699A v4 @ 2.40GHz
        2 "physical id"s (chips)
        88 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 22
        siblings : 44
        physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
        28
        physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
        28
cache size : 28160 KB
```

```
From /proc/meminfo
MemTotal:      263821376 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux x240 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015 (8d714a0)
x86_64 x86_64 x86_64 GNU/Linux
```

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## Platform Notes (Continued)

run-level 3 Nov 18 16:26

SPEC is set to: /home/cpu2006-1.2-ic17.0  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda2 btrfs 746G 42G 703G 6% /home  
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO -[C4E126N-2.20]- 09/07/2016

Memory:

16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz  
8x NO DIMM Unknown

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic17.0/libs/32:/home/cpu2006-1.2-ic17.0/libs/64:/home/cpu2006-1.2-ic17.0/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Fortran benchmarks:

```
ifort -m64
```

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## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
  450.soplex: -D_FILE_OFFSET_BITS=64
  453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
  -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
  -no-prec-div(pass 2) -fno-alias -auto-ilp32
  -qopt-mem-layout-trans=3

```

447.dealII: basepeak = yes

```

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
  -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
  -no-prec-div(pass 2) -qopt-malloc-options=3
  -qopt-mem-layout-trans=3

```

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## Peak Optimization Flags (Continued)

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll14 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll14 -auto -inline-calloc  
-qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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