



# SPEC<sup>®</sup> CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 480 Gen9  
(2.60 GHz, Intel Xeon E5-2697A v4)

SPECfp<sup>®</sup>\_rate2006 = 1030

SPECfp\_rate\_base2006 = 1000

CPU2006 license: 3

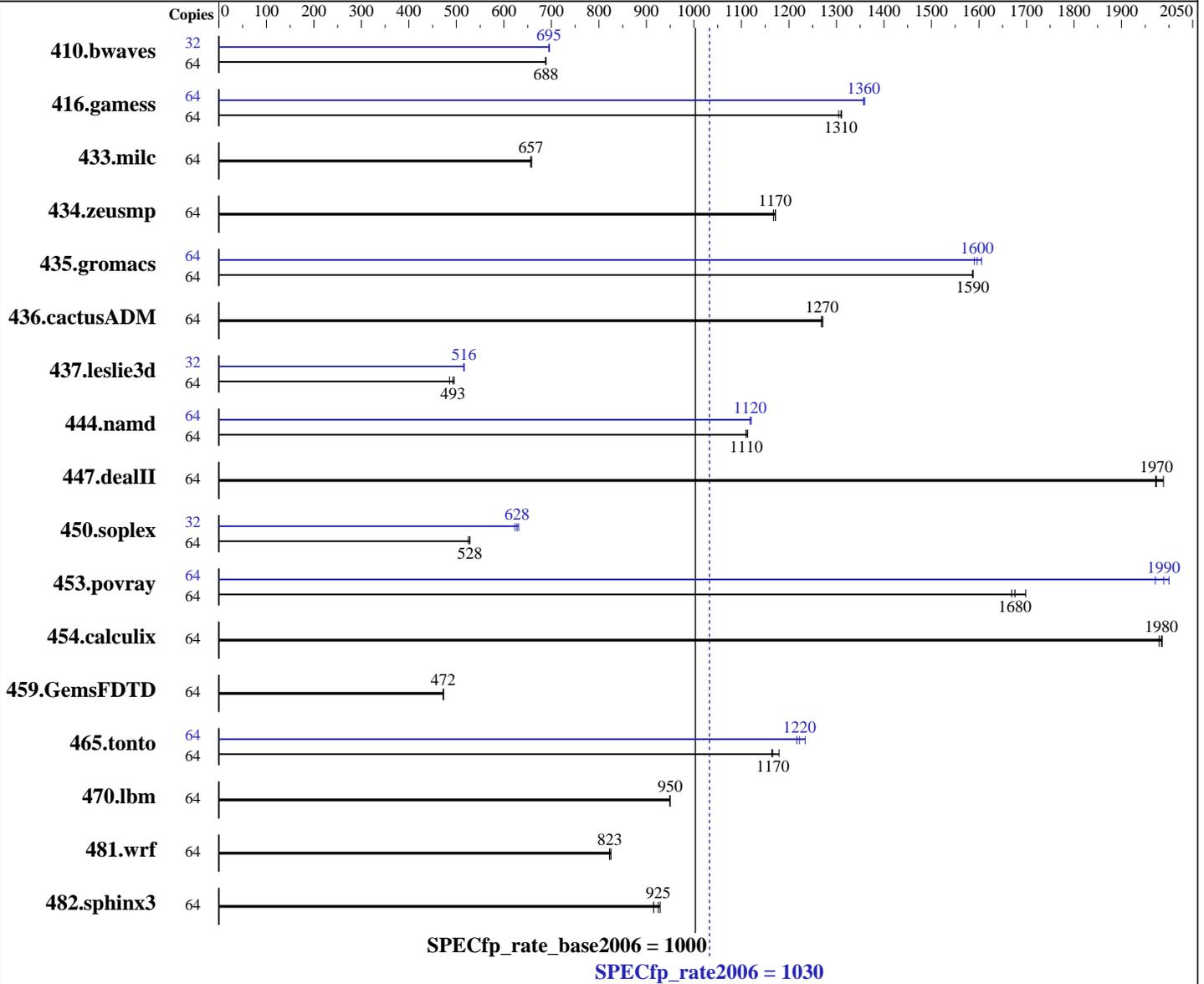
Test sponsor: HPE

Tested by: HPE

Test date: Nov-2016

Hardware Availability: Dec-2016

Software Availability: Sep-2016



## Hardware

CPU Name: Intel Xeon E5-2697A v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

## Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP1  
 Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C++ Studio XE for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 40 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 600 GB 10 K SAS, RAID 0  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	<b><u>1263</u></b>	<b><u>688</u></b>	1263	688	1264	688	32	626	695	<b><u>626</u></b>	<b><u>695</u></b>	625	696
416.gamess	64	<b><u>957</u></b>	<b><u>1310</u></b>	960	1310	956	1310	64	922	1360	<b><u>923</u></b>	<b><u>1360</u></b>	923	1360
433.milc	64	892	658	895	657	<b><u>894</u></b>	<b><u>657</u></b>	64	892	658	895	657	<b><u>894</u></b>	<b><u>657</u></b>
434.zeusmp	64	497	1170	499	1170	<b><u>497</u></b>	<b><u>1170</u></b>	64	497	1170	499	1170	<b><u>497</u></b>	<b><u>1170</u></b>
435.gromacs	64	<b><u>288</u></b>	<b><u>1590</u></b>	288	1590	288	1590	64	285	1610	287	1590	<b><u>286</u></b>	<b><u>1600</u></b>
436.cactusADM	64	602	1270	<b><u>602</u></b>	<b><u>1270</u></b>	603	1270	64	602	1270	<b><u>602</u></b>	<b><u>1270</u></b>	603	1270
437.leslie3d	64	<b><u>1221</u></b>	<b><u>493</u></b>	1214	495	1239	486	32	584	515	<b><u>583</u></b>	<b><u>516</u></b>	583	516
444.namd	64	461	1110	463	1110	<b><u>462</u></b>	<b><u>1110</u></b>	64	459	1120	458	1120	<b><u>459</u></b>	<b><u>1120</u></b>
447.dealII	64	368	1990	371	1970	<b><u>371</u></b>	<b><u>1970</u></b>	64	368	1990	371	1970	<b><u>371</u></b>	<b><u>1970</u></b>
450.soplex	64	1017	525	<b><u>1011</u></b>	<b><u>528</u></b>	1010	528	32	423	631	<b><u>425</u></b>	<b><u>628</u></b>	428	623
453.povray	64	200	1700	204	1670	<b><u>203</u></b>	<b><u>1680</u></b>	64	170	2000	173	1970	<b><u>171</u></b>	<b><u>1990</u></b>
454.calculix	64	<b><u>266</u></b>	<b><u>1980</u></b>	267	1980	266	1990	64	<b><u>266</u></b>	<b><u>1980</u></b>	267	1980	266	1990
459.GemsFDTD	64	<b><u>1438</u></b>	<b><u>472</u></b>	1436	473	1438	472	64	<b><u>1438</u></b>	<b><u>472</u></b>	1436	473	1438	472
465.tonto	64	534	1180	541	1160	<b><u>540</u></b>	<b><u>1170</u></b>	64	<b><u>515</u></b>	<b><u>1220</u></b>	510	1230	518	1220
470.lbm	64	925	950	926	949	<b><u>926</u></b>	<b><u>950</u></b>	64	925	950	926	949	<b><u>926</u></b>	<b><u>950</u></b>
481.wrf	64	<b><u>869</u></b>	<b><u>823</u></b>	870	822	866	825	64	<b><u>869</u></b>	<b><u>823</u></b>	870	822	866	825
482.sphinx3	64	1363	915	<b><u>1349</u></b>	<b><u>925</u></b>	1343	929	64	1363	915	<b><u>1349</u></b>	<b><u>925</u></b>	1343	929

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>



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**Test date:** Nov-2016

**Hardware Availability:** Dec-2016

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## Platform Notes

### BIOS Configuration:

```

Power Profile set to Custom
Power Regulator to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C6 State
Minimum Processor Idle Power Package C-State set to No Package State
Energy/Performance Bias set to Maximum Performance
Collaborative Power Control set to Disabled
QPI Snoop Configuration set to Cluster on Die
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

```

```

Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-8p7b Fri Nov 11 19:03:06 2016

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

### From /proc/cpuinfo

```

model name : Intel(R) Xeon(R) CPU E5-2697A v4 @ 2.60GHz
 2 "physical id"s (chips)
 64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 16
  siblings  : 32
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 20480 KB

```

### From /proc/meminfo

```

MemTotal:      264543332 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

### /usr/bin/lsb\_release -d

```
SUSE Linux Enterprise Server 12 SP1
```

### From /etc/\*release\* /etc/\*version\*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"

```

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## Platform Notes (Continued)

```
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux linux-8p7b 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 11 08:37
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   517G  59G  458G  12% /home
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS HP I37 09/14/2016
Memory:
8x UNKNOWN NOT AVAILABLE
16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz
```

(End of data from sysinfo program)  
Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:  
16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

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## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3



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## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32  
-qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-malloc-options=3  
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

### Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: basepeak = yes

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revD.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revD.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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