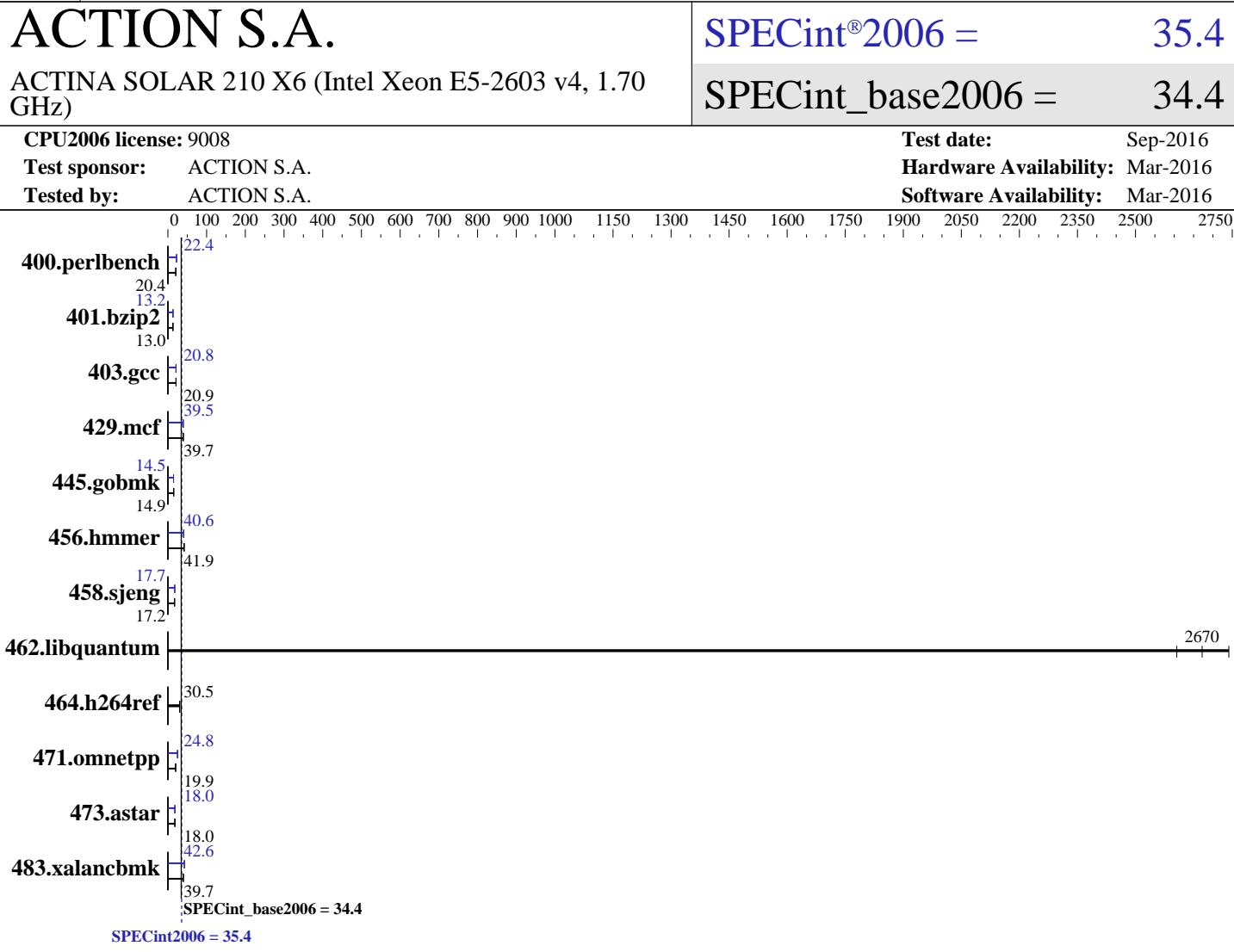




SPEC® CINT2006 Result

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Hardware		Software
CPU Name:	Intel Xeon E5-2603 v4	Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
CPU Characteristics:		3.10.0-327.18.2.el7.x86_64
CPU MHz:	1700	Compiler: C/C++: Version 16.0.3.210 of Intel C++ Studio XE for Linux
FPU:	Integrated	Auto Parallel: Yes
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip	File System: ext4
CPU(s) orderable:	1,2 chips	System State: Run level 3 (multi-user)
Primary Cache:	32 KB I + 32 KB D on chip per core	Base Pointers: 32/64-bit
Secondary Cache:	256 KB I+D on chip per core	Peak Pointers: 32/64-bit
L3 Cache:	15 MB I+D on chip per chip	Other Software: Microquill SmartHeap V10.0
Other Cache:	None	
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)	
Disk Subsystem:	1 x 240 GB SATA II SSD	
Other Hardware:	None	



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CPU2006 license: 9008

Test date: Sep-2016

Test sponsor: ACTION S.A.

Hardware Availability: Mar-2016

Tested by: ACTION S.A.

Software Availability: Mar-2016

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	479	20.4	480	20.4	479	20.4	437	22.3	436	22.4	437	22.4
401.bzip2	744	13.0	744	13.0	744	13.0	732	13.2	731	13.2	732	13.2
403.gcc	385	20.9	385	20.9	383	21.0	387	20.8	384	21.0	386	20.8
429.mcf	230	39.6	230	39.7	230	39.7	230	39.7	231	39.4	231	39.5
445.gobmk	702	14.9	703	14.9	701	15.0	721	14.5	721	14.5	721	14.6
456.hmmer	223	41.9	223	41.9	223	41.9	230	40.6	230	40.5	230	40.6
458.sjeng	705	17.2	705	17.2	706	17.1	685	17.7	685	17.7	685	17.7
462.libquantum	7.75	2670	7.95	2610	7.56	2740	7.75	2670	7.95	2610	7.56	2740
464.h264ref	726	30.5	727	30.5	730	30.3	726	30.5	727	30.5	730	30.3
471.omnetpp	314	19.9	314	19.9	315	19.9	252	24.8	253	24.7	252	24.8
473.astar	391	18.0	389	18.0	389	18.0	391	17.9	391	18.0	389	18.0
483.xalancbmk	174	39.7	173	39.8	174	39.7	162	42.6	162	42.6	162	42.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Bios Settings:

Power & Performance = Performance
Enforce POR = Disabled
Memory Operating Speed Selection = Auto
Set FAN Profile = Performance
Fan PWM Offset = 0

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date::: 2012-07-17 #\\$ e86d102572650a6e4d596a3cee98f191
running on SUT Thu Sep 22 03:03:00 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

Continued on next page



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Platform Notes (Continued)

```

model name : Intel(R) Xeon(R) CPU E5-2603 v4 @ 1.70GHz
  2 "physical id"s (chips)
    12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 6
  siblings   : 6
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB

From /proc/meminfo
MemTotal:      263860780 kB
HugePages_Total:       1
Hugepagesize:     2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.2 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.2"
  PRETTY_NAME="Red Hat Enterprise Linux"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
os-release.rpmsave:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.0 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.0"
  PRETTY_NAME="Red Hat Enterprise Linux"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
Linux SUT 3.10.0-327.18.2.el7.x86_64 #2 SMP Wed Jun 1 17:37:13 CEST 2016
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 21 10:10

SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sdal      ext4  212G  118G   84G  59%  /
Additional information from dmidecode:
  BIOS Intel Corporation SE5C610.86B.11.01.0136.062220161656 06/22/2016
Continued on next page

```



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Platform Notes (Continued)

Memory:

16x 16 GB
16x Hynix HMA42GR7AFR4N-TF 16 GB 1866 MHz 2 rank
8x NO DIMM NO DIMM

(End of data from sysinfo program)
dmidecode does not properly detect memory modules
16 modules of 16 GB were used to run the test (256 GB total)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"
OMP_NUM_THREADS = "12"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enable
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
Binaries compiled on a system with 2x Xeon E5-2650 v4 chips + 256 GB memory
using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX



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Test sponsor: ACTION S.A.	Hardware Availability: Mar-2016
Tested by: ACTION S.A.	Software Availability: Mar-2016

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/lib/ia32_lin
```

```
445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/lib/ia32_lin
```

C++ benchmarks:

```
471.omnetpp: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/lib/ia32_lin
```

```
473.astar: icpc -m64
```

```
483.xalancbmk: icpc -m32 -L/opt/intel/lib/ia32
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
403.gcc: -DSPEC_CPU_LP64
```

```
429.mcf: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

```
464.h264ref: -DSPEC_CPU_LP64
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
            -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
          -opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
            -ansi-alias

456.hmmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
             -ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes
```

C++ benchmarks:

```
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-ra-region-strategy=block -ansi-alias
              -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
            -auto-p32 -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
                -ansi-alias -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap
```

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-aug-2015-For-Intel-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-aug-2015-For-Intel-Platform.xml>

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