SPEC® CFP2006 Result

Lenovo Group Limited

Lenovo ThinkServer TD350
(2.10 GHz, Intel Xeon E5-2683 v4)

$SPECfp^®\_rate2006 = 955$

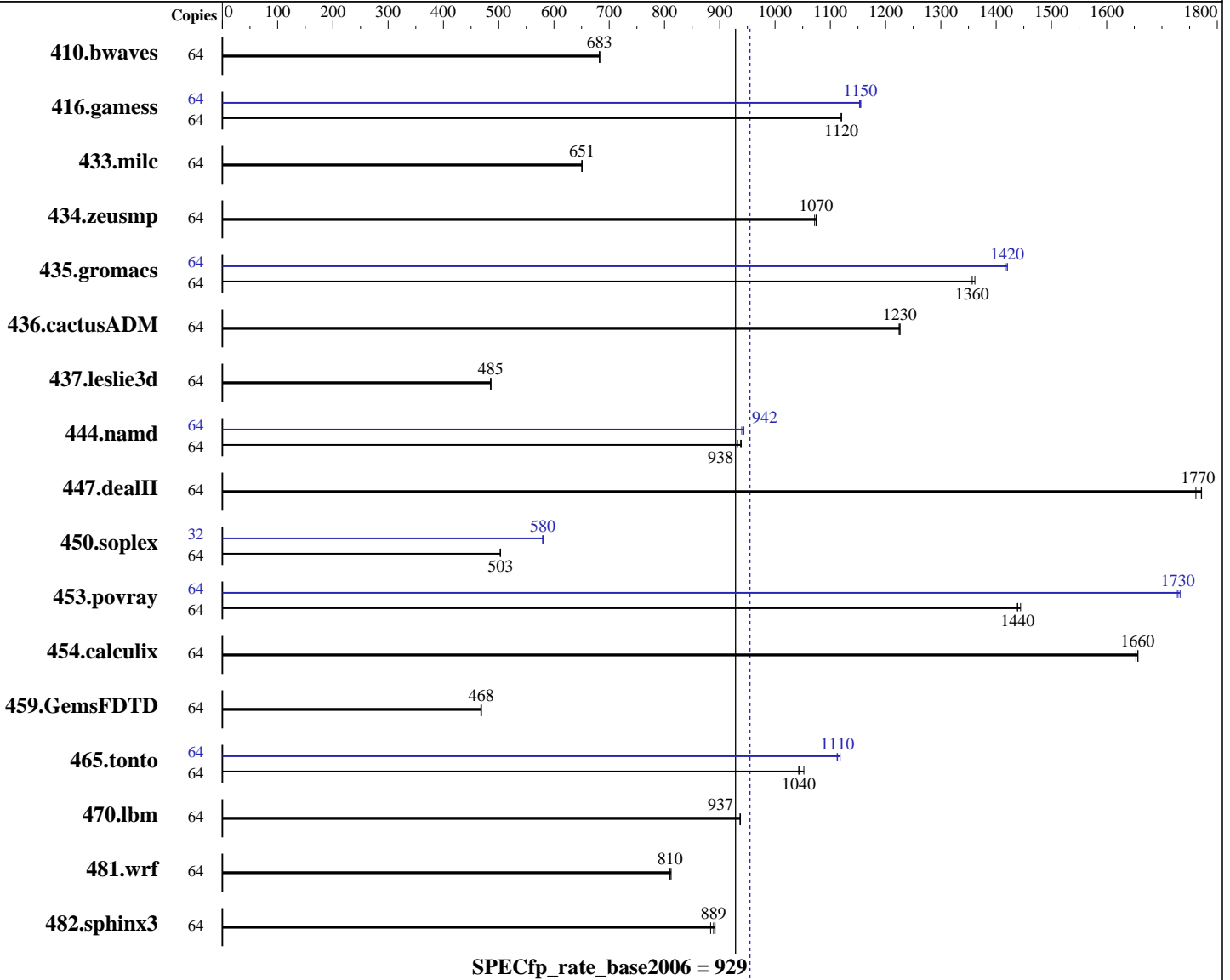$SPECfp\_rate\_base2006 = 929$

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Oct-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

Copies	Benchmark	Result
	410.bwaves 64	683
	416.gamess 64 / 64	1150 / 1120
	433.milc 64	651
	434.zeusmp 64	1070
	435.gromacs 64 / 64	1420 / 1360
	436.cactusADM 64	1230
	437.leslie3d 64	485
	444.namd 64 / 64	942 / 938
	447.dealII 64	1770
	450.soplex 32 / 64	580 / 503
	453.povray 64 / 64	1730 / 1440
	454.calculix 64	1660
	459.GemsFDTD 64	468
	465.tonto 64 / 64	1110 / 1040
	470.lbm 64	937
	481.wrf 64	810
	482.sphinx3 64	889

SPECfp_rate_base2006 = 929
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Hardware

CPU Name: Intel Xeon E5-2683 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz: 2100
FPU: Integrated
CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64) Kernel 3.12.49-11-default
Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)

Continued on next page

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L3 Cache:	40 MB I+D on chip per chip	Base Pointers:	32/64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)	Other Software:	None
Disk Subsystem:	1 x 800 GB SATA SSD		
Other Hardware:	None		

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	1275	682	1274	683	**1274**	**683**	64	1275	682	1274	683	**1274**	**683**
416.gamess	64	1119	1120	1119	1120	**1119**	**1120**	64	1087	1150	**1086**	**1150**	1085	1160
433.milc	64	**903**	**651**	903	651	903	651	64	**903**	**651**	903	651	903	651
434.zeusmp	64	541	1080	543	1070	**542**	**1070**	64	541	1080	543	1070	**542**	**1070**
435.gromacs	64	**337**	**1360**	336	1360	337	1350	64	322	1420	**322**	**1420**	323	1420
436.cactusADM	64	625	1220	**624**	**1230**	624	1230	64	625	1220	**624**	**1230**	624	1230
437.leslie3d	64	1240	485	**1239**	**485**	1238	486	64	1240	485	**1239**	**485**	1238	486
444.namd	64	547	939	**547**	**938**	551	932	64	**545**	**942**	546	940	544	944
447.dealII	64	413	1770	**413**	**1770**	416	1760	64	413	1770	**413**	**1770**	416	1760
450.soplex	64	1060	503	1062	502	**1060**	**503**	32	**460**	**580**	460	581	461	579
453.povray	64	**237**	**1440**	237	1440	236	1440	64	197	1730	**197**	**1730**	196	1730
454.calculix	64	319	1660	**319**	**1660**	319	1650	64	319	1660	**319**	**1660**	319	1650
459.GemsFDTD	64	**1450**	**468**	1449	469	1452	468	64	**1450**	**468**	1449	469	1452	468
465.tonto	64	**604**	**1040**	599	1050	604	1040	64	**566**	**1110**	563	1120	566	1110
470.lbm	64	**938**	**937**	938	937	939	937	64	**938**	**937**	938	937	939	937
481.wrf	64	880	812	883	810	**882**	**810**	64	880	812	883	810	**882**	**810**
482.sphinx3	64	**1403**	**889**	1412	883	1399	891	64	**1403**	**889**	1412	883	1399	891

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches

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Platform Notes

```
BIOS Configuration:
 Cluster On Die set to Enabled
 Early Snoop set to Disabled
 Performance Profile set to Custom
 C1E Support set to Disabled
 Core C3 set to Disabled
 Core C6 set to Disabled
 Thermal Profile set to Max Performance
 Memory Power Savings set to Disabled
 Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
 $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
 running on TD350-01 Sat Oct  8 21:31:25 2016

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2683 v4 @ 2.10GHz
       2 "physical id"s (chips)
       64 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 16
       siblings  : 32
       physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
       physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
    cache size : 20480 KB

 From /proc/meminfo
    MemTotal:      264554012 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

 From /etc/*release* /etc/*version*
    SuSE-release:
       SUSE Linux Enterprise Server 12 (x86_64)
       VERSION = 12
       PATCHLEVEL = 1
       # This file is deprecated and will be removed in a future service pack or
       release.
       # Please check /etc/os-release for details about this release.
    os-release:
       NAME="SLES"
       VERSION="12-SP1"
       VERSION_ID="12.1"
       PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
       ID="sles"
       ANSI_COLOR="0;32"
       CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

Continued on next page

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Platform Notes (Continued)

```
uname -a:
   Linux TD350-01 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
   (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 8 09:43

SPEC is set to: /home/cpu2006-1.2-ic16.0
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sda4       xfs   689G  107G  582G  16% /home
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS LENOVO TB5TS362 03/24/2016
  Memory:
  16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

 Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB
 memory using RedHat EL 7.2 glibc 2.17
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc    -m64
```

C++ benchmarks:
```
icpc   -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

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Software Availability: Mar-2016

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
   416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
   434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
     470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
    icc   -m64
```

C++ benchmarks (except as noted below):
```
    icpc   -m64
```

```
  450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Continued on next page

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Peak Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

```
      410.bwaves: -DSPEC_CPU_LP64
      416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
   436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
      437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
       447.dealII: -DSPEC_CPU_LP64
      450.soplex: -D_FILE_OFFSET_BITS=64
      453.povray: -DSPEC_CPU_LP64
     454.calculix: -DSPEC_CPU_LP64 -nofor_main
   459.GemsFDTD: -DSPEC_CPU_LP64
        465.tonto: -DSPEC_CPU_LP64
         470.lbm: -DSPEC_CPU_LP64
         481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
      482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
        433.milc: basepeak = yes

        470.lbm: basepeak = yes

      482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
        444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
                  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
                  -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
                  -prof-use(pass 2) -fno-alias -auto-ilp32

       447.dealII: basepeak = yes
```

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Peak Optimization Flags (Continued)

450.soplex: `-xCORE-AVX2(pass 2)` `-prof-gen:threadsafe(pass 1)`
`-ipo(pass 2)` `-O3(pass 2)` `-no-prec-div(pass 2)`
`-par-num-threads=1(pass 1)` `-opt-mem-layout-trans=3(pass 2)`
`-prof-use(pass 2)` `-opt-malloc-options=3`

453.povray: `-xCORE-AVX2(pass 2)` `-prof-gen:threadsafe(pass 1)`
`-ipo(pass 2)` `-O3(pass 2)` `-no-prec-div(pass 2)`
`-par-num-threads=1(pass 1)` `-opt-mem-layout-trans=3(pass 2)`
`-prof-use(pass 2)` `-unroll4` `-ansi-alias`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `-xCORE-AVX2(pass 2)` `-prof-gen:threadsafe(pass 1)`
`-ipo(pass 2)` `-O3(pass 2)` `-no-prec-div(pass 2)`
`-par-num-threads=1(pass 1)` `-prof-use(pass 2)` `-unroll2`
`-inline-level=0` `-scalar-rep-`

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-xCORE-AVX2(pass 2)` `-prof-gen:threadsafe(pass 1)`
`-ipo(pass 2)` `-O3(pass 2)` `-no-prec-div(pass 2)`
`-par-num-threads=1(pass 1)` `-prof-use(pass 2)` `-unroll4` `-auto`
`-inline-calloc` `-opt-malloc-options=3`

Benchmarks using both Fortran and C:

435.gromacs: `-xCORE-AVX2(pass 2)` `-prof-gen:threadsafe(pass 1)`
`-ipo(pass 2)` `-O3(pass 2)` `-no-prec-div(pass 2)`
`-par-num-threads=1(pass 1)` `-opt-mem-layout-trans=3(pass 2)`
`-prof-use(pass 2)` `-opt-prefetch` `-auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml

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Tested with SPEC CPU2006 v1.2.
Report generated on Wed Nov 2 10:39:03 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 November 2016.