SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX250 S2, Intel Xeon E5-2650L v2, 1.70 GHz

$SPECint^{®}\_rate2006 =$ 565

$SPECint\_rate\_base2006 =$ 543

CPU2006 license: 19	**Test date:** Nov-2013
Test sponsor: Fujitsu	**Hardware Availability:** Sep-2013
Tested by: Fujitsu	**Software Availability:** Sep-2013

Copies 0 100 250 400 550 700 850 1000 1150 1300 1450 1600 1750 1900 2050 2200 2350 2500 2650 2800 2950 3100 3250 3500

```
400.perlbench   40  474
                40  392
401.bzip2       40  301
                40  295
403.gcc         40  440
429.mcf         40  884
445.gobmk       40  399
                40  382
456.hmmer       40  798
                40  716
458.sjeng       40  396
                40  383
462.libquantum  40  3500
464.h264ref     40  657
                40  650
471.omnetpp     40  347
                40  327
473.astar       40  315
483.xalancbmk   40  597
```

SPECint_rate_base2006 = 543

SPECint_rate2006 = 565

Hardware

CPU Name:	Intel Xeon E5-2650L v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.10 GHz
CPU MHz:	1700
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (16 x 8 GB 2Rx8 PC3-14900R-13, ECC, running at 1600 MHz and CL11)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.11.1.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 5 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX250 S2, Intel Xeon E5-2650L v2, 1.70 GHz

SPECint_rate2006 = 565

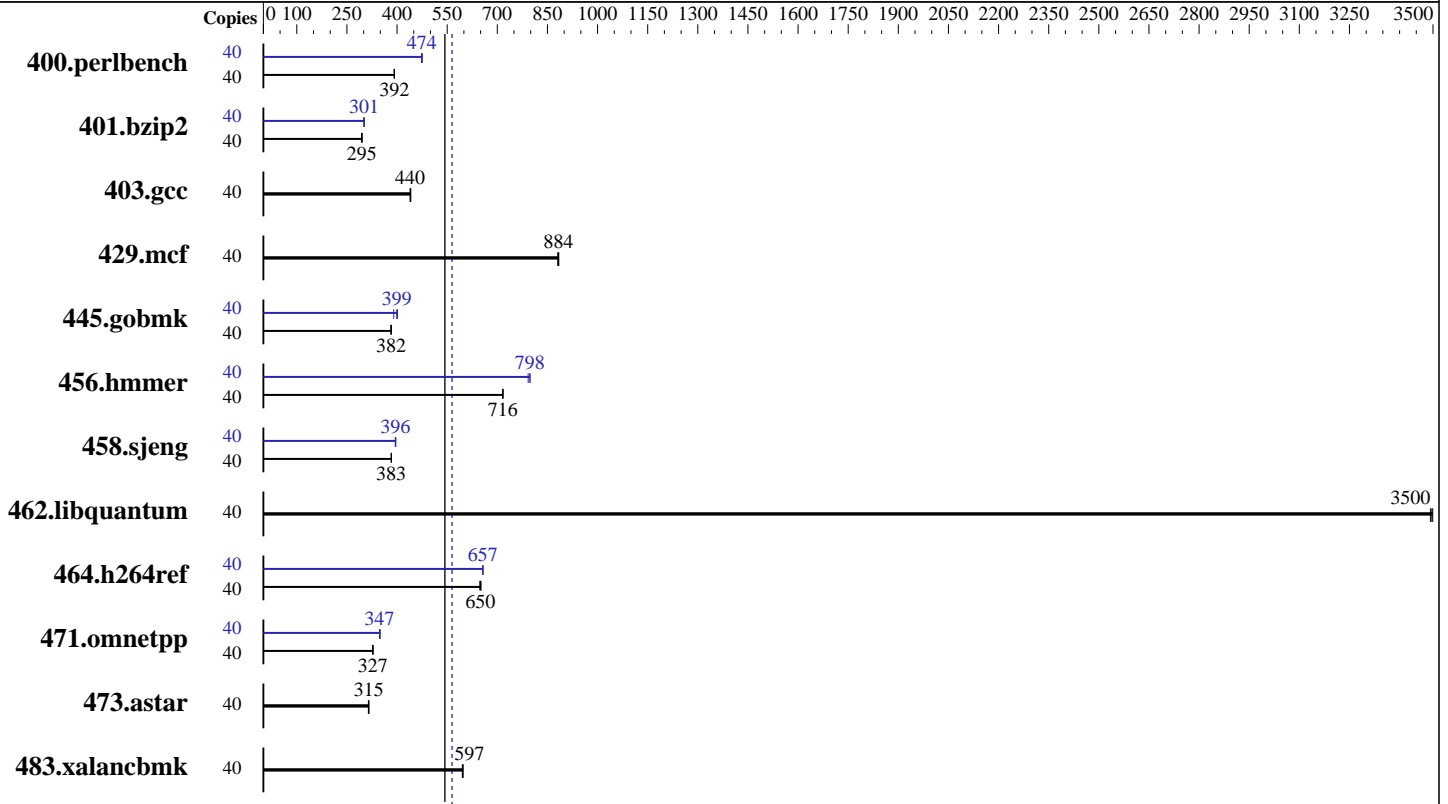SPECint_rate_base2006 = 543

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Nov-2013
Hardware Availability: Sep-2013
Software Availability: Sep-2013

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	**997**	**392**	999	391	996	392	40	822	475	826	473	**824**	**474**
401.bzip2	40	1314	294	**1309**	**295**	1306	296	40	1283	301	1279	302	**1282**	**301**
403.gcc	40	730	441	**732**	**440**	733	439	40	730	441	**732**	**440**	733	439
429.mcf	40	413	884	**413**	**884**	414	881	40	413	884	**413**	**884**	414	881
445.gobmk	40	1100	381	**1097**	**382**	1095	383	40	1076	390	**1051**	**399**	1045	401
456.hmmer	40	520	717	**521**	**716**	521	716	40	468	798	**468**	**798**	471	792
458.sjeng	40	1263	383	1265	383	**1265**	**383**	40	1222	396	**1223**	**396**	1226	395
462.libquantum	40	237	3500	237	3490	**237**	**3500**	40	237	3500	237	3490	**237**	**3500**
464.h264ref	40	1367	648	1359	651	**1362**	**650**	40	1347	657	1349	656	**1348**	**657**
471.omnetpp	40	761	328	764	327	**763**	**327**	40	720	347	**719**	**347**	715	350
473.astar	40	894	314	890	316	**890**	**315**	40	894	314	890	316	**890**	**315**
483.xalancbmk	40	462	598	**463**	**597**	464	595	40	462	598	**463**	**597**	464	595

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

   The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

   Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

   BIOS configuration:
   Energy Performance = Performance

General Notes

   Environment variables set by runspec before the start of the run:
   LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"

   Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
   memory using RedHat EL 6.4
   Transparent Huge Pages enabled with:
   echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
   Filesystem page cache cleared with:
   echo 1>   /proc/sys/vm/drop_caches
   runspec command invoked through numactl i.e.:
   numactl --interleave=all runspec <etc>

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX250 S2, Intel Xeon E5-2650L v2, 1.70 GHz

SPECint_rate2006 =   565

SPECint_rate_base2006 = 543

CPU2006 license:	19	**Test date:**	Nov-2013
Test sponsor:	Fujitsu	**Hardware Availability:**	Sep-2013
Tested by:	Fujitsu	**Software Availability:**	Sep-2013

General Notes (Continued)

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

Fujitsu

PRIMERGY CX250 S2, Intel Xeon E5-2650L v2, 1.70 GHz

SPECint_rate2006 =   565

SPECint_rate_base2006 = 543

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Nov-2013

Hardware Availability: Sep-2013

Software Availability: Sep-2013

Peak Compiler Invocation (Continued)

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll2 -ansi-alias`

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY CX250 S2, Intel Xeon E5-2650L v2, 1.70 GHz

SPECint_rate2006 =   565

SPECint_rate_base2006 =   543

CPU2006 license: 19	**Test date:** Nov-2013
Test sponsor: Fujitsu	**Hardware Availability:** Sep-2013
Tested by: Fujitsu	**Software Availability:** Sep-2013

Peak Optimization Flags (Continued)

C++ benchmarks:

> 471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
> `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
> `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
> `-L/sh -lsmartheap`

> 473.astar: `basepeak = yes`

> 483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 18:08:37 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 December 2013.