



# SPEC® CINT2006 Result

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## IBM Corporation

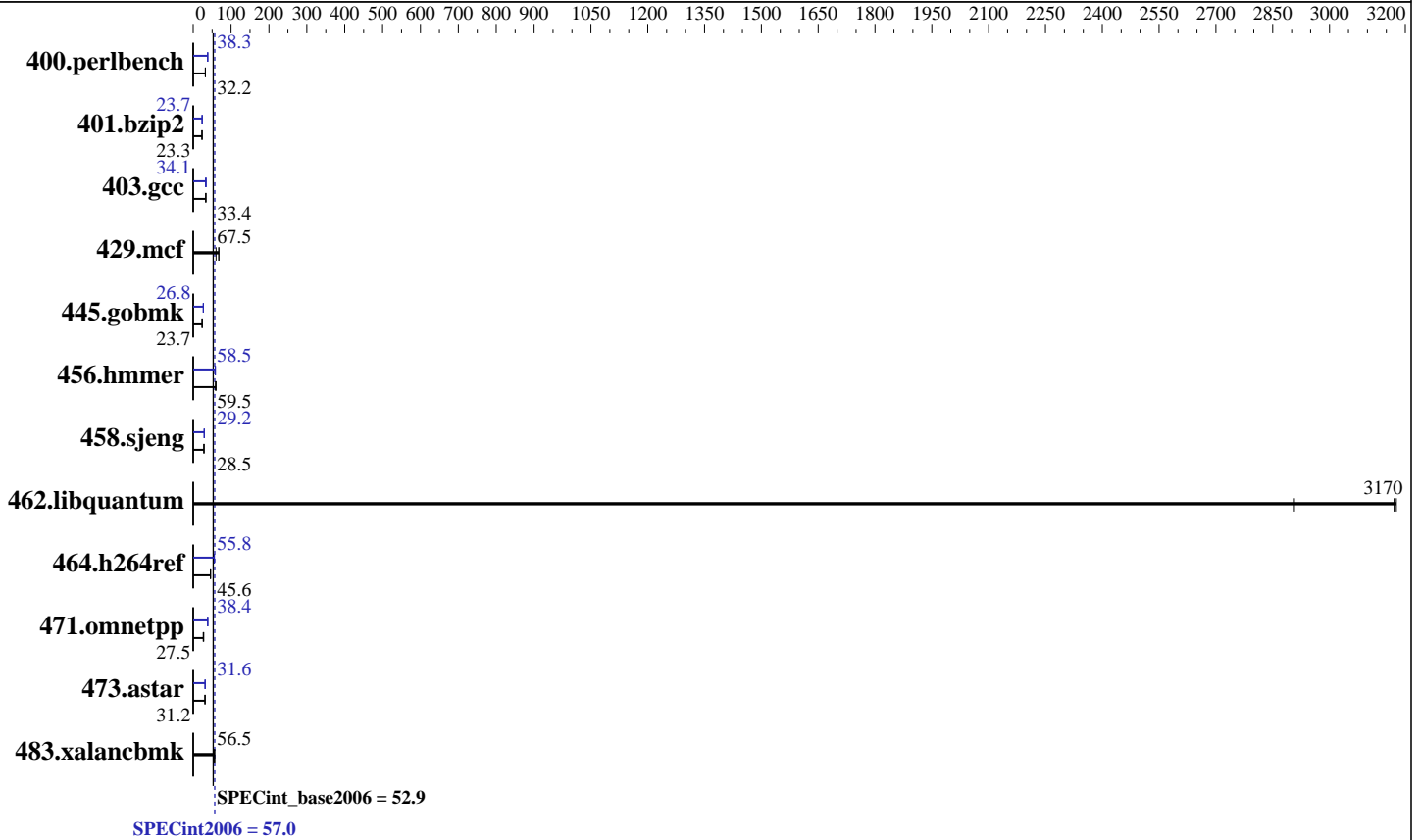
**SPECint®2006 = 57.0**

IBM System x3650 M4  
(Intel Xeon E5-2650 v2, 2.60 GHz)

**SPECint\_base2006 = 52.9**

**CPU2006 license:** 11  
**Test sponsor:** IBM Corporation  
**Tested by:** IBM Corporation

**Test date:** Oct-2013  
**Hardware Availability:** Dec-2013  
**Software Availability:** Sep-2013



### Hardware

**CPU Name:** Intel Xeon E5-2650 v2  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.40 GHz  
**CPU MHz:** 2600  
**FPU:** Integrated  
**CPU(s) enabled:** 16 cores, 2 chips, 8 cores/chip  
**CPU(s) orderable:** 1,2 chips  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 20 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
**Disk Subsystem:** 1 x 400 GB SAS SSD, RAID 0  
**Other Hardware:** None

### Software

**Operating System:** Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
**Compiler:** C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
**Auto Parallel:** Yes  
**File System:** ext4  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	303	32.3	<b>303</b>	<b>32.2</b>	303	32.2	255	38.3	<b>255</b>	<b>38.3</b>	255	38.4
401.bzip2	<b>413</b>	<b>23.3</b>	413	23.4	414	23.3	<b>407</b>	<b>23.7</b>	408	23.7	406	23.8
403.gcc	241	33.4	<b>241</b>	<b>33.4</b>	241	33.4	236	34.1	248	32.5	<b>236</b>	<b>34.1</b>
429.mcf	151	60.4	134	67.8	<b>135</b>	<b>67.5</b>	151	60.4	134	67.8	<b>135</b>	<b>67.5</b>
445.gobmk	<b>442</b>	<b>23.7</b>	442	23.7	443	23.7	391	26.8	391	26.8	<b>391</b>	<b>26.8</b>
456.hammer	158	59.2	155	60.2	<b>157</b>	<b>59.5</b>	159	58.6	160	58.4	<b>160</b>	<b>58.5</b>
458.sjeng	<b>425</b>	<b>28.5</b>	425	28.5	425	28.5	414	29.2	414	29.2	<b>414</b>	<b>29.2</b>
462.libquantum	7.13	2910	<b>6.54</b>	<b>3170</b>	6.52	3180	7.13	2910	<b>6.54</b>	<b>3170</b>	6.52	3180
464.h264ref	485	45.6	483	45.8	<b>485</b>	<b>45.6</b>	<b>396</b>	<b>55.8</b>	397	55.8	396	55.8
471.omnetpp	236	26.5	<b>227</b>	<b>27.5</b>	226	27.7	<b>163</b>	<b>38.4</b>	164	38.2	162	38.6
473.astar	<b>225</b>	<b>31.2</b>	224	31.4	225	31.2	222	31.7	224	31.4	<b>222</b>	<b>31.6</b>
483.xalancbmk	122	56.3	<b>122</b>	<b>56.5</b>	122	56.6	122	56.3	<b>122</b>	<b>56.5</b>	122	56.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Zone reclaim mode enabled with:  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode

## Platform Notes

BIOS setting:  
Operating Mode set to Maximum Performance  
Hyper-Threading set to Disabled  
Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191  
running on x3650M4 Tue Oct 8 01:38:53 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2650 v2 @ 2.60GHz  
2 "physical id"s (chips)  
16 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with  
Continued on next page



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### Platform Notes (Continued)

```
caution.)
  cpu cores : 8
  siblings  : 8
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
  cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      264342648 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux x3650M4 2.6.32-358.18.1.el6.x86_64 #1 SMP Fri Aug 2 17:04:38 EDT 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 7 14:13
```

```
SPEC is set to: /home/SPECcpu-new
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/mapper/vg_x3650m4-lv_home
                ext4      313G      203G      94G   69% /home
```

```
Additional information from dmidecode:
BIOS IBM -[TESTBUILD-1.50]- 08/09/2013
Memory:
8x Not Specified Not Specified
16x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank
```

(End of data from sysinfo program)  
"Not Specified" memory information from dmidecode indicates unused DIMM slots.

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/SPECcpu-new/libs/32:/home/SPECcpu-new/libs/64:/home/SPECcpu-new/sh"  
OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:

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## General Notes (Continued)

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
  
C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/sh -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-ansi-alias  
401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch  
-ansi-alias  
403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32  
429.mcf: basepeak = yes  
445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

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## Peak Optimization Flags (Continued)

456.hmmr: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>



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