



SPEC® CINT2006 Result

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Fujitsu

SPECint®_rate2006 = **NC**

PRIMERGY TX140 S1p, Intel Pentium G550, 2.60 GHz

SPECint_rate_base2006 = **NC**

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2012
Hardware Availability: Jun-2012
Software Availability: Feb-2012

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result does not meet the 3 month availability requirement in the SPEC CPU2006 run rules, because the tested processor was not supported on this platform within three months of publication.

	Copies
400.perlbench	
401.bzip2	
403.gcc	
429.mcf	
445.gobmk	
456.hmmer	
458.sjeng	
462.libquantum	
464.h264ref	
471.omnetpp	
483.xalancbmk	

Non-Compliant

Hardware

CPU Name: Intel Celeron G550
CPU Characteristics:
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
2.6.32-220.el6.x86_64
Compiler: C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux

Continued on next page



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CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 2 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC, running at 1067 MHz and CL7)
Disk Subsystem: 1 x SATA, 500 GB / 7200 RPM
Other Hardware: None

Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		
401.bzip2	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		
403.gcc	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		
429.mcf	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		
445.gobmk	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		
456.hmmer	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		
458.sjeng	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		
462.libquantum	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		
464.lzbench	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		
471.omnetpp	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		
473.astar	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		
483.xalancbmk	2	NC	NC	NC	NC	NC	NC	2	NC	NC	NC	NC	NC	NC		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.



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Operating System Notes

Stack size set to unlimited using "ulimit -u unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl:
numactl --interleave=all runspec <etc>

General Notes

Environment variables set by user before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

Binaries compiled on a system with 1x E3-1270v2 CPU + 32 GB memory using RHEL6.
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
gcc -m32

C++ benchmarks:
g++ -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
400.perlbenc: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64
C++ benchmarks:
icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINK_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINK_UNIX
483.xalancbmk: -DSPEC_CPU_LINK_UNIX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
405gcc: basepeak = yes
405gcc: basepeak = yes
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/opt/SmartHeap/lib -lsmartheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

-Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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