



# SPEC<sup>®</sup> CFP2006 Result

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Dell Inc.

SPECfp<sup>®</sup>2006 = 59.9

PowerEdge R820 (Intel Xeon E5-4620, 2.20 GHz)

SPECfp\_base2006 = 55.9

CPU2006 license: 55

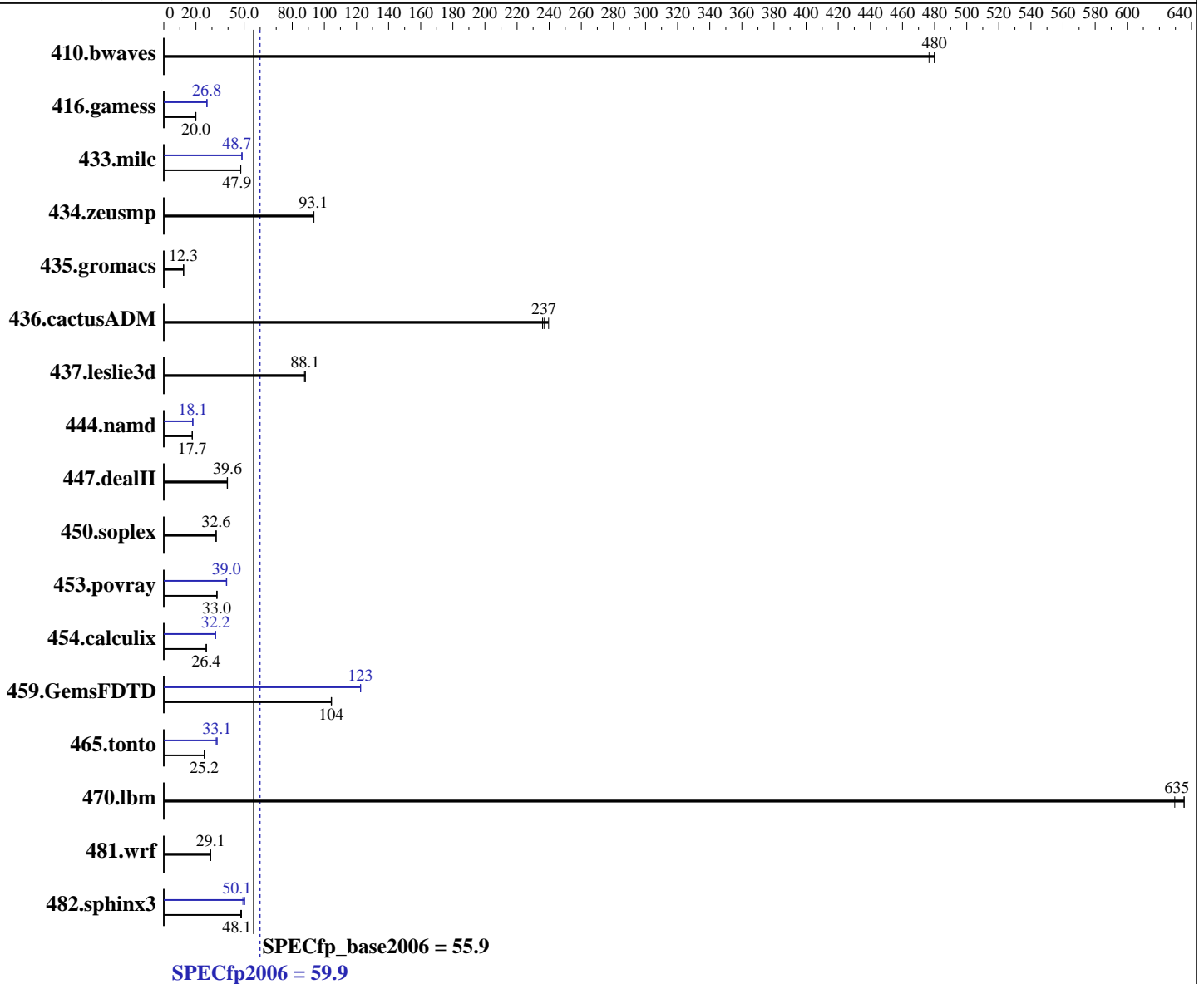
Test date: Mar-2012

Test sponsor: Dell Inc.

Hardware Availability: May-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



## Hardware

CPU Name: Intel Xeon E5-4620  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.60 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.27-default  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)

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L3 Cache: 16 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 4 x 300 GB 10000 RPM SAS, RAID 0  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	28.3	480	28.5	477	<b>28.3</b>	<b>480</b>	28.3	480	28.5	477	<b>28.3</b>	<b>480</b>
416.gamess	<b>981</b>	<b>20.0</b>	978	20.0	982	19.9	728	26.9	<b>730</b>	<b>26.8</b>	730	26.8
433.milc	192	47.9	191	48.0	<b>192</b>	<b>47.9</b>	<b>189</b>	<b>48.7</b>	189	48.7	189	48.7
434.zeusmp	<b>97.7</b>	<b>93.1</b>	97.3	93.5	97.7	93.1	<b>97.7</b>	<b>93.1</b>	97.3	93.5	97.7	93.1
435.gromacs	578	12.4	582	12.3	<b>580</b>	<b>12.3</b>	578	12.4	582	12.3	<b>580</b>	<b>12.3</b>
436.cactusADM	50.7	236	49.9	240	<b>50.5</b>	<b>237</b>	50.7	236	49.9	240	<b>50.5</b>	<b>237</b>
437.leslie3d	107	88.1	107	87.6	<b>107</b>	<b>88.1</b>	107	88.1	107	87.6	<b>107</b>	<b>88.1</b>
444.namd	<b>452</b>	<b>17.7</b>	452	17.8	452	17.7	444	18.1	<b>444</b>	<b>18.1</b>	444	18.1
447.dealII	<b>289</b>	<b>39.6</b>	288	39.7	290	39.5	<b>289</b>	<b>39.6</b>	288	39.7	290	39.5
450.soplex	<b>256</b>	<b>32.6</b>	256	32.5	256	32.6	<b>256</b>	<b>32.6</b>	256	32.5	256	32.6
453.povray	160	33.2	161	33.0	<b>161</b>	<b>33.0</b>	136	39.0	<b>136</b>	<b>39.0</b>	136	39.2
454.calculix	<b>312</b>	<b>26.4</b>	311	26.5	313	26.4	256	32.2	<b>256</b>	<b>32.2</b>	257	32.0
459.GemsFDTD	102	105	<b>102</b>	<b>104</b>	102	104	86.6	123	86.6	123	<b>86.6</b>	<b>123</b>
465.tonto	389	25.3	<b>390</b>	<b>25.2</b>	392	25.1	297	33.1	302	32.6	<b>297</b>	<b>33.1</b>
470.lbm	21.6	635	21.8	630	<b>21.6</b>	<b>635</b>	21.6	635	21.8	630	<b>21.6</b>	<b>635</b>
481.wrf	384	29.1	<b>384</b>	<b>29.1</b>	383	29.2	384	29.1	<b>384</b>	<b>29.1</b>	383	29.2
482.sphinx3	<b>405</b>	<b>48.1</b>	403	48.4	407	47.9	387	50.3	396	49.2	<b>389</b>	<b>50.1</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

System Profile set to Custom  
 CPU Power Management set to Maximum Performance  
 Memory Frequency set to Maximum Performance  
 Turbo Boost set to Enabled  
 C States/C1E set to Enabled  
 Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on icon4p Mon Mar 12 18:57:34 2012

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-4620 0 @ 2.20GHz
 4 "physical id"s (chips)
 64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 8
  siblings     : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
  physical 2: cores 0 1 2 3 4 5 6 7
  physical 3: cores 0 1 2 3 4 5 6 7
cache size     : 16384 KB

```

```

From /proc/meminfo
MemTotal:      264501512 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2

```

```

uname -a:
Linux icon4p 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
(d73692b) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 12 10:55 last=S

```

SPEC is set to: /root/CPU2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext3  1.1T  123G  917G  12% /

```

Additional information from dmidecode:

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
OMP_NUM_THREADS = "32"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages disabled with:

```
echo never > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

The Dell PowerEdge R820 and

the Bull NovaScale R470 F3 Models are electronically equivalent.

The results have been measured on a Dell PowerEdge R820 model.

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

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## Base Portability Flags (Continued)

482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

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## Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

### C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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