



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®2006 = 45.0

PowerEdge R420 (Intel Xeon E5-2403, 1.80 GHz)

SPECfp_base2006 = 43.6

CPU2006 license: 55

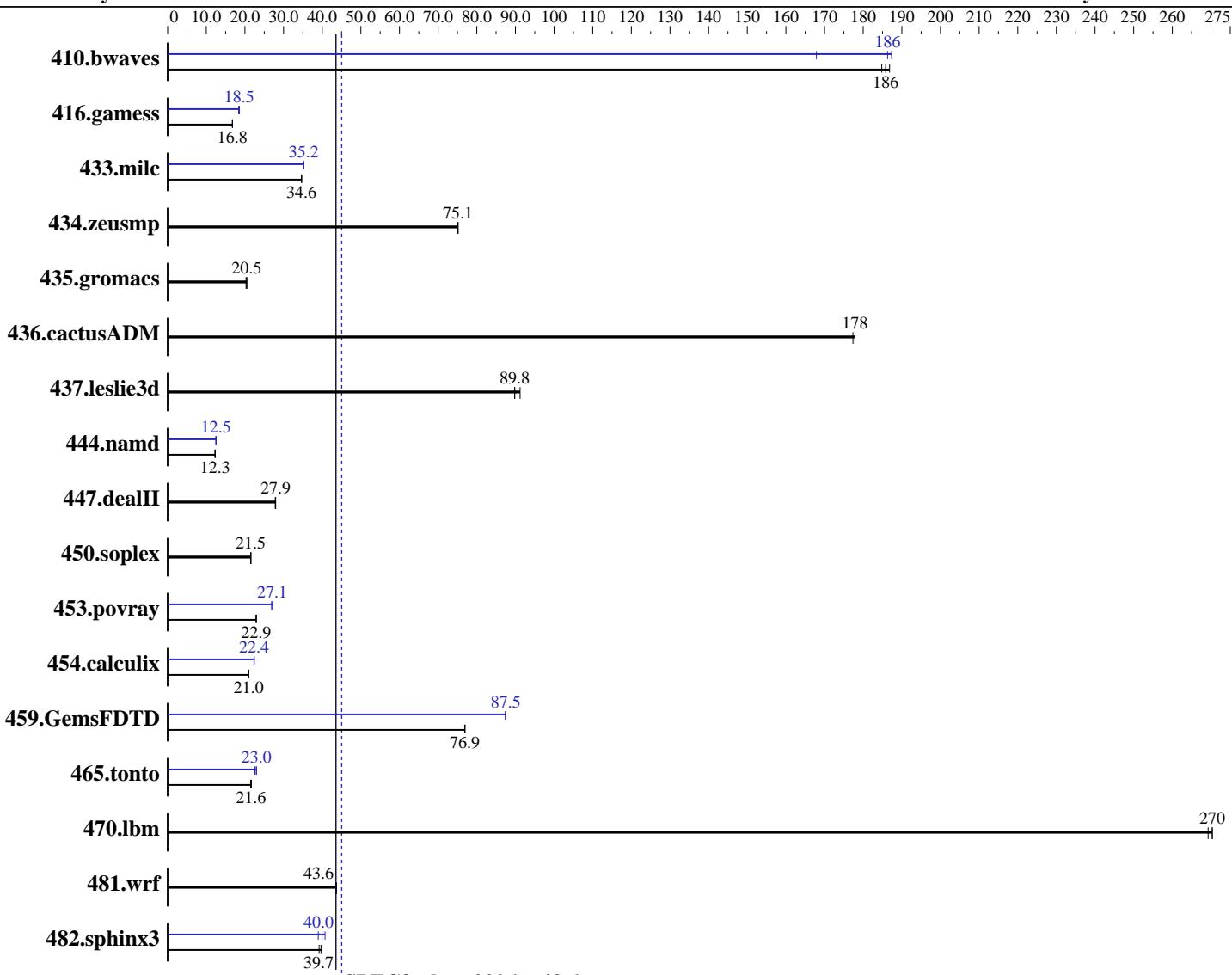
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Mar-2012

Hardware Availability: May-2012

Software Availability: Feb-2012



Hardware

CPU Name: Intel Xeon E5-2403
 CPU Characteristics:
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86_64)
 3.0.13-0.9-default
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE
 for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran
 Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (add definition here)

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L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz)
 Disk Subsystem: 2 x 300 GB 15000 RPM SAS, RAID 1
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	73.5	185	72.7	187	<u>73.1</u>	<u>186</u>	80.9	168	<u>72.9</u>	<u>186</u>	72.5	187
416.gamess	<u>1166</u>	<u>16.8</u>	1166	16.8	1171	16.7	<u>1054</u>	18.6	1066	18.4	<u>1057</u>	<u>18.5</u>
433.milc	265	34.6	<u>265</u>	<u>34.6</u>	265	34.7	<u>261</u>	<u>35.2</u>	261	35.2	261	35.2
434.zeusmp	<u>121</u>	<u>75.1</u>	121	75.0	121	75.1	<u>121</u>	<u>75.1</u>	121	75.0	121	75.1
435.gromacs	<u>349</u>	<u>20.5</u>	352	20.3	349	20.5	<u>349</u>	<u>20.5</u>	352	20.3	349	20.5
436.cactusADM	<u>67.2</u>	<u>178</u>	67.4	177	67.2	178	<u>67.2</u>	<u>178</u>	67.4	177	67.2	178
437.leslie3d	105	89.8	103	91.2	<u>105</u>	<u>89.8</u>	105	89.8	103	91.2	<u>105</u>	<u>89.8</u>
444.namd	<u>653</u>	<u>12.3</u>	653	12.3	653	12.3	<u>641</u>	<u>12.5</u>	641	12.5	641	12.5
447.dealII	409	28.0	<u>410</u>	<u>27.9</u>	411	27.8	409	28.0	<u>410</u>	<u>27.9</u>	411	27.8
450.soplex	387	21.5	388	21.5	<u>388</u>	<u>21.5</u>	387	21.5	388	21.5	<u>388</u>	<u>21.5</u>
453.povray	232	22.9	232	22.9	<u>232</u>	<u>22.9</u>	195	27.2	<u>196</u>	<u>27.1</u>	198	26.9
454.calculix	<u>394</u>	<u>21.0</u>	393	21.0	396	20.8	368	22.4	369	22.3	<u>368</u>	<u>22.4</u>
459.GemsFDTD	<u>138</u>	<u>76.9</u>	138	77.0	138	76.9	<u>121</u>	<u>87.5</u>	121	87.5	121	87.4
465.tonto	<u>455</u>	<u>21.6</u>	455	21.6	458	21.5	436	22.6	<u>429</u>	<u>23.0</u>	428	23.0
470.lbm	<u>50.8</u>	<u>270</u>	50.8	270	51.0	269	<u>50.8</u>	<u>270</u>	50.8	270	51.0	269
481.wrf	256	43.7	<u>256</u>	<u>43.6</u>	259	43.1	256	43.7	<u>256</u>	<u>43.6</u>	259	43.1
482.sphinx3	497	39.2	<u>491</u>	<u>39.7</u>	488	39.9	<u>488</u>	<u>40.0</u>	479	40.7	500	39.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

CPU Power Management set to Maximum Performance

Memory Frequency set to Maximum Performance

Turbo Boost set to Enabled

C States/C1E set to Enabled

Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 #\\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on Slice Wed Mar 14 01:10:49 2012

This section contains SUT (System Under Test) info as seen by

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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2403 0 @ 1.80GHz
        2 "physical id"s (chips)
        8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 4
    siblings   : 4
    physical 0: cores 0 1 2 3
    physical 1: cores 0 1 2 3
cache size : 10240 kB
```

```
From /proc/meminfo
    MemTotal:      49381468 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2
```

```
uname -a:
Linux Slice 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012 (54ddfaf)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 13 14:20 last=S
```

```
SPEC is set to: /root/CPU2006-1.2
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sdal      ext3  266G  9.1G  244G   4%  /
```

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/root/CPU2006-1.2/lib32:/root/CPU2006-1.2/lib64"

OMP_NUM_THREADS = "8"

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General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages disabled with:

```
echo never > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

The Dell PowerEdge R420 and

the Bull NovaScale R430 F3 models are electronically equivalent.

The results have been measured on a Dell PowerEdge R420 model

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```



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Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias  
-parallel
```

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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