



# SPEC® CFP2006 Result

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## IBM Corporation

IBM System x iDataPlex dx360 M4 (Intel Xeon E5-2650)

**SPECfp®2006 = 75.9**

**SPECfp\_base2006 = 71.5**

CPU2006 license: 11

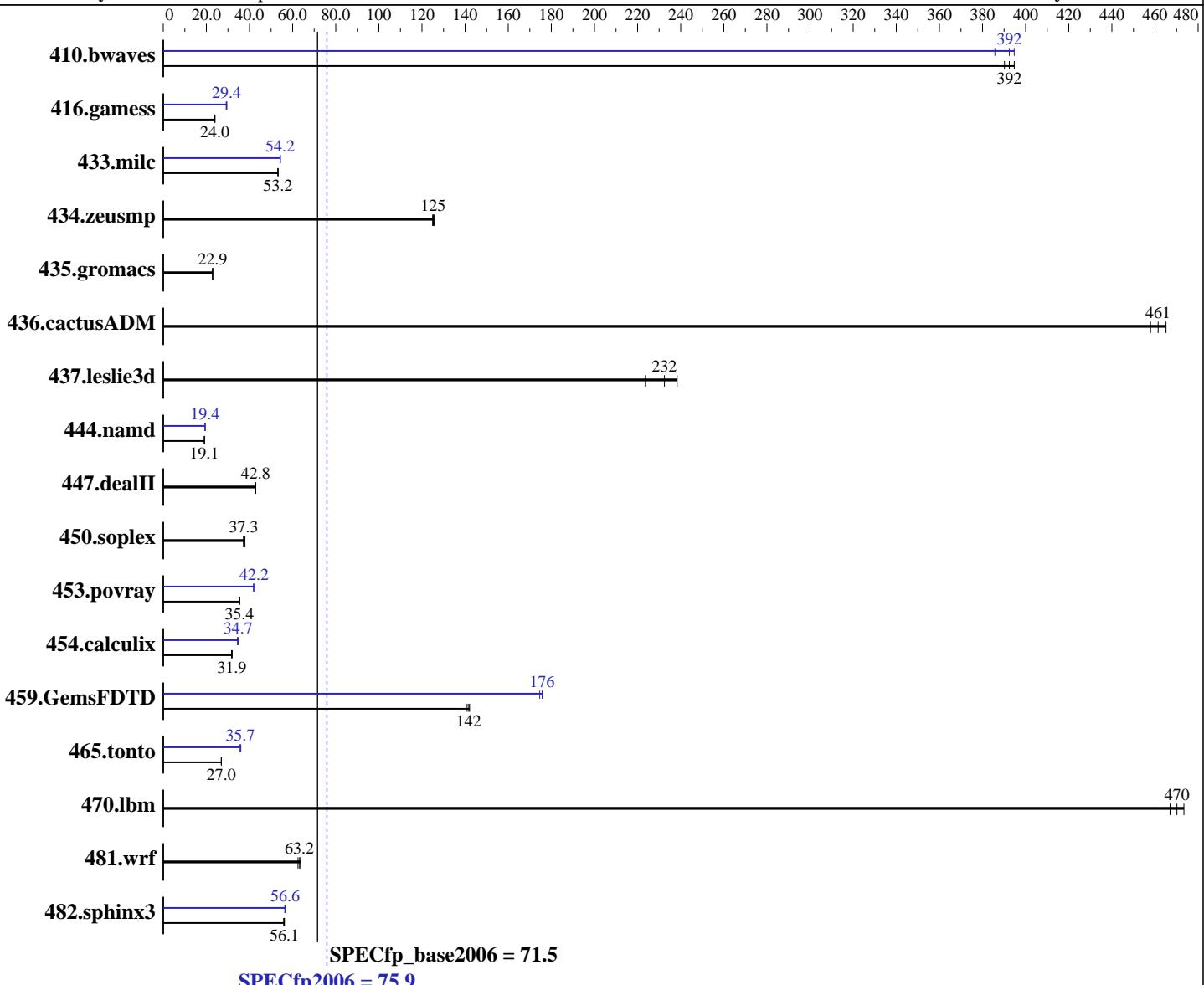
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2650  
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
CPU MHz: 2000  
FPU: Integrated  
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
2.6.32-220.el6.x86\_64  
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
Auto Parallel: Yes  
File System: ext4

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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	<b>34.6</b>	<b>392</b>	34.8	390	34.4	395	<b>35.2</b>	<b>386</b>	<b>34.6</b>	<b>392</b>	34.4	395
416.gamess	820	23.9	<b>817</b>	<b>24.0</b>	817	24.0	<b>665</b>	<b>29.4</b>	671	29.2	<b>666</b>	<b>29.4</b>
433.milc	<b>172</b>	<b>53.2</b>	173	53.2	172	53.3	<b>169</b>	<b>54.3</b>	169	54.2	<b>169</b>	<b>54.2</b>
434.zeusmp	72.5	126	72.9	125	<b>72.7</b>	<b>125</b>	<b>72.5</b>	126	72.9	125	<b>72.7</b>	<b>125</b>
435.gromacs	312	22.9	311	22.9	<b>312</b>	<b>22.9</b>	312	22.9	311	22.9	<b>312</b>	<b>22.9</b>
436.cactusADM	26.1	458	<b>25.9</b>	<b>461</b>	25.7	465	<b>26.1</b>	458	<b>25.9</b>	<b>461</b>	25.7	465
437.leslie3d	<b>40.4</b>	<b>232</b>	42.0	224	39.4	238	<b>40.4</b>	<b>232</b>	42.0	224	39.4	238
444.namd	420	19.1	<b>420</b>	<b>19.1</b>	421	19.1	413	19.4	<b>413</b>	<b>19.4</b>	413	19.4
447.dealII	267	42.8	268	42.8	<b>267</b>	<b>42.8</b>	267	42.8	268	42.8	<b>267</b>	<b>42.8</b>
450.soplex	<b>223</b>	<b>37.3</b>	224	37.2	220	37.9	<b>223</b>	<b>37.3</b>	224	37.2	220	37.9
453.povray	<b>150</b>	<b>35.4</b>	151	35.3	150	35.5	<b>126</b>	<b>42.2</b>	125	42.4	127	41.8
454.calculix	258	31.9	261	31.7	<b>259</b>	<b>31.9</b>	240	34.4	<b>238</b>	<b>34.7</b>	238	34.7
459.GemsFDTD	74.7	142	<b>74.9</b>	<b>142</b>	75.3	141	<b>60.4</b>	<b>176</b>	60.8	175	60.4	176
465.tonto	365	26.9	<b>365</b>	<b>27.0</b>	365	27.0	278	35.4	273	36.0	<b>275</b>	<b>35.7</b>
470.lbm	29.4	467	29.0	473	<b>29.2</b>	<b>470</b>	29.4	467	29.0	473	<b>29.2</b>	<b>470</b>
481.wrf	179	62.5	<b>177</b>	<b>63.2</b>	176	63.6	<b>179</b>	62.5	<b>177</b>	<b>63.2</b>	176	63.6
482.sphinx3	<b>347</b>	<b>56.1</b>	347	56.2	349	55.8	<b>344</b>	<b>56.6</b>	<b>344</b>	<b>56.6</b>	346	56.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Zone reclaim mode enabled with:

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

## Platform Notes

BIOS Settings:

Operating Mode set to Maximum Performance

Sysinfo program /root/SPECcpu-v1.2/Docs/sysinfo

\$Rev: 6775 \$ \$Date::: 2011-08-16 #\\$ 8787f7622badcf24e01c368b1db4377c

running on tianden Thu Apr 26 06:45:25 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 0 @ 2.00GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      132093704 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux tianden 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 24 15:34
```

```
SPEC is set to: /root/SPECcpu-v1.2
Filesystem      Type   Size  Used Avail Use% Mounted on
/dev/mapper/vg_tianen-lv_root
                  ext4   145G   74G   64G  54%  /
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/root/SPECcpu-v1.2/libs/32:/root/SPECcpu-v1.2/libs/64"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

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## General Notes (Continued)

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

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## Base Optimization Flags (Continued)

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
           -ansi-alias
```

470.lbm: basepeak = yes

```
482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias
              -parallel
```

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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