



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1027GR-TSF (X9DRG-HF, Intel Xeon E5-2667, 2.9GHz)

SPECint®2006 = 50.4

SPECint_base2006 = 47.5

CPU2006 license: 001176

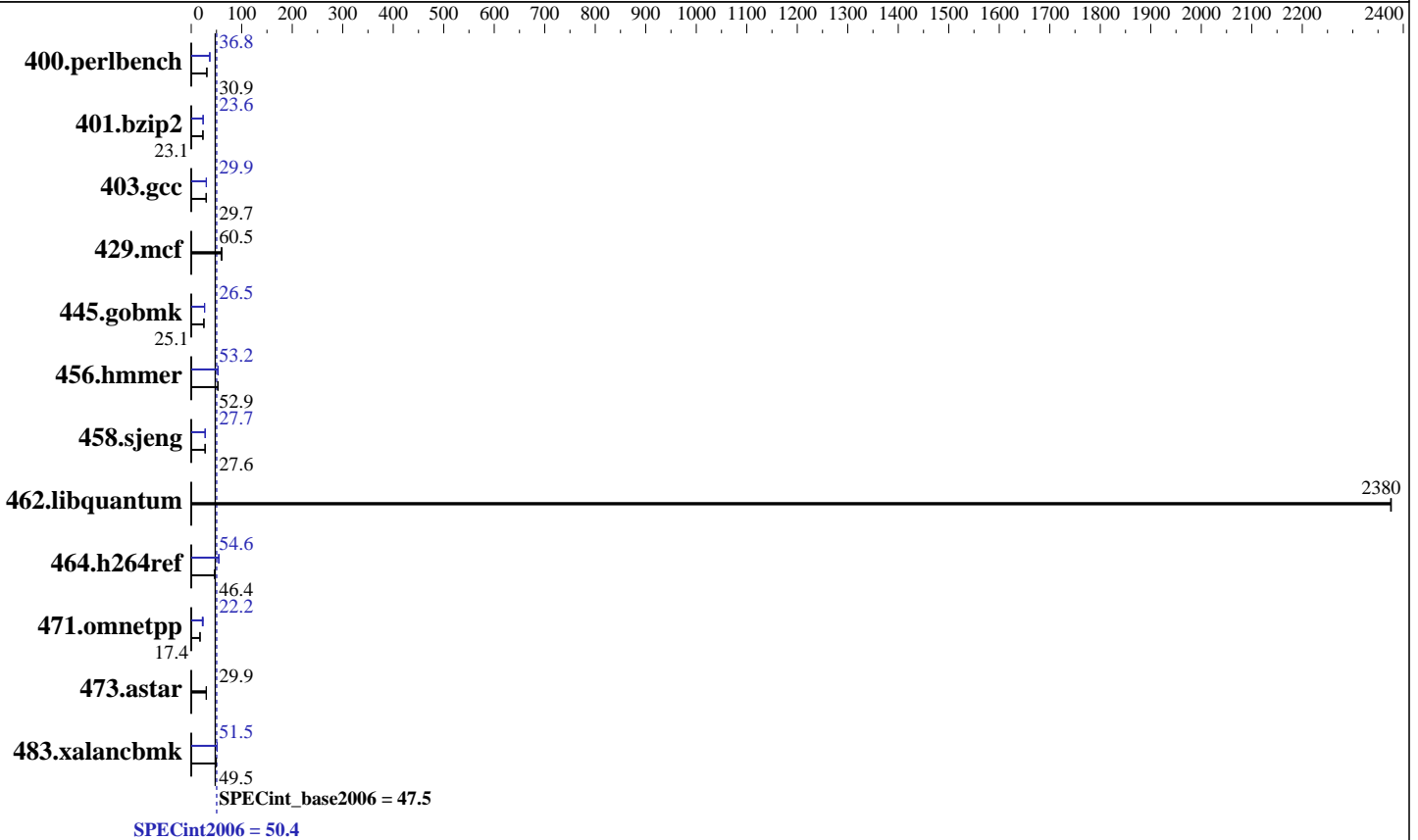
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2012

Hardware Availability: Mar-2012

Software Availability: Oct-2011



Hardware

CPU Name: Intel Xeon E5-2667
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2900
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 1 TB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)
 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1027GR-TSF (X9DRG-HF, Intel Xeon E5-2667, 2.9GHz)

SPECint2006 = 50.4

SPECint_base2006 = 47.5

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2012
Hardware Availability: Mar-2012
Software Availability: Oct-2011

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	316	31.0	316	30.9	317	30.9	265	36.8	266	36.8	266	36.7
401.bzip2	417	23.1	417	23.1	417	23.2	409	23.6	409	23.6	409	23.6
403.gcc	271	29.7	272	29.6	271	29.7	269	29.9	269	29.9	270	29.8
429.mcf	151	60.5	153	59.5	151	60.6	151	60.5	153	59.5	151	60.6
445.gobmk	418	25.1	419	25.0	418	25.1	397	26.4	397	26.5	397	26.5
456.hammer	176	52.9	176	52.9	176	52.9	176	52.9	175	53.2	175	53.2
458.sjeng	439	27.5	439	27.6	438	27.6	437	27.7	438	27.7	438	27.6
462.libquantum	8.72	2380	8.72	2380	8.72	2380	8.72	2380	8.72	2380	8.72	2380
464.h264ref	479	46.2	477	46.4	476	46.4	403	54.9	406	54.6	406	54.6
471.omnetpp	359	17.4	359	17.4	360	17.3	282	22.2	283	22.1	263	23.8
473.astar	235	29.9	235	29.9	235	29.8	235	29.9	235	29.9	235	29.8
483.xalancbmk	138	49.9	140	49.5	140	49.2	134	51.4	134	51.5	134	51.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Platform Notes

Sysinfo program /home/cpu2006/config/sysinfo.rev6800
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Sat Apr 28 07:46:26 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2667 0 @ 2.90GHz
2 "physical id"s (chips)
24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 12
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1027GR-TSF (X9DRG-HF, Intel Xeon E5-2667, 2.9GHz)

SPECint2006 = 50.4

SPECint_base2006 = 47.5

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2012

Hardware Availability: Mar-2012

Software Availability: Oct-2011

Platform Notes (Continued)

cache size : 15360 KB

From /proc/meminfo

MemTotal: 65949756 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

Red Hat Enterprise Linux Server release 6.1 (Santiago)

From /etc/*release* /etc/*version*

redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)

system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)

system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:

Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10 15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 27 02:13

SPEC is set to: /home/cpu2006

Filesystem Type Size Used Avail Use% Mounted on

/dev/mapper/VolGroup-lv_home

ext4 162G 104G 50G 68% /home

Additional information from dmidecode:

Memory:

8x Hynix Semiconducto HMT31GR7CFR4C 8 GB 1600 MHz 1 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

memory using RHEL5.5

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1027GR-TSF (X9DRG-HF, Intel Xeon E5-2667, 2.9GHz)

SPECint2006 = 50.4

SPECint_base2006 = 47.5

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2012

Hardware Availability: Mar-2012

Software Availability: Oct-2011

Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs -L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1027GR-TSF (X9DRG-HF, Intel Xeon E5-2667, 2.9GHz)

SPECint2006 = 50.4

SPECint_base2006 = 47.5

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2012

Hardware Availability: Mar-2012

Software Availability: Oct-2011

Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
           -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
         -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
           -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2)
             -opt-ra-region-strategy=block -ansi-alias
             -Wl,-z,muldefs -L/smartheap -lsmartheap

```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1027GR-TSF (X9DRG-HF, Intel Xeon E5-2667, 2.9GHz)

SPECint2006 = 50.4

SPECint_base2006 = 47.5

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2012

Hardware Availability: Mar-2012

Software Availability: Oct-2011

Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 04:39:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 June 2012.