



SPEC® CINT2006 Result

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Fujitsu

CELSIUS R920 (Intel Xeon E5-2643)

SPECint®2006 = 51.9

SPECint_base2006 = 48.8

CPU2006 license: 19

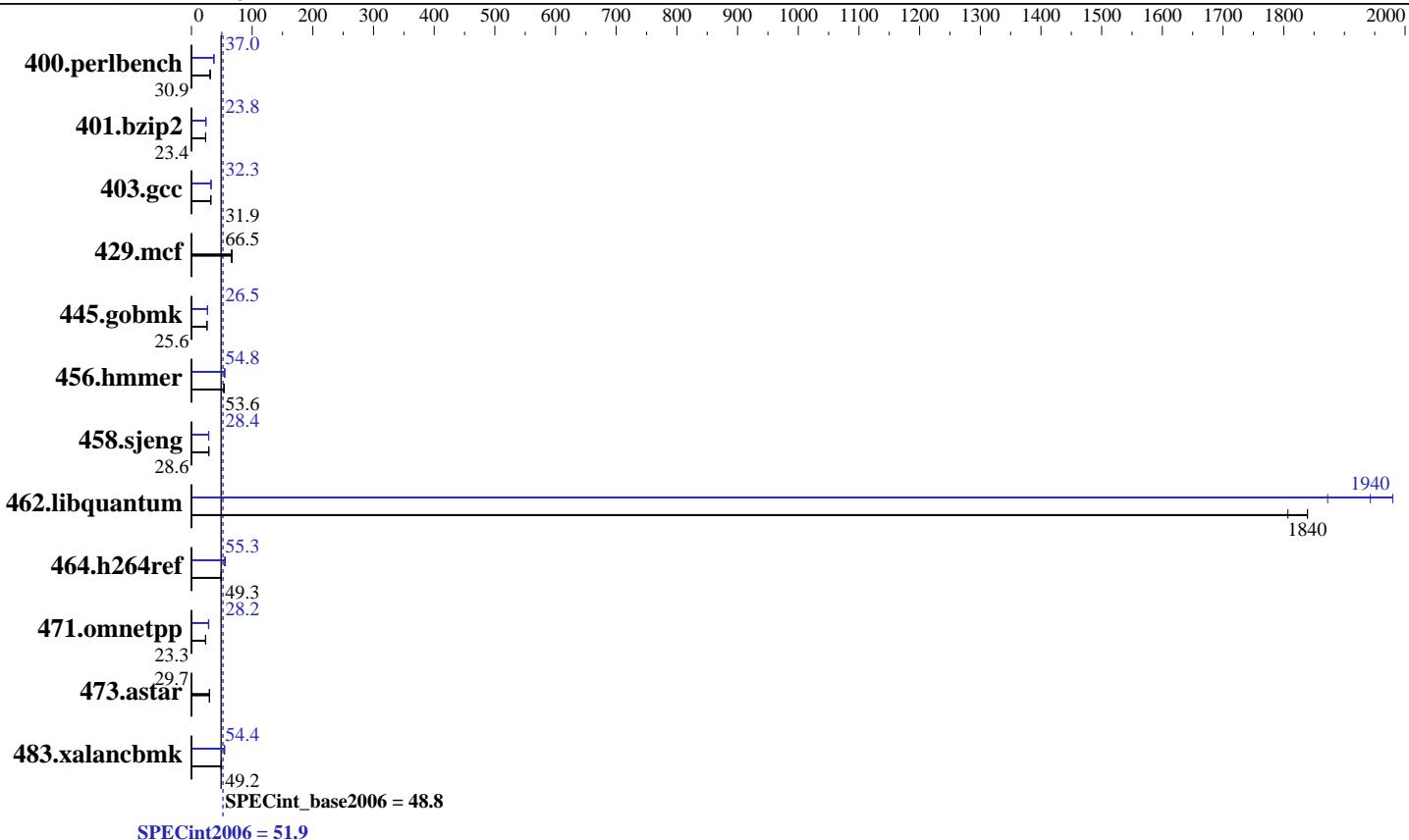
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



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Hardware

CPU Name: Intel Xeon E5-2643
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz: 3300
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 10 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx8 PC3-12800R-11, ECC)
Disk Subsystem: 1 x SATA III, 500 GB, 7200 rpm
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1, 2.6.32-131.0.15.el6.x86_64
Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi - user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap 10 (Multi-Core)



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	316	30.9	317	30.8	316	30.9	265	36.9	264	37.0	264	37.0
401.bzip2	413	23.4	414	23.3	413	23.4	406	23.8	406	23.8	407	23.7
403.gcc	253	31.9	252	31.9	253	31.9	249	32.3	249	32.3	249	32.3
429.mcf	137	66.6	137	66.5	139	65.8	137	66.6	137	66.5	139	65.8
445.gobmk	411	25.5	410	25.6	410	25.6	396	26.5	396	26.5	396	26.5
456.hmmer	174	53.6	174	53.6	174	53.6	171	54.6	170	54.8	170	54.9
458sjeng	423	28.6	423	28.6	423	28.6	426	28.4	425	28.4	425	28.4
462.libquantum	11.5	1810	11.3	1840	11.3	1840	11.1	1870	10.7	1940	10.5	1980
464.h264ref	455	48.7	449	49.3	448	49.4	400	55.3	405	54.6	399	55.5
471.omnetpp	267	23.4	269	23.3	269	23.3	219	28.6	222	28.1	222	28.2
473.astar	235	29.8	237	29.7	238	29.6	235	29.8	237	29.7	238	29.6
483.xalancbmk	139	49.5	141	49.0	140	49.2	126	54.7	127	54.4	127	54.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:

Hyper-Threading Technology = Disabled

Frequency Floor Override = Enabled

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/work/cpu2006/libs/32:/work/cpu2006/libs/64"

OMP_NUM_THREADS = "8"

Binaries compiled on a system with
2x Xeon E5-2690 CPU + 64 GB memory using
Red Hat Enterprise Linux Server release 6.1 (Santiago)

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enable



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/opt/SmartHeap/lib64 -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hammer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias`

456.hammer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32 -ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch
-auto-p32

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.xml>



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