



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Acer Incorporated

Gateway GW2000h-GW170h F1 (Intel Xeon X5650, 2.66GHz)

SPECint®\_rate2006 = 366

SPECint\_rate\_base2006 = 343

CPU2006 license: 97

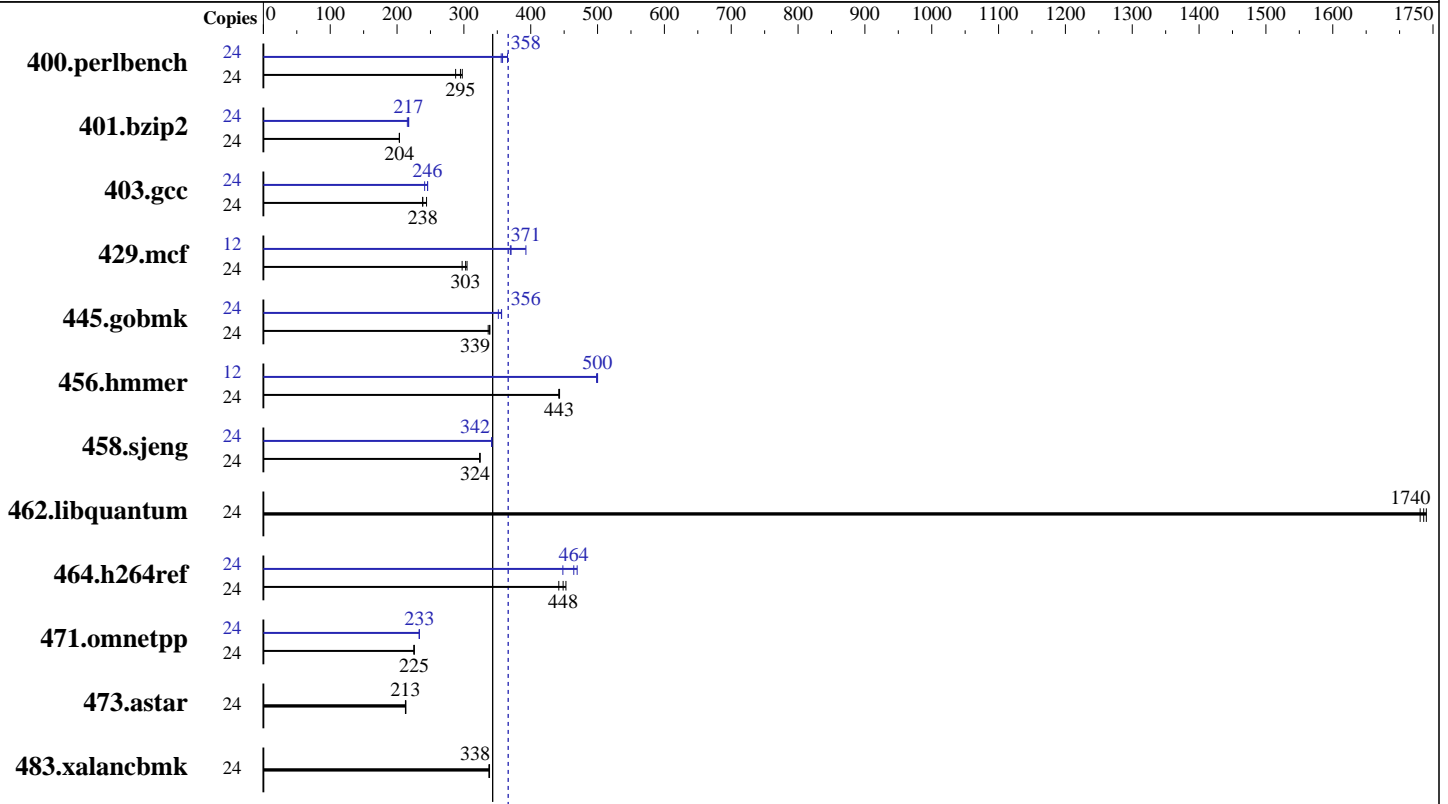
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Aug-2011

Hardware Availability: Aug-2010

Software Availability: Jan-2011



SPECint\_rate2006 = 366

SPECint\_rate\_base2006 = 343

### Hardware

CPU Name: Intel Xeon X5650  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 1000 GB SATA 7200RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Acer Incorporated

Gateway GW2000h-GW170h F1 (Intel Xeon X5650, 2.66GHz)

SPECint\_rate2006 = 366

SPECint\_rate\_base2006 = 343

CPU2006 license: 97  
Test sponsor: Acer Incorporated  
Tested by: Acer Incorporated

Test date: Aug-2011  
Hardware Availability: Aug-2010  
Software Availability: Jan-2011

## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	815	288	788	298	<u>795</u>	<u>295</u>	24	659	356	<u>655</u>	<u>358</u>	642	365
401.bzip2	24	1138	204	1138	204	<u>1138</u>	<u>204</u>	24	<u>1067</u>	<u>217</u>	1075	215	1065	217
403.gcc	24	791	244	811	238	<u>810</u>	<u>238</u>	24	<u>786</u>	<u>246</u>	786	246	801	241
429.mcf	24	736	297	<u>723</u>	<u>303</u>	718	305	12	296	370	<u>295</u>	<u>371</u>	279	393
445.gobmk	24	748	336	743	339	<u>743</u>	<u>339</u>	24	706	356	<u>707</u>	<u>356</u>	716	352
456.hammer	24	507	442	<u>506</u>	<u>443</u>	505	443	12	<u>224</u>	<u>500</u>	225	498	224	500
458.sjeng	24	896	324	897	324	<u>896</u>	<u>324</u>	24	850	342	850	342	<u>850</u>	<u>342</u>
462.libquantum	24	286	1740	287	1730	<u>286</u>	<u>1740</u>	24	286	1740	287	1730	<u>286</u>	<u>1740</u>
464.h264ref	24	1173	453	1202	442	<u>1184</u>	<u>448</u>	24	1131	470	<u>1144</u>	<u>464</u>	1185	448
471.omnetpp	24	665	226	666	225	<u>665</u>	<u>225</u>	24	643	233	643	233	<u>643</u>	<u>233</u>
473.astar	24	791	213	793	213	<u>792</u>	<u>213</u>	24	791	213	793	213	<u>792</u>	<u>213</u>
483.xalancbmk	24	491	337	489	338	<u>490</u>	<u>338</u>	24	491	337	489	338	<u>490</u>	<u>338</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
Large pages were disabled for this run

## Platform Notes

BIOS Settings:  
Fan speed = full speed (Default = Balanced)  
Data Reuse = Disabled (Default = Enabled)

## General Notes

Binaries compiled on RHEL5.5  
The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent. This result was measured on Gateway GW2000ht-GW170ht F1.

## Base Compiler Invocation

C benchmarks:  
icc -m32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Acer Incorporated**

Gateway GW2000h-GW170h F1 (Intel Xeon X5650, 2.66GHz)

**SPECint\_rate2006 = 366**

**SPECint\_rate\_base2006 = 343**

**CPU2006 license:** 97

**Test sponsor:** Acer Incorporated

**Tested by:** Acer Incorporated

**Test date:** Aug-2011

**Hardware Availability:** Aug-2010

**Software Availability:** Jan-2011

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Acer Incorporated

Gateway GW2000h-GW170h F1 (Intel Xeon X5650, 2.66GHz)

SPECint\_rate2006 = 366

SPECint\_rate\_base2006 = 343

**CPU2006 license:** 97  
**Test sponsor:** Acer Incorporated  
**Tested by:** Acer Incorporated

**Test date:** Aug-2011  
**Hardware Availability:** Aug-2010  
**Software Availability:** Jan-2011

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Acer Incorporated**

Gateway GW2000h-GW170h F1 (Intel Xeon X5650, 2.66GHz)

**SPECint\_rate2006 = 366**

**SPECint\_rate\_base2006 = 343**

**CPU2006 license:** 97

**Test sponsor:** Acer Incorporated

**Tested by:** Acer Incorporated

**Test date:** Aug-2011

**Hardware Availability:** Aug-2010

**Software Availability:** Jan-2011

## Peak Optimization Flags (Continued)

471.omnetpp (continued):  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 22:41:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 13 September 2011.