SPEC® CFP2006 Result

ASUSTeK Computer Inc.

(Test Sponsor: Intel Corporation)

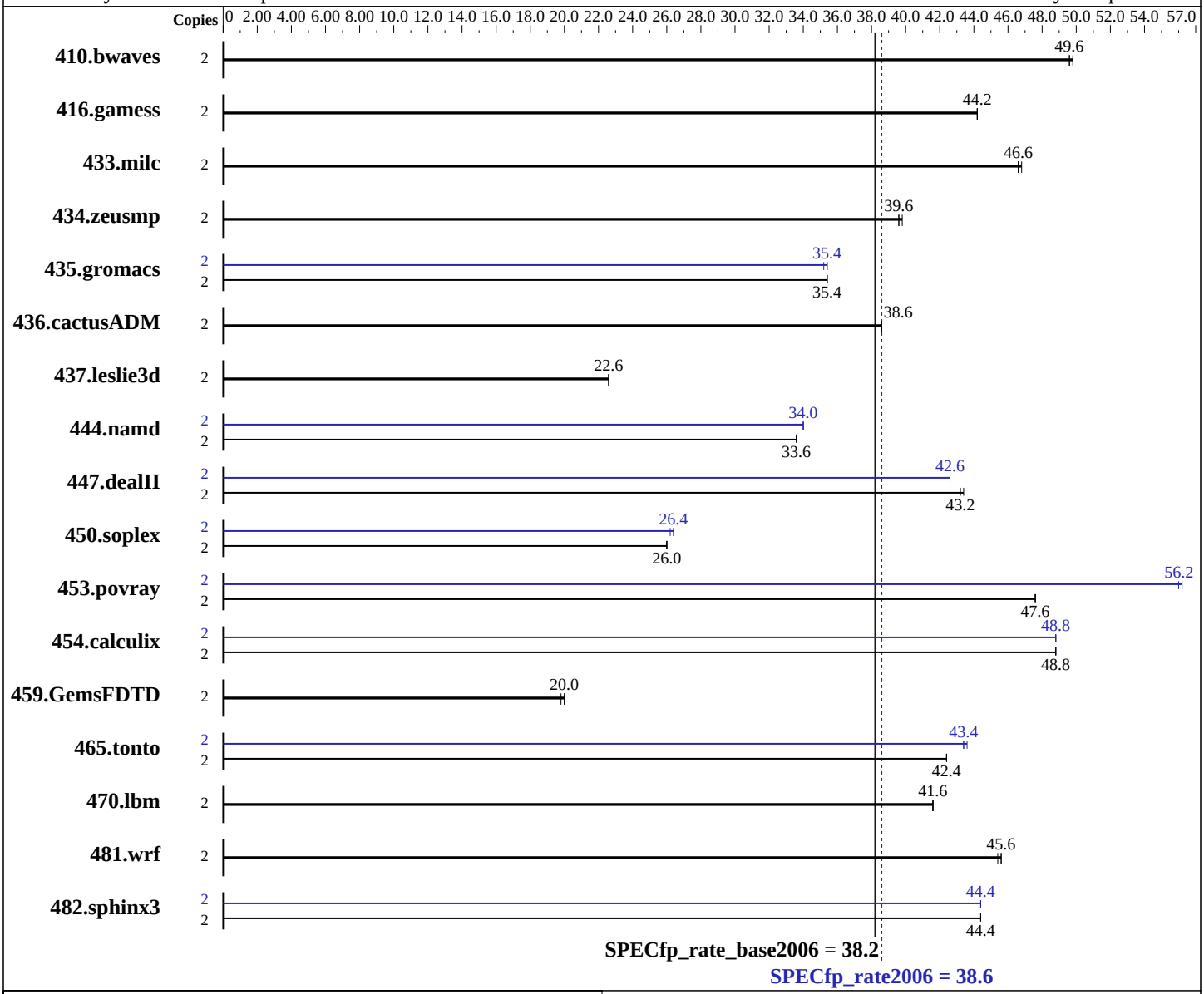
ASUSTek M4A89GTD PRO/USB3 (Phenom II X2 555)

SPECfp®_rate2006 = 38.6

SPECfp_rate_base2006 = 38.2

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2011
Hardware Availability: Feb-2011
Software Availability: Apr-2011



Hardware

CPU Name:	AMD Phenom II X2 555
CPU Characteristics:	
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

Continued on next page

Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Intel Visual Fortran Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS

Continued on next page

SPEC CFP2006 Result

ASUSTeK Computer Inc.
(Test Sponsor: Intel Corporation)

ASUSTek M4A89GTD PRO/USB3 (Phenom II X2 555)

SPECfp_rate2006 = 38.6

SPECfp_rate_base2006 = 38.2

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2011
Hardware Availability: Feb-2011
Software Availability: Apr-2011

L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (2 x 2 GB 2Rx4 PC3-10600U-9)
Disk Subsystem: 1 TB Seagate SATA, 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 9.01 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	546	49.8	**548**	**49.6**	548	49.6	2	546	49.8	**548**	**49.6**	548	49.6
416.gamess	2	886	44.2	884	44.2	**885**	**44.2**	2	886	44.2	884	44.2	**885**	**44.2**
433.milc	2	392	46.8	**394**	**46.6**	394	46.6	2	392	46.8	**394**	**46.6**	394	46.6
434.zeusmp	2	458	39.8	459	39.6	**459**	**39.6**	2	458	39.8	459	39.6	**459**	**39.6**
435.gromacs	2	403	35.4	403	35.4	**403**	**35.4**	2	405	35.4	**405**	**35.4**	405	35.2
436.cactusADM	2	619	38.6	620	38.6	**620**	**38.6**	2	619	38.6	620	38.6	**620**	**38.6**
437.leslie3d	2	**833**	**22.6**	833	22.6	833	22.6	2	**833**	**22.6**	833	22.6	833	22.6
444.namd	2	478	33.6	**478**	**33.6**	478	33.6	2	**473**	**34.0**	473	34.0	472	34.0
447.dealII	2	528	43.4	**529**	**43.2**	529	43.2	2	536	42.6	537	42.6	**536**	**42.6**
450.soplex	2	640	26.0	**641**	**26.0**	641	26.0	2	635	26.2	**634**	**26.4**	634	26.4
453.povray	2	224	47.6	**223**	**47.6**	223	47.6	2	190	56.0	190	56.2	**190**	**56.2**
454.calculix	2	338	48.8	**338**	**48.8**	338	48.8	2	339	48.8	338	48.8	**338**	**48.8**
459.GemsFDTD	2	1066	20.0	1067	19.8	**1066**	**20.0**	2	1066	20.0	1067	19.8	**1066**	**20.0**
465.tonto	2	465	42.4	464	42.4	**465**	**42.4**	2	453	43.4	**453**	**43.4**	452	43.6
470.lbm	2	661	41.6	662	41.6	**662**	**41.6**	2	661	41.6	662	41.6	**662**	**41.6**
481.wrf	2	491	45.6	491	45.4	**491**	**45.6**	2	491	45.6	491	45.4	**491**	**45.6**
482.sphinx3	2	877	44.4	877	44.4	**877**	**44.4**	2	878	44.4	**879**	**44.4**	880	44.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 The start command with the /affinity switch was used to bind processes to cores
```

General Notes

```
 Tested systems can be used with Shin-G ATX case,
 PC Power and Cooling 1200W power supply
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features
-Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: /arch:SSE3 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

447.dealII: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep- -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

450.soplex: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

453.povray: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

Continued on next page

Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

436.cactusADM: basepeak = yes

454.calculix: /arch:SSE3 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 22:27:11 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 September 2011.