



# SPEC® CINT2006 Result

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## Bull SAS

SPECint®2006 = 43.8

NovaScale R430 F2 (Intel Xeon X5675, 3.46 GHz)

SPECint\_base2006 = 42.3

CPU2006 license: 20

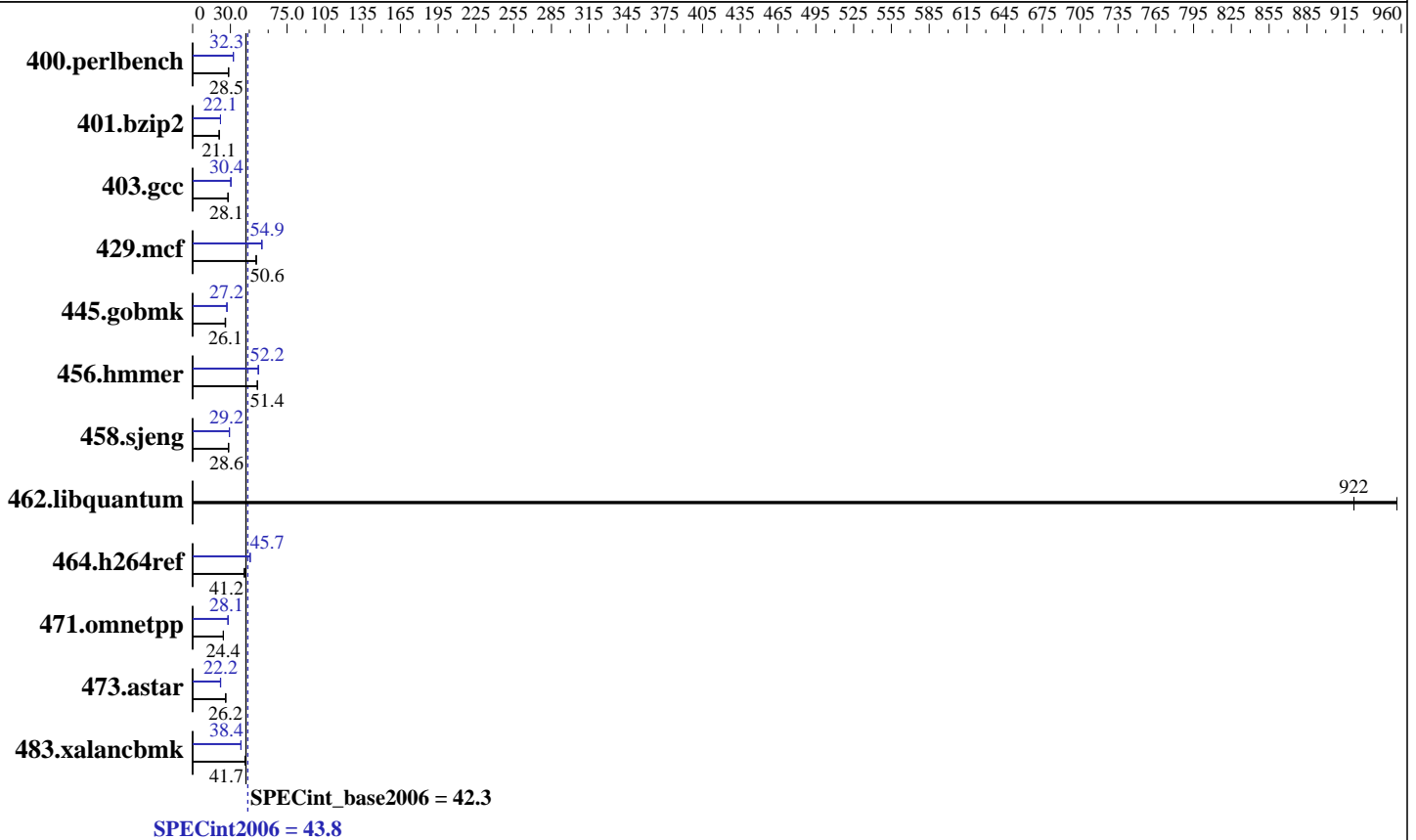
Test date: Mar-2011

Test sponsor: Bull SAS

Hardware Availability: Feb-2011

Tested by: Dell Inc.

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Xeon X5675  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
 CPU MHz: 3067  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	342	28.5	343	28.5	<b>342</b>	<b>28.5</b>	<b>302</b>	<b>32.3</b>	302	32.4	302	32.3
401.bzip2	457	21.1	<b>457</b>	<b>21.1</b>	457	21.1	436	22.1	436	22.1	<b>436</b>	<b>22.1</b>
403.gcc	285	28.3	<b>287</b>	<b>28.1</b>	287	28.0	265	30.4	265	30.4	<b>265</b>	<b>30.4</b>
429.mcf	180	50.6	181	50.3	<b>180</b>	<b>50.6</b>	<b>166</b>	<b>54.9</b>	166	54.8	165	55.1
445.gobmk	407	25.8	401	26.1	<b>402</b>	<b>26.1</b>	<b>385</b>	<b>27.2</b>	385	27.3	385	27.2
456.hmmer	181	51.4	<b>181</b>	<b>51.4</b>	181	51.4	<b>179</b>	<b>52.2</b>	179	52.2	179	52.2
458.sjeng	423	28.6	426	28.4	<b>424</b>	<b>28.6</b>	415	29.2	<b>414</b>	<b>29.2</b>	414	29.2
462.libquantum	21.7	956	22.5	922	<b>22.5</b>	<b>922</b>	21.7	956	22.5	922	<b>22.5</b>	<b>922</b>
464.h264ref	541	40.9	536	41.3	<b>537</b>	<b>41.2</b>	484	45.7	486	45.5	<b>484</b>	<b>45.7</b>
471.omnetpp	256	24.4	<b>256</b>	<b>24.4</b>	257	24.3	222	28.1	222	28.1	<b>222</b>	<b>28.1</b>
473.astar	267	26.3	<b>268</b>	<b>26.2</b>	268	26.2	<b>316</b>	<b>22.2</b>	316	22.2	318	22.1
483.xalancbmk	<b>166</b>	<b>41.7</b>	165	41.8	166	41.7	<b>180</b>	<b>38.4</b>	180	38.4	180	38.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
```

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)  
Data Reuse = Disabled (Default = Enabled)  
Logical Processor = Disabled (Default = Enabled)

## General Notes

OMP\_NUM\_THREADS set to number of cores  
Binaries were compiled on RHEL5.5  
The Dell PowerEdge R410 and  
the Bull NovaScale R430 F2 models are electronically equivalent.  
The results have been measured on a Dell PowerEdge R410 model.

## Base Compiler Invocation

C benchmarks:  
icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap64  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32

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## Peak Compiler Invocation (Continued)

429.mcf: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

456.hammer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

429.mcf: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

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## Peak Optimization Flags (Continued)

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/smartheap -lsmartheap64

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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