



# SPEC® CINT2006 Result

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Fujitsu

CELSIUS W410, Intel Core i5-2400

**SPECint®2006 = 42.2**

**SPECint\_base2006 = 40.6**

CPU2006 license: 19

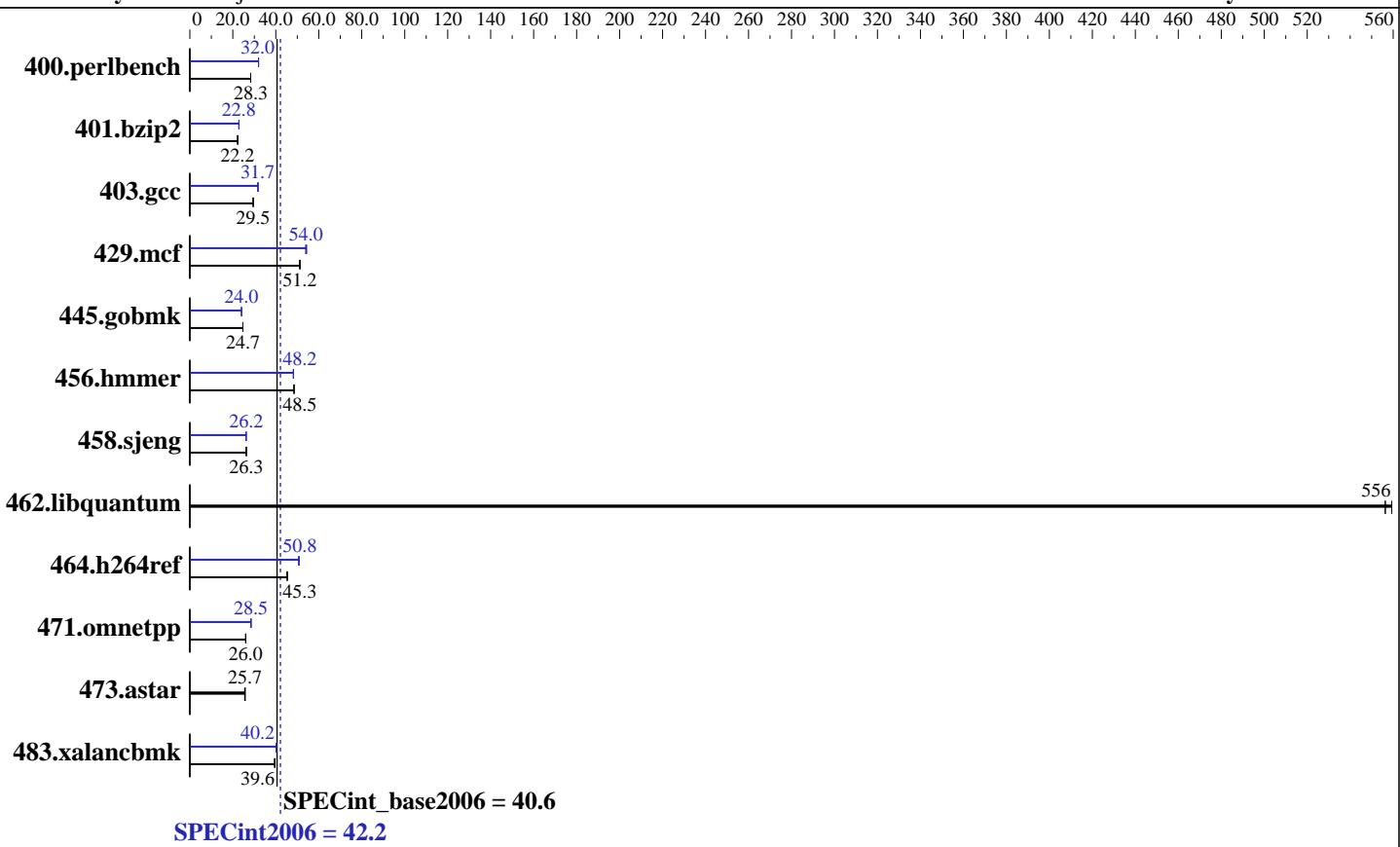
Test sponsor: Fujitsu

Tested by: Fujitsu

**Test date:** Jun-2011

**Hardware Availability:** Mar-2011

**Software Availability:** Jan-2011



## Hardware

CPU Name: Intel Core i5-2400  
CPU Characteristics: Intel Turbo Boost Technology up to 3.4 GHz  
CPU MHz: 3100  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 6 MB I+D on chip per chip  
Other Cache: None  
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)  
Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm  
Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, kernel 2.6.32.12-0.7-default  
Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.2.137 Build 20110112  
Auto Parallel: Yes  
File System: ext3  
System State: Run Level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>345</b>	<b>28.3</b>	345	28.3	345	28.3	<b>305</b>	<b>32.0</b>	<b>306</b>	<b>31.9</b>	<b>306</b>	<b>32.0</b>
401.bzip2	<b>434</b>	<b>22.2</b>	434	22.2	433	22.3	<b>421</b>	<b>22.9</b>	<b>423</b>	<b>22.8</b>	423	22.8
403.gcc	273	29.5	<b>273</b>	<b>29.5</b>	273	29.5	<b>253</b>	<b>31.8</b>	254	31.6	<b>254</b>	<b>31.7</b>
429.mcf	179	51.1	<b>178</b>	<b>51.2</b>	178	51.3	<b>168</b>	<b>54.4</b>	<b>169</b>	<b>54.0</b>	169	54.0
445.gobmk	425	24.7	<b>425</b>	<b>24.7</b>	425	24.7	<b>436</b>	<b>24.1</b>	436	24.0	<b>436</b>	<b>24.0</b>
456.hmmer	192	48.5	<b>192</b>	<b>48.5</b>	192	48.5	<b>194</b>	<b>48.2</b>	194	48.2	<b>194</b>	<b>48.2</b>
458.sjeng	461	26.3	460	26.3	<b>461</b>	<b>26.3</b>	<b>462</b>	<b>26.2</b>	<b>462</b>	<b>26.2</b>	<b>462</b>	<b>26.2</b>
462.libquantum	37.0	559	<b>37.2</b>	<b>556</b>	37.2	556	<b>37.0</b>	<b>559</b>	<b>37.2</b>	<b>556</b>	37.2	556
464.h264ref	<b>489</b>	<b>45.3</b>	487	45.5	489	45.2	<b>437</b>	<b>50.7</b>	435	50.9	<b>436</b>	<b>50.8</b>
471.omnetpp	241	25.9	<b>240</b>	<b>26.0</b>	240	26.1	<b>220</b>	<b>28.5</b>	<b>220</b>	<b>28.5</b>	220	28.4
473.astar	275	25.5	<b>274</b>	<b>25.7</b>	272	25.8	<b>275</b>	<b>25.5</b>	<b>274</b>	<b>25.7</b>	272	25.8
483.xalancbmk	<b>174</b>	<b>39.6</b>	175	39.3	174	39.6	<b>171</b>	<b>40.3</b>	172	40.2	<b>171</b>	<b>40.2</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run  
Hugepages were enabled by:

```
mount -t hugetlbfs nodev /mnt/hugepages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## General Notes

OMP\_NUM\_THREADS set to number of cores

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

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## Base Portability Flags (Continued)

```
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
-B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/smartheap/intel64 -lsmartheap64
-B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32
```

```
429.mcf: icc -m32
```

```
445.gobmk: icc -m32
```

```
464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m32
```

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## Peak Compiler Invocation (Continued)

473.astar: icpc -m64

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
    -ansi-alias
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
    -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
    -opt-malloc-options=3 -auto-ilp32
    -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
    -ansi-alias
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
    -auto-ilp32 -ansi-alias
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
    -ansi-alias
    -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
```

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

```
464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
              -ansi-alias
              -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs
              -L/opt/smartheap/ia32 -lsmartheap
              -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

473.astar: basepeak = yes

```
483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
                 -Wl,-z,muldefs -L/opt/smartheap/ia32 -lsmartheap
                 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>  
[http://www.spec.org/cpu2006/flags/Fujitsu\\_CELSIUS\\_Platform.20110720.html](http://www.spec.org/cpu2006/flags/Fujitsu_CELSIUS_Platform.20110720.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>  
[http://www.spec.org/cpu2006/flags/Fujitsu\\_CELSIUS\\_Platform.20110720.xml](http://www.spec.org/cpu2006/flags/Fujitsu_CELSIUS_Platform.20110720.xml)

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