SPEC® CINT2006 Result
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SPECint®_rate2006 = 243

IBM BladeCenter HX5 (Intel Xeon E7530)

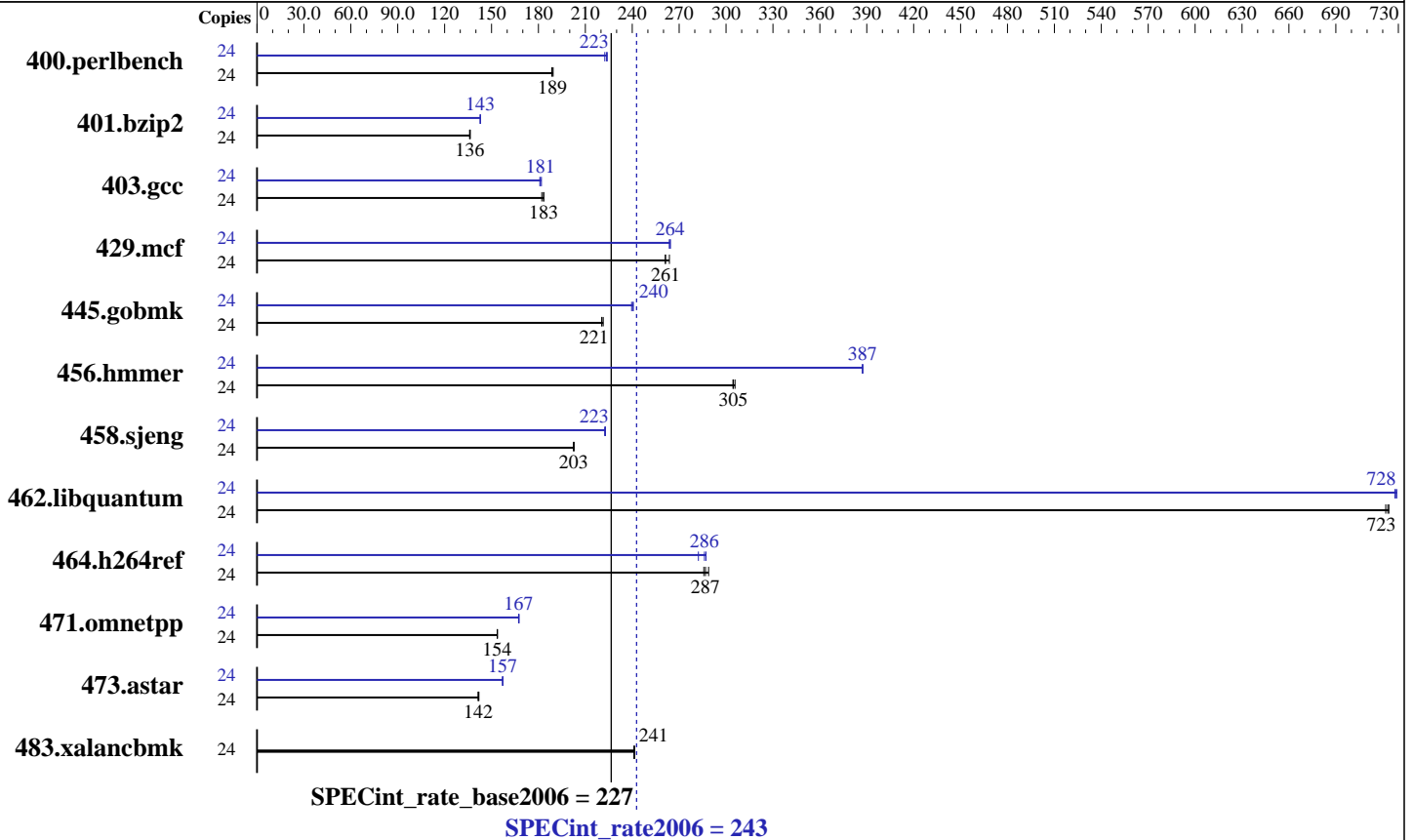SPECint_rate_base2006 = 227

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jun-2010

Hardware Availability: Jun-2010

Software Availability: Jan-2010

Benchmark results chart (Copies = 24 for all):

- **400.perlbench**: 223 / 189
- **401.bzip2**: 143 / 136
- **403.gcc**: 181 / 183
- **429.mcf**: 264 / 261
- **445.gobmk**: 240 / 221
- **456.hmmer**: 387 / 305
- **458.sjeng**: 223 / 203
- **462.libquantum**: 728 / 723
- **464.h264ref**: 286 / 287
- **471.omnetpp**: 167 / 154
- **473.astar**: 157 / 142
- **483.xalancbmk**: 241

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Hardware

CPU Name:	Intel Xeon E7530
CPU Characteristics:	Intel Turbo Boost Technology up to 2.13 GHz
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (16 x 8 GB PC3-8500R CL7, Quad Rank, running at 978 MHz)
Disk Subsystem:	2 x 50 GB SATA, SSD
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	1244	189	**1241**	**189**	1238	189	24	1054	222	1047	224	**1049**	**223**
401.bzip2	24	1704	136	1698	136	**1700**	**136**	24	**1622**	**143**	1623	143	1620	143
403.gcc	24	1053	183	**1054**	**183**	1061	182	24	1068	181	**1066**	**181**	1062	182
429.mcf	24	838	261	830	264	**838**	**261**	24	828	264	**828**	**264**	830	264
445.gobmk	24	1137	221	**1141**	**221**	1142	221	24	1051	240	**1048**	**240**	1046	241
456.hmmer	24	**735**	**305**	732	306	736	304	24	578	387	578	387	**578**	**387**
458.sjeng	24	**1434**	**203**	1435	202	1432	203	24	1306	222	1303	223	**1303**	**223**
462.libquantum	24	**688**	**723**	687	724	689	722	24	**683**	**728**	683	728	682	729
464.h264ref	24	1839	289	**1852**	**287**	1859	286	24	1881	282	1849	287	**1856**	**286**
471.omnetpp	24	**976**	**154**	976	154	976	154	24	897	167	895	168	**896**	**167**
473.astar	24	1192	141	1187	142	**1190**	**142**	24	1073	157	1071	157	**1072**	**157**
483.xalancbmk	24	686	241	**686**	**241**	687	241	24	686	241	**686**	**241**	687	241

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Platform Notes

     Turbo Boost set to Traditional

General Notes

     'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
     Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
```
icc -m32
```

C++ benchmarks:
```
icpc -m32
```

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Base Portability Flags

        400.perlbench: `-DSPEC_CPU_LINUX_IA32`
        462.libquantum: `-DSPEC_CPU_LINUX`
        483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

        `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:

        `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
        `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

Base Other Flags

C benchmarks:

        403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
        `icc  -m32`

        401.bzip2: `icc -m64`

        456.hmmer: `icc -m64`

        458.sjeng: `icc -m64`

        462.libquantum: `icc -m64`

C++ benchmarks (except as noted below):
        `icpc -m32`

        473.astar: `icpc -m64`

Peak Portability Flags

        400.perlbench: `-DSPEC_CPU_LINUX_IA32`
            401.bzip2: `-DSPEC_CPU_LP64`

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CPU2006 license: 11   **Test date:** Jun-2010
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Peak Portability Flags (Continued)

```
      456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
      473.astar: -DSPEC_CPU_LP64
  483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -ansi-alias

     401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

       403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

       429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

     445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                 -ipo -no-prec-div -ansi-alias

     456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                 -ansi-alias -auto-ilp32

     458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll4 -auto-ilp32

  462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                 -opt-prefetch

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

      473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
```

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Peak Optimization Flags (Continued)

473.astar (continued):
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100603.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100603.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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