



SPEC® CFP2006 Result

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Supermicro

SPECfp®_rate2006 = 197

Motherboard X8DTN+ (Intel Xeon L5640, 2.26 GHz)

SPECfp_rate_base2006 = 190

CPU2006 license: 001176

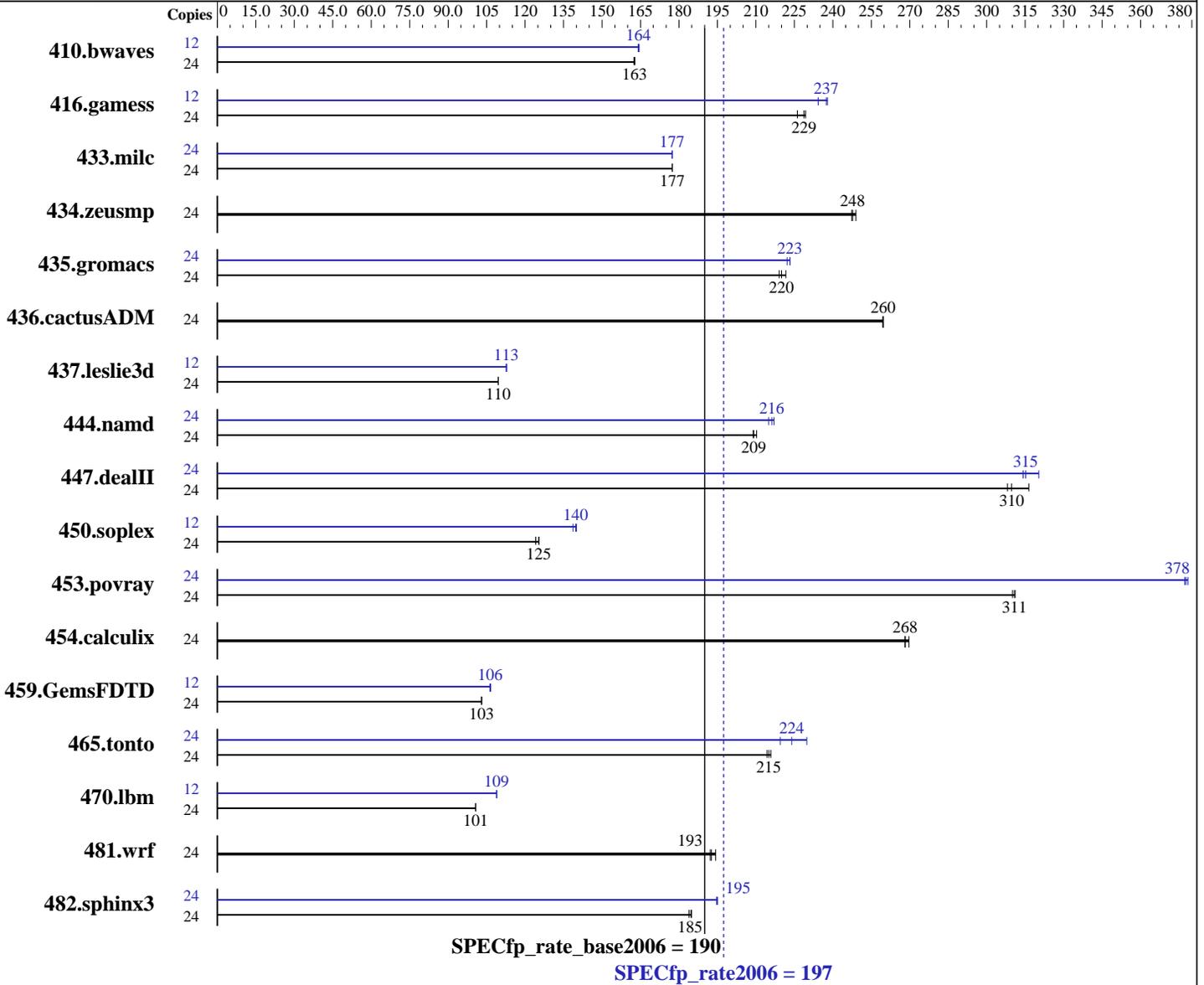
Test date: Mar-2010

Test sponsor: Supermicro

Hardware Availability: Mar-2010

Tested by: Supermicro

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon L5640
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB DDR3-1066 RDIMM, ECC, CL7)
Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|------------|-------------|------------|-------------|------------|--------|-------------|------------|-------------|------------|-------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 24 | 2008 | 162 | 2004 | 163 | 2005 | 163 | 12 | 994 | 164 | 991 | 164 | 993 | 164 |
| 416.gamess | 24 | 2049 | 229 | 2055 | 229 | 2078 | 226 | 12 | 990 | 237 | 1003 | 234 | 987 | 238 |
| 433.milc | 24 | 1242 | 177 | 1242 | 177 | 1242 | 177 | 24 | 1242 | 177 | 1242 | 177 | 1242 | 177 |
| 434.zeusmp | 24 | 877 | 249 | 882 | 248 | 883 | 247 | 24 | 877 | 249 | 882 | 248 | 883 | 247 |
| 435.gromacs | 24 | 779 | 220 | 773 | 222 | 782 | 219 | 24 | 768 | 223 | 771 | 222 | 767 | 223 |
| 436.cactusADM | 24 | 1104 | 260 | 1105 | 259 | 1104 | 260 | 24 | 1104 | 260 | 1105 | 259 | 1104 | 260 |
| 437.leslie3d | 24 | 2060 | 110 | 2059 | 110 | 2060 | 109 | 12 | 1001 | 113 | 1001 | 113 | 999 | 113 |
| 444.namd | 24 | 921 | 209 | 915 | 210 | 920 | 209 | 24 | 895 | 215 | 890 | 216 | 887 | 217 |
| 447.dealII | 24 | 868 | 316 | 886 | 310 | 891 | 308 | 24 | 857 | 320 | 871 | 315 | 874 | 314 |
| 450.soplex | 24 | 1612 | 124 | 1596 | 125 | 1597 | 125 | 12 | 722 | 139 | 715 | 140 | 716 | 140 |
| 453.povray | 24 | 412 | 310 | 411 | 311 | 410 | 311 | 24 | 337 | 378 | 338 | 377 | 338 | 378 |
| 454.calculix | 24 | 738 | 268 | 738 | 268 | 734 | 270 | 24 | 738 | 268 | 738 | 268 | 734 | 270 |
| 459.GemsFDTD | 24 | 2474 | 103 | 2471 | 103 | 2469 | 103 | 12 | 1195 | 107 | 1196 | 106 | 1198 | 106 |
| 465.tonto | 24 | 1102 | 214 | 1094 | 216 | 1098 | 215 | 24 | 1054 | 224 | 1027 | 230 | 1076 | 219 |
| 470.lbm | 24 | 3272 | 101 | 3273 | 101 | 3276 | 101 | 12 | 1513 | 109 | 1515 | 109 | 1515 | 109 |
| 481.wrf | 24 | 1380 | 194 | 1395 | 192 | 1392 | 193 | 24 | 1380 | 194 | 1395 | 192 | 1392 | 193 |
| 482.sphinx3 | 24 | 2543 | 184 | 2530 | 185 | 2533 | 185 | 24 | 2398 | 195 | 2402 | 195 | 2402 | 195 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

Fan speed set to Full Speed in BIOS Setup.
As tested, the system used a Supermicro
PWS-865-PQ power supply, 2 SNK-P0038P heatsinks,
and 2 FAN-0077L cooling fans.



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General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 08:01:01 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 4 May 2010.