SPEC® CINT2006 Result
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Intel Corporation

Supermicro SuperServer 7046A-T (Intel Xeon X5680, 3.33 GHz)

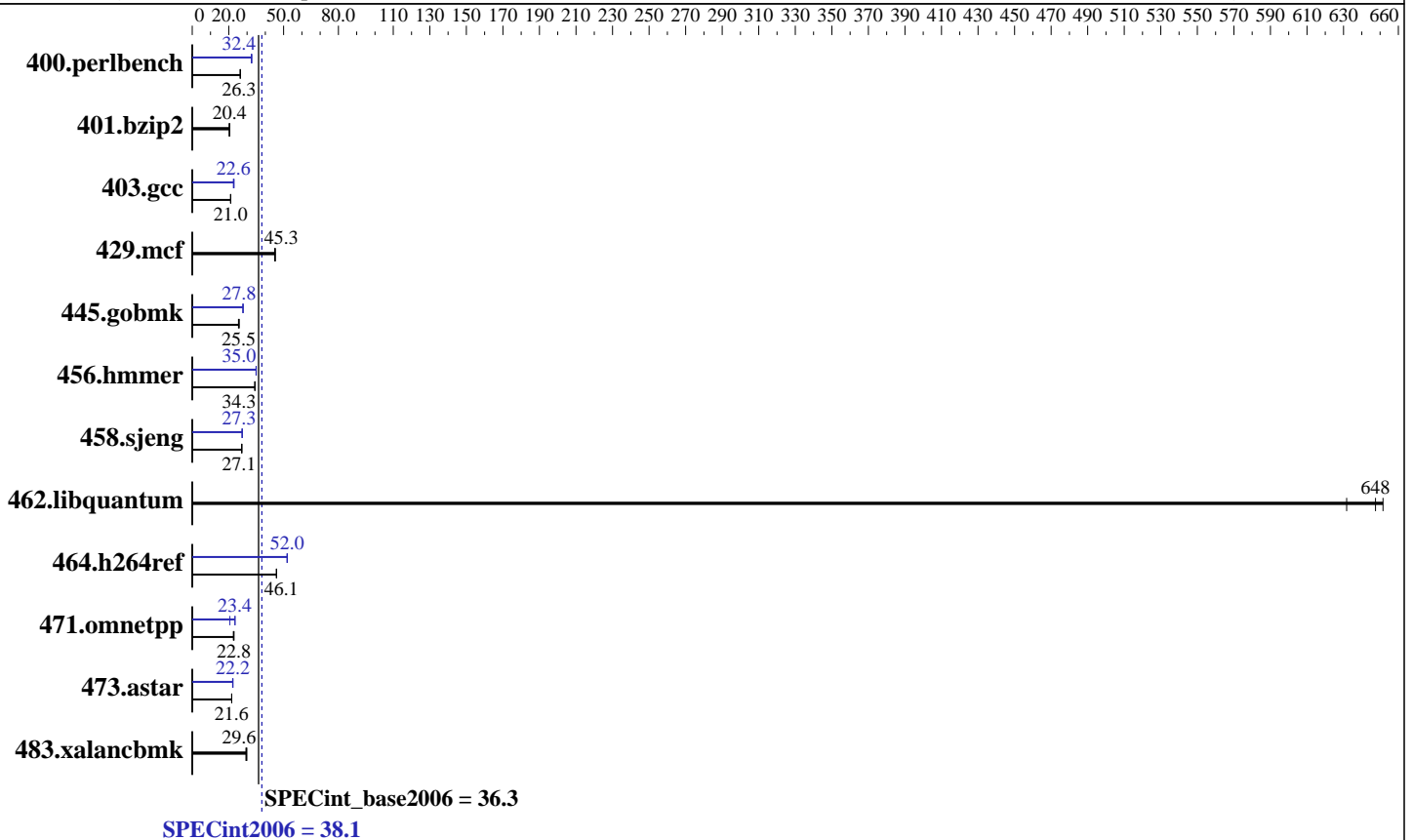SPECint®2006 = 38.1

SPECint_base2006 = 36.3

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Mar-2010
Hardware Availability: Mar-2010
Software Availability: Nov-2009

SPECint_base2006 = 36.3

SPECint2006 = 38.1

Benchmark	base	peak
400.perlbench	26.3	32.4
401.bzip2	20.4	
403.gcc	21.0	22.6
429.mcf	45.3	
445.gobmk	25.5	27.8
456.hmmer	34.3	35.0
458.sjeng	27.1	27.3
462.libquantum	648	
464.h264ref	46.1	52.0
471.omnetpp	22.8	23.4
473.astar	21.6	22.2
483.xalancbmk	29.6	

Hardware

CPU Name: Intel Xeon X5680
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 3333
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6x 4GB DDR3-1333, ECC, CL9)
Disk Subsystem: 1 x 300 GB SATA, 10000 RPM
Other Hardware: None

Software

Operating System: Windows 7 Enterprise (64-bit)
Compiler: Intel C++ Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cproc_p_11.1.045 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel: Yes
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	371	26.3	**372**	**26.3**	372	26.3	300	32.6	**301**	**32.4**	302	32.4
401.bzip2	471	20.5	**473**	**20.4**	477	20.2	471	20.5	**473**	**20.4**	477	20.2
403.gcc	**383**	**21.0**	382	21.1	383	21.0	352	22.9	**356**	**22.6**	356	22.6
429.mcf	**201**	**45.3**	202	45.1	201	45.5	**201**	**45.3**	202	45.1	201	45.5
445.gobmk	**411**	**25.5**	408	25.7	414	25.3	377	27.8	**378**	**27.8**	378	27.8
456.hmmer	272	34.3	**272**	**34.3**	272	34.3	267	35.0	**267**	**35.0**	267	34.9
458.sjeng	**447**	**27.1**	447	27.1	446	27.1	442	27.4	443	27.3	**443**	**27.3**
462.libquantum	31.8	652	32.8	632	**32.0**	**648**	31.8	652	32.8	632	**32.0**	**648**
464.h264ref	480	46.1	480	46.1	**480**	**46.1**	426	**52.0**	425	52.0	426	51.9
471.omnetpp	**274**	**22.8**	274	22.8	278	22.5	267	23.4	**268**	**23.4**	304	20.6
473.astar	325	21.6	**325**	**21.6**	326	21.6	315	22.3	317	22.1	**317**	**22.2**
483.xalancbmk	235	29.4	231	29.9	**233**	**29.6**	235	29.4	231	29.9	**233**	**29.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
    Tested systems is used with tower case,
    PC Power and Cooling dual 865W power supplys
    OMP_NUM_THREADS set to number of processors cores
    KMP_AFFINITY set to granularity=fine,scatter
    System was configured with nVidia Quadro FX 370 discrete graphics card
```

Base Compiler Invocation

C benchmarks:
```
    icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
    icl -Qvc9
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
                   -DSPEC_CPU_NO_NEED_VA_COPY
       401.bzip2: -DSPEC_CPU_P64
         403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
         429.mcf: -DSPEC_CPU_P64
       445.gobmk: -DSPEC_CPU_P64
       456.hmmer: -DSPEC_CPU_P64
```

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Base Portability Flags (Continued)

```
      458.sjeng: -DSPEC_CPU_P64
  462.libquantum: -DSPEC_CPU_P64
     464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
     471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
       473.astar: -DSPEC_CPU_P64
   483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:

```
    -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
    -Qauto-ilp32 /F512000000
```

C++ benchmarks:

```
    -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
    -Qauto-ilp32 /F512000000 shlW64M.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
    icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
    icl -Qvc9
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

```
 400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
                -Qauto-ilp32 /F512000000 shlW64M.lib
                          -link /FORCE:MULTIPLE

      401.bzip2: basepeak = yes

        403.gcc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F512000000

       429.mcf: basepeak = yes

     445.gobmk: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                -Qipo -O2 -Qprec-div- -Qansi-alias -Qauto-ilp32
                /F512000000

     456.hmmer: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
                -Qauto-ilp32 /F512000000

     458.sjeng: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto-ilp32 /F512000000

 462.libquantum: basepeak = yes

    464.h264ref: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
                 -Qauto-ilp32 /F512000000
```

C++ benchmarks:

```
    471.omnetpp: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                 -Qipo -O3 -Qprec-div- -Qansi-alias
                 -Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
                 shlW64M.lib             -link /FORCE:MULTIPLE

      473.astar: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                 -Qipo -O3 -Qprec-div- -Qansi-alias
                 -Qopt-ra-region-strategy=routine -Qauto-ilp32 /F512000000
                 shlW64M.lib             -link /FORCE:MULTIPLE

   483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

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The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.xml

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