SPEC® CINT2006 Result
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Fujitsu

PRIMERGY TX300 S6, Intel Xeon L5630, 2.13 GHz

$SPECint®\_rate2006 =$ 193

$SPECint\_rate\_base2006 =$ 181

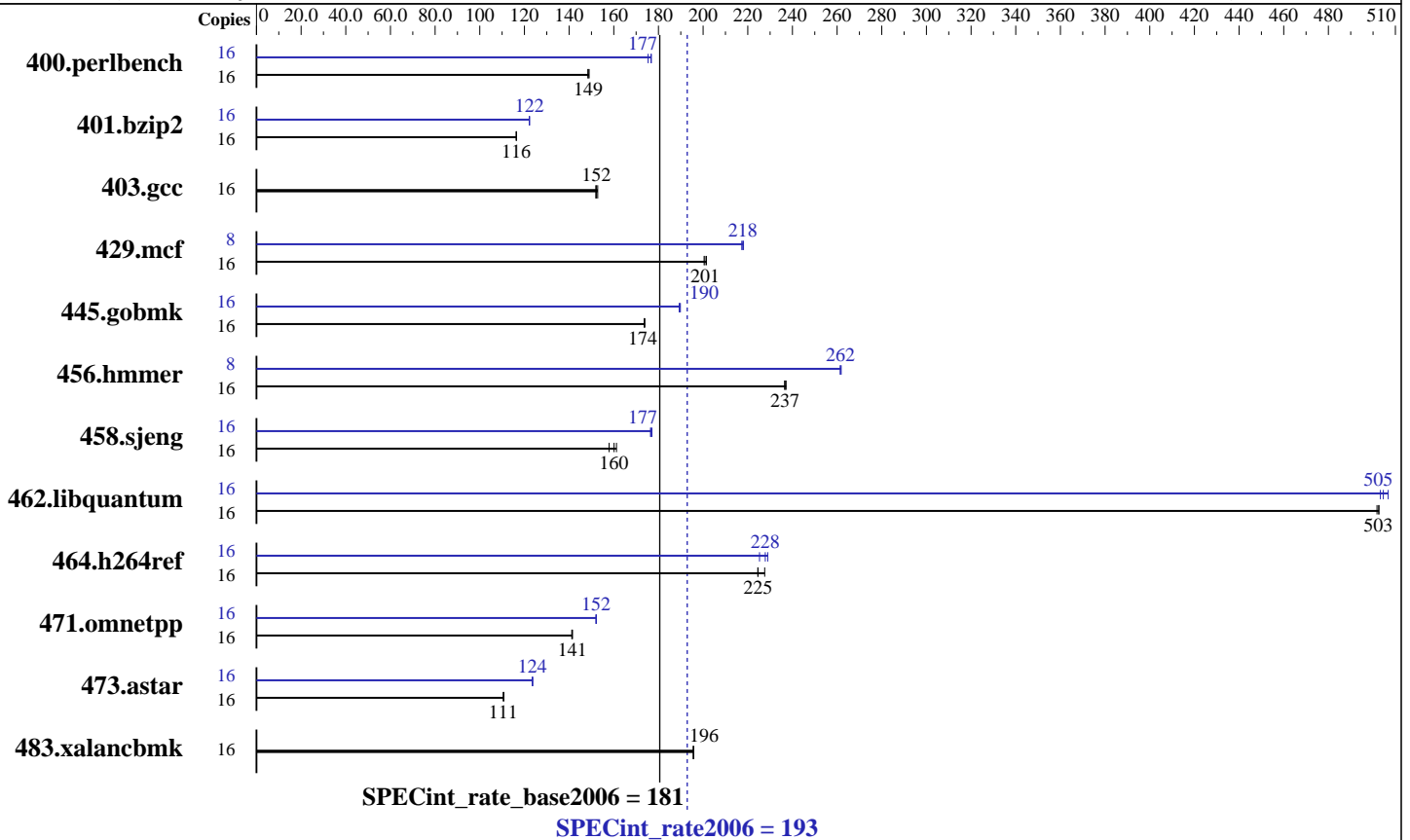**CPU2006 license:** 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010

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Hardware

CPU Name:	Intel Xeon L5630
CPU Characteristics:	Intel Turbo Boost Technology up to 2.40 GHz
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem:	1 x SATA, 160 GB, 5.4 krpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1049	149	1053	148	**1052**	**149**	16	**885**	**177**	884	177	891	175
401.bzip2	16	**1327**	**116**	1326	116	1327	116	16	1263	122	**1263**	**122**	1264	122
403.gcc	16	848	152	**846**	**152**	843	153	16	848	152	**846**	**152**	843	153
429.mcf	16	**727**	**201**	728	200	724	201	8	336	217	335	218	**335**	**218**
445.gobmk	16	967	174	965	174	**966**	**174**	16	887	189	884	190	**886**	**190**
456.hmmer	16	629	237	631	236	**631**	**237**	8	285	262	**285**	**262**	286	261
458.sjeng	16	1201	161	1226	158	**1209**	**160**	16	**1096**	**177**	1097	176	1093	177
462.libquantum	16	**660**	**503**	661	502	660	503	16	**657**	**505**	654	507	659	503
464.h264ref	16	**1576**	**225**	1577	225	1556	228	16	**1554**	**228**	1572	225	1547	229
471.omnetpp	16	708	141	707	141	**707**	**141**	16	658	152	**658**	**152**	657	152
473.astar	16	**1016**	**111**	1014	111	1016	111	16	**909**	**124**	910	123	908	124
483.xalancbmk	16	**564**	**196**	564	196	564	196	16	**564**	**196**	564	196	564	196

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    The system automatically configures the memory to run at 1066 MHz.
    BIOS configuration:
    Data Reuse Optimization = Disable

General Notes

    This result was measured on the PRIMERGY RX300 S6. The PRIMERGY RX300 S6
    and the PRIMERGY TX300 S6 are electronically equivalent.

    For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
       icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

    462.libquantum: icc -m64

C++ benchmarks (except as noted below):
    icpc -m32

    473.astar: icpc -m64

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Peak Portability Flags

```
       400.perlbench: -DSPEC_CPU_LINUX_IA32
          401.bzip2: -DSPEC_CPU_LP64
          456.hmmer: -DSPEC_CPU_LP64
           458.sjeng: -DSPEC_CPU_LP64
       462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
           473.astar: -DSPEC_CPU_LP64
       483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
       400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                       -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                       -prof-use(pass 2) -ansi-alias

          401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                       -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                       -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

            403.gcc: basepeak = yes

            429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

          445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                       -ipo -no-prec-div -ansi-alias

          456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                       -ansi-alias -auto-ilp32

           458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                       -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                       -prof-use(pass 2) -unroll4 -auto-ilp32

       462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                       -opt-prefetch

         464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                       -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                       -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
        471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                       -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                       -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                       -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

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Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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