



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = 39.7

IBM System x3250 M2 (Intel Core 2 Duo E7400)

SPECint_rate_base2006 = 37.6

CPU2006 license: 11

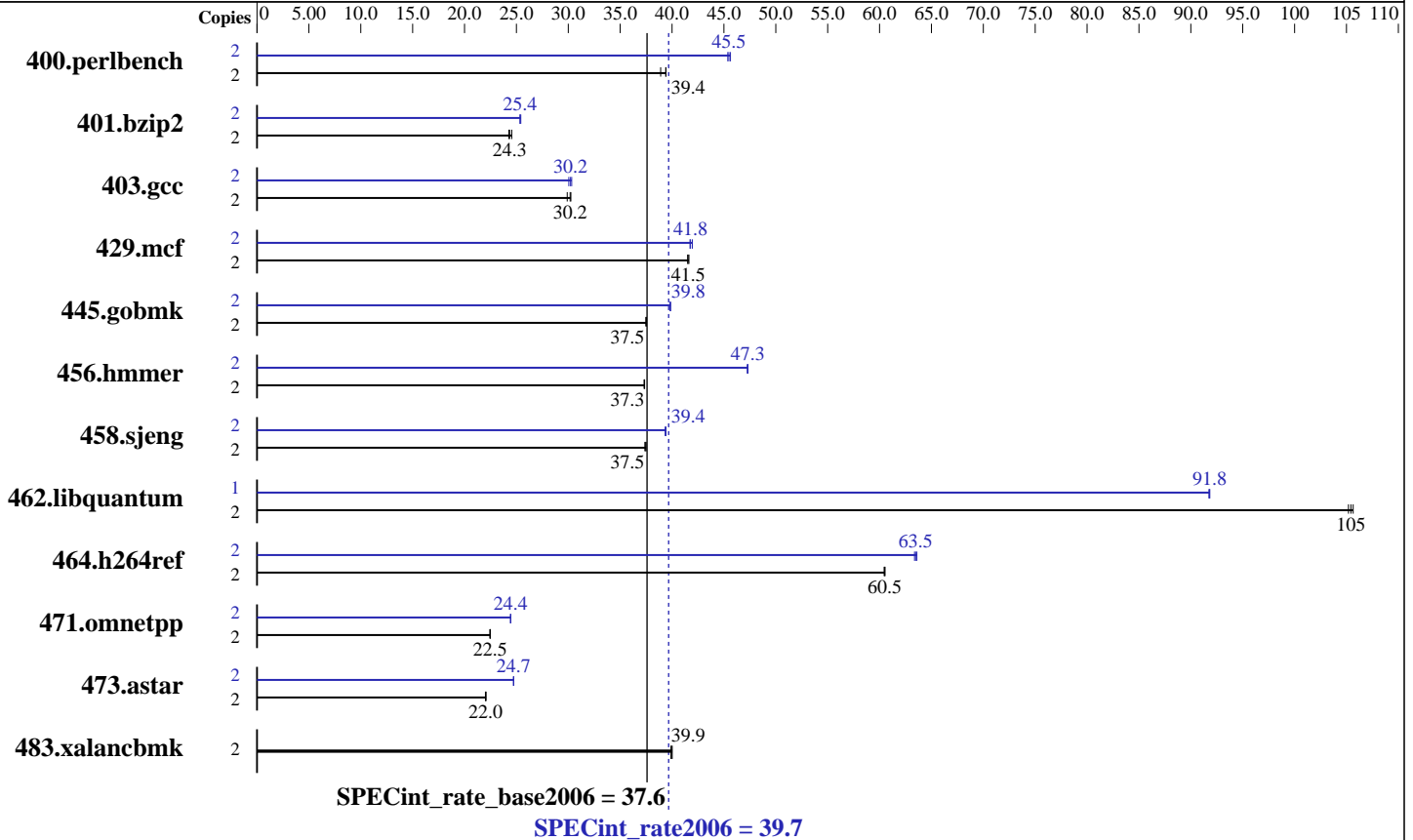
Test date: May-2009

Test sponsor: IBM Corporation

Hardware Availability: Apr-2009

Tested by: IBM Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Duo E7400
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 3 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB(4 x 2 GB DDR2-6400E ECC)
 Disk Subsystem: 1 x 250 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10(x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	502	38.9	496	39.4	496	39.4	2	431	45.4	428	45.6	430	45.5
401.bzip2	2	793	24.3	795	24.3	786	24.6	2	761	25.4	761	25.4	759	25.4
403.gcc	2	539	29.9	534	30.2	532	30.2	2	531	30.3	533	30.2	536	30.0
429.mcf	2	439	41.5	439	41.5	438	41.6	2	435	42.0	437	41.8	437	41.7
445.gobmk	2	559	37.5	559	37.5	560	37.5	2	526	39.8	527	39.8	527	39.8
456.hammer	2	500	37.3	500	37.4	500	37.3	2	395	47.2	395	47.3	394	47.3
458.sjeng	2	646	37.5	647	37.4	645	37.5	2	615	39.4	614	39.4	615	39.4
462.libquantum	2	393	105	394	105	392	106	1	226	91.7	226	91.8	226	91.8
464.h264ref	2	732	60.4	731	60.5	732	60.5	2	696	63.6	698	63.4	697	63.5
471.omnetpp	2	556	22.5	556	22.5	557	22.4	2	511	24.4	511	24.4	511	24.4
473.astar	2	637	22.0	638	22.0	636	22.1	2	568	24.7	568	24.7	568	24.7
483.xalancbmk	2	345	40.0	346	39.9	346	39.9	2	345	40.0	346	39.9	346	39.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

```

taskset was used to bind processes to cores except
for 462.libquantum peak
Hardware Sector Prefetch Enable and Adjacent Sector Prefetch Enable
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"

```

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

```

Continued on next page



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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090902.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090902.xml>

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For other inquiries, please contact webmaster@spec.org.

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