SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX300 S5, Intel Xeon W5590, 3.33 GHz

SPECint®_rate2006 =   274
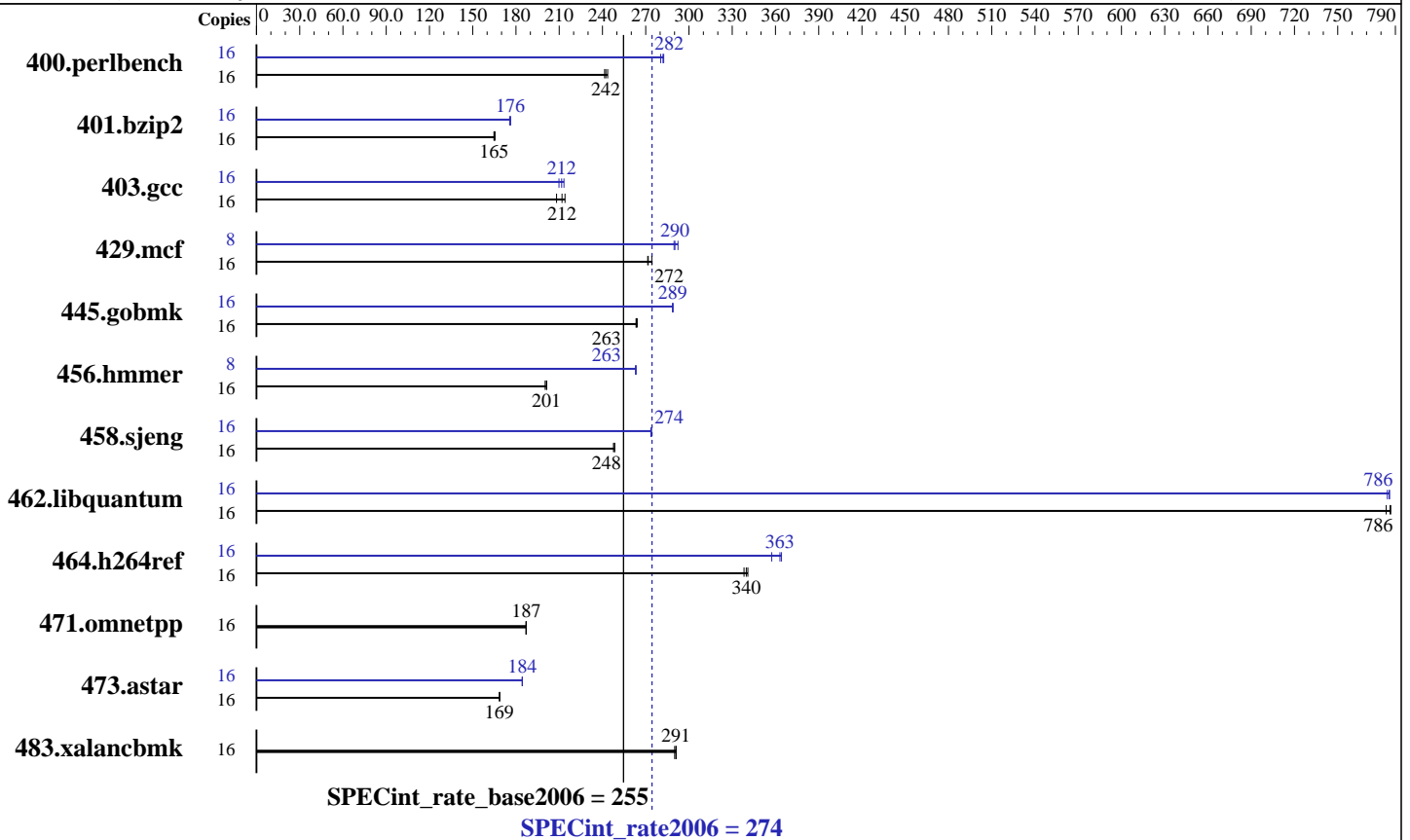
SPECint_rate_base2006 =   255

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2009
Hardware Availability: Oct-2009
Software Availability: Feb-2009

Copies		
400.perlbench	16	282
	16	242
401.bzip2	16	176
	16	165
403.gcc	16	212
	16	212
429.mcf	8	290
	16	272
445.gobmk	16	289
	16	263
456.hmmer	8	263
	16	201
458.sjeng	16	274
	16	248
462.libquantum	16	786
	16	786
464.h264ref	16	363
	16	340
471.omnetpp	16	187
473.astar	16	184
	16	169
483.xalancbmk	16	291

SPECint_rate_base2006 = 255

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Hardware

CPU Name: Intel Xeon W5590
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 3333
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem: 1 x SAS, 73 GB, 10000 RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel: No
File System: ext3
System State: Multi-User Run Level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	647	241	**645**	**242**	642	244	16	**554**	**282**	557	280	553	282
401.bzip2	16	933	165	**936**	**165**	937	165	16	879	176	876	176	**877**	**176**
403.gcc	16	**608**	**212**	602	214	619	208	16	**608**	**212**	614	210	604	213
429.mcf	16	532	274	538	271	**537**	**272**	8	252	290	250	292	**251**	**290**
445.gobmk	16	636	264	**637**	**263**	637	263	16	**581**	**289**	581	289	582	289
456.hmmer	16	741	201	**742**	**201**	746	200	8	284	263	284	263	**284**	**263**
458.sjeng	16	782	248	779	248	**780**	**248**	16	707	274	**707**	**274**	708	274
462.libquantum	16	423	784	**422**	**786**	421	787	16	**422**	**786**	423	784	422	786
464.h264ref	16	1047	338	**1042**	**340**	1039	341	16	972	364	**975**	**363**	991	357
471.omnetpp	16	534	187	535	187	**534**	**187**	16	534	187	535	187	**534**	**187**
473.astar	16	**666**	**169**	666	169	666	169	16	**609**	**184**	609	184	609	184
483.xalancbmk	16	379	291	381	290	**379**	**291**	16	379	291	381	290	**379**	**291**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    BIOS configuration:
    Memory Speed = Max Performance

General Notes

    This result was measured on the PRIMERGY RX300 S5. The PRIMERGY RX300 S5
    and the PRIMERGY TX300 S5 are electronically equivalent.

    For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

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Base Portability Flags

        400.perlbench: `-DSPEC_CPU_LINUX_IA32`
        462.libquantum: `-DSPEC_CPU_LINUX`
        483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
        `-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc`
        `-opt-malloc-options=3 -opt-prefetch`

C++ benchmarks:
        `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
        `-L/spec/cpu2006.1.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

          403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
        `icc`

        401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

        456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

        458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
        `icpc`

        473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

Peak Portability Flags

        400.perlbench: `-DSPEC_CPU_LINUX_IA32`
        401.bzip2: `-DSPEC_CPU_LP64`
        456.hmmer: `-DSPEC_CPU_LP64`

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CPU2006 license:	19	**Test date:**	Aug-2009
Test sponsor:	Fujitsu	**Hardware Availability:**	Oct-2009
Tested by:	Fujitsu	**Software Availability:**	Feb-2009

Peak Portability Flags (Continued)

```
       458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LINUX
        473.astar: -DSPEC_CPU_LP64
   483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -ansi-alias -opt-prefetch

       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

         403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
                  -opt-malloc-options=3

         429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -opt-prefetch

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                  -ipo -no-prec-div -ansi-alias

       456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                  -ansi-alias -auto-ilp32

       458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll4 -auto-ilp32

   462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
                  -opt-malloc-options=3 -opt-prefetch

     464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
     471.omnetpp: basepeak = yes

        473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
                  -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.14.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.14.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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