



# SPEC® CFP2006 Result

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**IBM Corporation**

**SPECfp®2006 = 32.1**

IBM System x3400 M2 (Intel Xeon E5520)

**SPECfp\_base2006 = 30.4**

CPU2006 license: 11

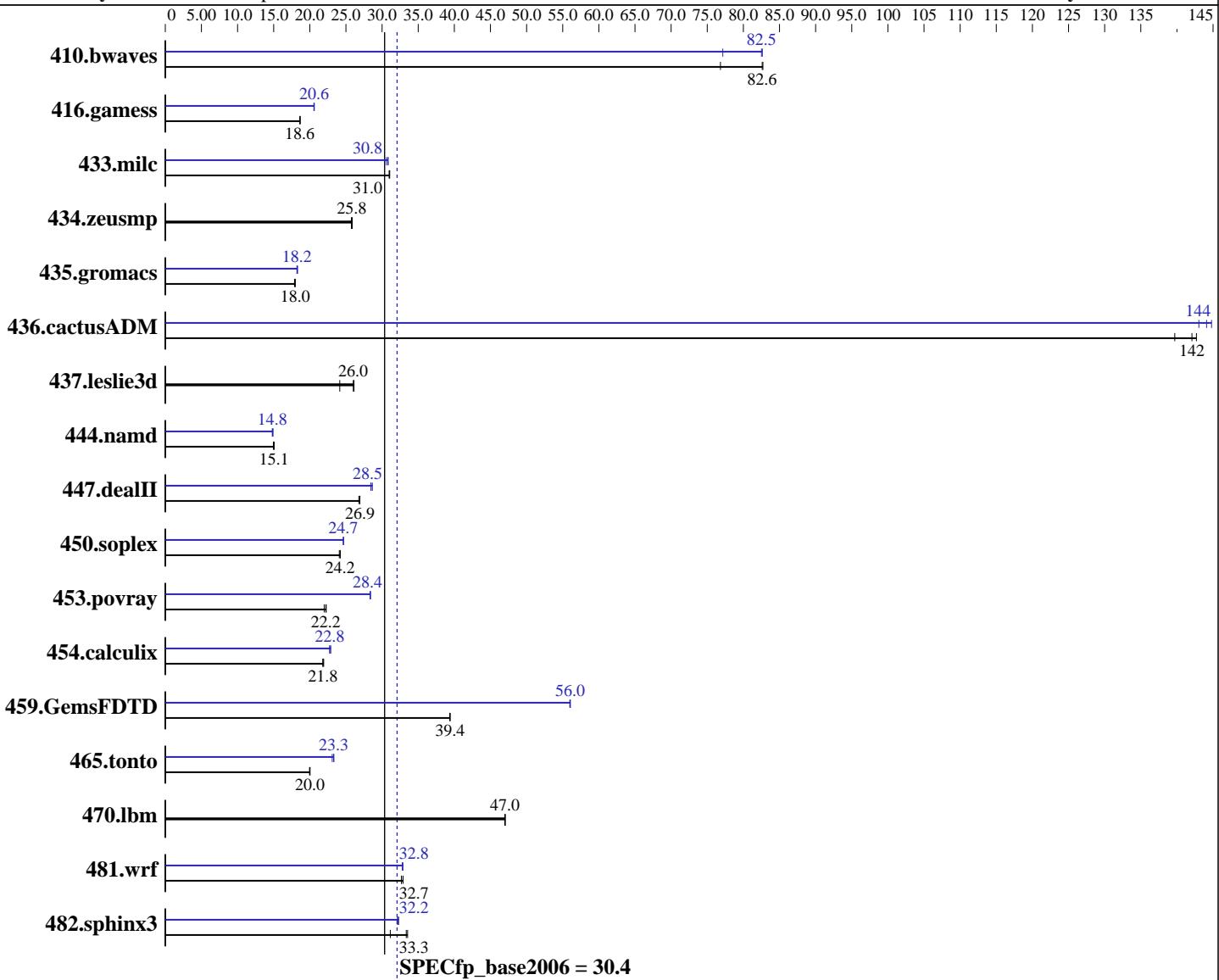
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



## Hardware

CPU Name: Intel Xeon E5520  
CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz  
CPU MHz: 2267  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64)  
SP2 with Linux patch kernel 20090119,  
Kernel 2.6.16.60-0.34-smp  
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux  
Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
l\_cprof\_p\_11.0.080  
Auto Parallel: Yes  
File System: ReiserFS  
System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip Other Cache: None Memory: 24 GB (12 x 2 GB PC3-10600R, 2 Rank, running at 1066 MHz) Disk Subsystem: 1 x 146 GB SAS, 15000 RPM Other Hardware: None	Base Pointers: Peak Pointers: Other Software:	64-bit 32/64-bit Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	177	76.8	164	82.7	<b><u>165</u></b>	<b><u>82.6</u></b>	<b><u>165</u></b>	<b><u>82.5</u></b>	176	77.2	164	82.6
416.gamess	<b><u>1051</u></b>	<b><u>18.6</u></b>	1049	18.7	1051	18.6	952	20.6	<b><u>950</u></b>	<b><u>20.6</u></b>	950	20.6
433.milc	<b><u>296</u></b>	<b><u>31.0</u></b>	296	31.1	296	31.0	298	30.8	300	30.6	<b><u>298</u></b>	<b><u>30.8</u></b>
434.zeusmp	353	25.8	<b><u>353</u></b>	<b><u>25.8</u></b>	352	25.9	353	25.8	<b><u>353</u></b>	<b><u>25.8</u></b>	352	25.9
435.gromacs	399	17.9	<b><u>397</u></b>	<b><u>18.0</u></b>	397	18.0	392	18.2	<b><u>392</u></b>	<b><u>18.2</u></b>	390	18.3
436.cactusADM	<b><u>84.1</u></b>	<b><u>142</u></b>	83.7	143	85.5	140	<b><u>82.9</u></b>	<b><u>144</u></b>	82.5	145	83.5	143
437.leslie3d	360	26.1	389	24.1	<b><u>361</u></b>	<b><u>26.0</u></b>	360	26.1	389	24.1	<b><u>361</u></b>	<b><u>26.0</u></b>
444.namd	536	15.0	533	15.1	<b><u>533</u></b>	<b><u>15.1</u></b>	<b><u>541</u></b>	<b><u>14.8</u></b>	541	14.8	538	14.9
447.dealII	425	26.9	<b><u>425</u></b>	<b><u>26.9</u></b>	427	26.8	399	28.7	<b><u>402</u></b>	<b><u>28.5</u></b>	402	28.5
450.soplex	344	24.2	346	24.1	<b><u>344</u></b>	<b><u>24.2</u></b>	<b><u>338</u></b>	<b><u>24.7</u></b>	339	24.6	338	24.7
453.povray	<b><u>239</u></b>	<b><u>22.2</u></b>	242	22.0	239	22.3	188	28.4	187	28.4	<b><u>188</u></b>	<b><u>28.4</u></b>
454.calculix	378	21.8	376	21.9	<b><u>378</u></b>	<b><u>21.8</u></b>	<b><u>362</u></b>	<b><u>22.8</u></b>	363	22.8	360	22.9
459.GemsFDTD	269	39.4	<b><u>269</u></b>	<b><u>39.4</u></b>	269	39.4	190	56.0	<b><u>189</u></b>	<b><u>56.0</u></b>	189	56.0
465.tonto	<b><u>492</u></b>	<b><u>20.0</u></b>	492	20.0	493	20.0	422	23.3	426	23.1	<b><u>422</u></b>	<b><u>23.3</u></b>
470.lbm	292	47.1	<b><u>292</u></b>	<b><u>47.0</u></b>	292	47.0	292	47.1	<b><u>292</u></b>	<b><u>47.0</u></b>	292	47.0
481.wrf	342	32.7	339	32.9	<b><u>342</u></b>	<b><u>32.7</u></b>	<b><u>340</u></b>	<b><u>32.8</u></b>	340	32.9	341	32.8
482.sphinx3	581	33.5	626	31.1	<b><u>584</u></b>	<b><u>33.3</u></b>	<b><u>605</u></b>	<b><u>32.2</u></b>	603	32.3	607	32.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run  
 OMP\_NUM\_THREADS set to number of cores  
 KMP\_AFFINITY set to granularity=fine,scatter  
 KMP\_STACKSIZE set to 200M

## Base Compiler Invocation

C benchmarks:  
 icc

C++ benchmarks:  
 icpc

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## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

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## Peak Compiler Invocation (Continued)

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):

`icpc`

450.soplex: `icpc -m32`

Fortran benchmarks:

`ifort`

Benchmarks using both Fortran and C:

`icc ifort`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`  
416.gamess: `-DSPEC_CPU_LP64`  
    433.milc: `-DSPEC_CPU_LP64`  
    434.zeusmp: `-DSPEC_CPU_LP64`  
    435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`  
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`  
437.leslie3d: `-DSPEC_CPU_LP64`  
    444.namd: `-DSPEC_CPU_LP64`  
    447.dealII: `-DSPEC_CPU_LP64`  
    453.povray: `-DSPEC_CPU_LP64`  
    454.calculix: `-DSPEC_CPU_LP64 -nofor_main`  
459.GemsFDTD: `-DSPEC_CPU_LP64`  
    465.tonto: `-DSPEC_CPU_LP64`  
    470.lbm: `-DSPEC_CPU_LP64`  
    481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`  
    `-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)`  
    `-fno-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
           -fno-alias -auto-ilp32

447.dealII: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
           -unroll12 -ansi-alias -scalar-rep -opt-prefetch

450.soplex: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
           -opt-malloc-options=3

453.povray: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
           -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsse4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
           -parallel

416.gamess: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
           -unroll12 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
           -unroll12 -Ob0 -opt-prefetch -parallel

465.tonto: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
           -unroll14 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
           -opt-prefetch -auto-ilp32

436.cactusADM: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
           -unroll12 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xsse4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

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## Peak Optimization Flags (Continued)

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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