SPEC® CINT2006 Result

FORMAT

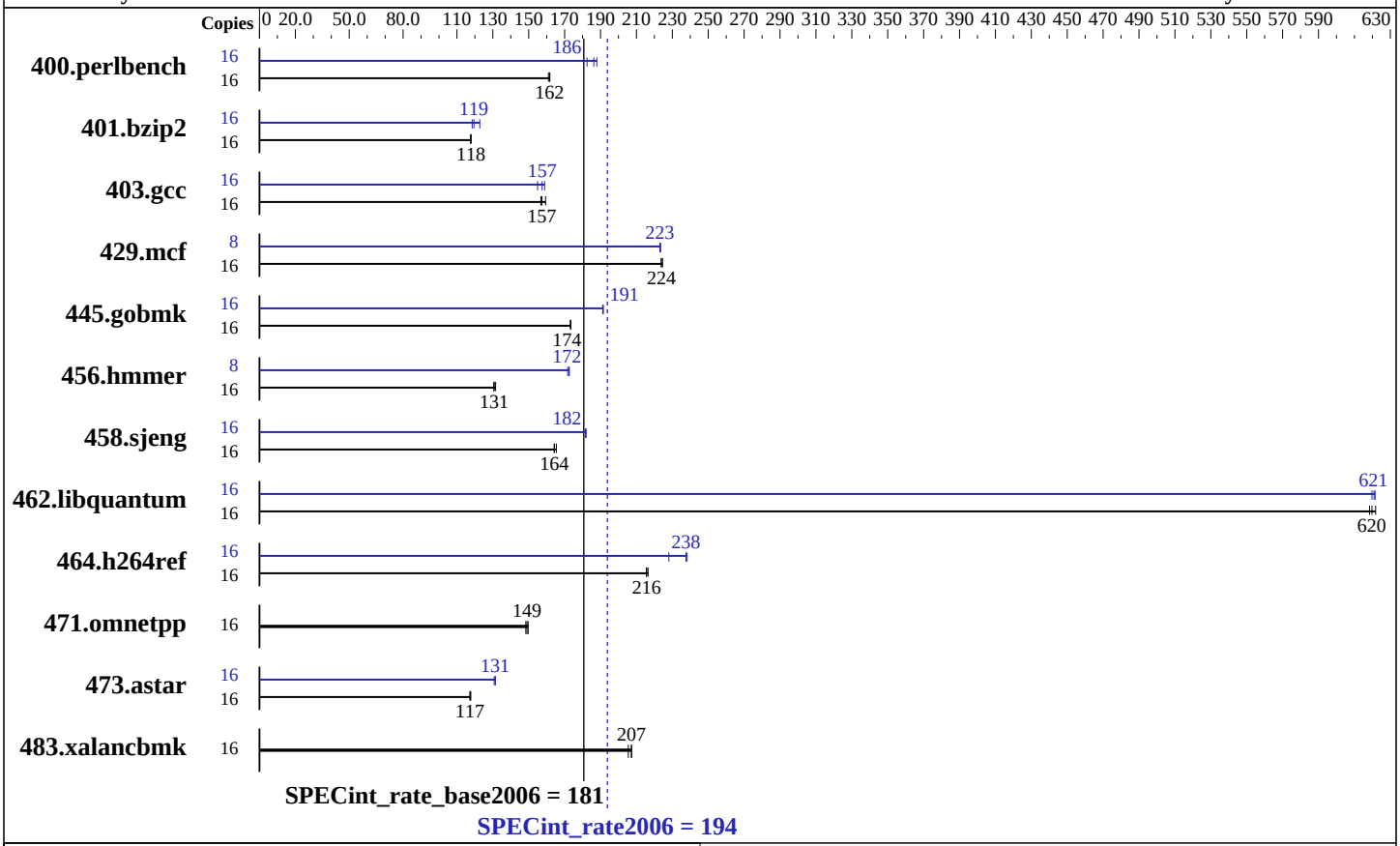
Format R2625URLX (Intel Xeon E5520, 2.26 GHz)

SPECint®_rate2006 = 194

SPECint_rate_base2006 = 181

CPU2006 license: 9015
Test sponsor: FORMAT
Tested by: FORMAT

Test date: Jun-2009
Hardware Availability: Mar-2009
Software Availability: Mar-2009



Hardware

CPU Name:	Intel Xeon E5520
CPU Characteristics:	Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB DDR3-1066 QR ECC Reg. DIMM)
Disk Subsystem:	1 x 250 GB 7200 RPM SATA
Other Hardware:	None

Software

Operating System:	Scientific Linux 5.3 (SLC53) Kernel 2.6.18-128.1.10.el5 x86_64
Compiler:	Intel C++ Compiler Professional 11.0 for Linux Build 20090318 Package ID: l_cproc_p_11.0.083
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 (32-bit) Microquill SmartHeap V9.1 (64-bit) binutils-2.17.50.0.6-9.el5

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**967**	**162**	966	162	971	161	16	857	182	**839**	**186**	832	188
401.bzip2	16	**1311**	**118**	1310	118	1313	118	16	1303	119	**1292**	**119**	1258	123
403.gcc	16	808	159	**818**	**157**	822	157	16	811	159	**818**	**157**	831	155
429.mcf	16	652	224	650	225	**652**	**224**	8	327	223	326	224	**327**	**223**
445.gobmk	16	967	174	**967**	**174**	970	173	16	877	191	**877**	**191**	878	191
456.hmmer	16	**1142**	**131**	1144	131	1135	132	8	435	172	**433**	**172**	432	173
458.sjeng	16	1170	165	**1178**	**164**	1179	164	16	**1065**	**182**	1064	182	1066	182
462.libquantum	16	533	622	536	618	**535**	**620**	16	535	620	**534**	**621**	533	622
464.h264ref	16	**1641**	**216**	1643	216	1636	216	16	**1490**	**238**	1486	238	1553	228
471.omnetpp	16	668	150	674	148	**671**	**149**	16	668	150	674	148	**671**	**149**
473.astar	16	954	118	958	117	**957**	**117**	16	859	131	855	131	**855**	**131**
483.xalancbmk	16	537	205	533	207	**533**	**207**	16	537	205	533	207	**533**	**207**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/azeh/spec/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/083/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/083/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/083/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/083/bin/intel64/icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32 -Wl,-z,muldefs -L/home/azeh/spec/lib -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.20090710.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.20090710.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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