SPEC® CINT2006 Result
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Itautec
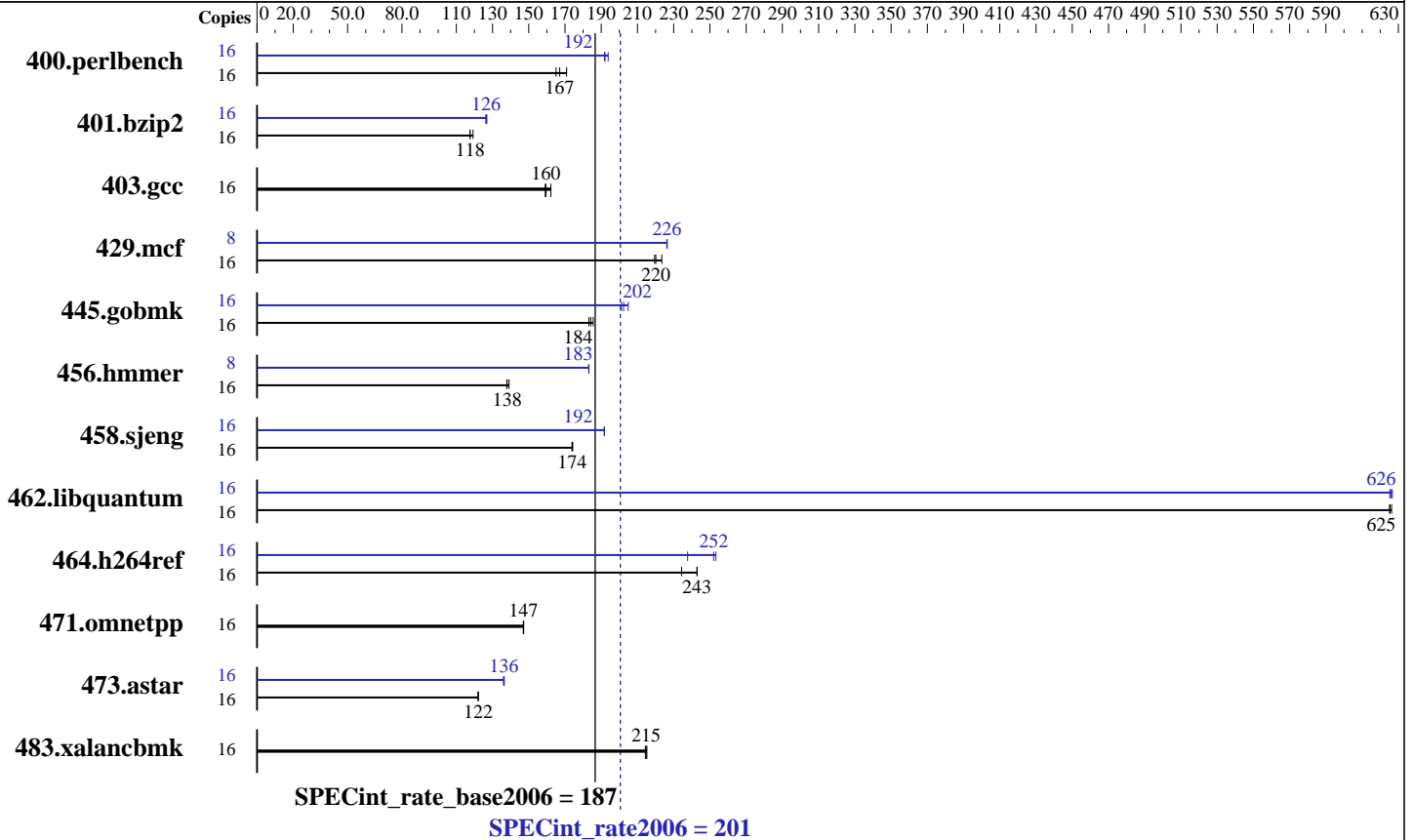
SPECint®_rate2006 = 201

Servidor Itautec MX203 (Intel Xeon E5520)

SPECint_rate_base2006 = 187

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jun-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

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Hardware

CPU Name:	Intel Xeon E5520
CPU Characteristics:	Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4GB DDR3-1333, CL 9, ECC, running at 1066MHz)
Disk Subsystem:	1 x 160 GB SATA-2, 7200RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.34-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**936**	**167**	947	165	915	171	16	815	192	**814**	**192**	807	194
401.bzip2	16	1295	119	1314	118	**1313**	**118**	16	**1221**	**126**	1222	126	1217	127
403.gcc	16	810	159	**807**	**160**	794	162	16	810	159	**807**	**160**	794	162
429.mcf	16	665	219	653	223	**663**	**220**	8	323	226	322	226	**322**	**226**
445.gobmk	16	916	183	904	186	**911**	**184**	16	819	205	**829**	**202**	834	201
456.hmmer	16	1073	139	1083	138	**1081**	**138**	8	407	183	408	183	**408**	**183**
458.sjeng	16	1111	174	**1113**	**174**	1114	174	16	**1010**	**192**	1010	192	1010	192
462.libquantum	16	**530**	**625**	530	625	529	626	16	530	625	529	627	**530**	**626**
464.h264ref	16	1511	234	1458	243	**1458**	**243**	16	1398	253	**1405**	**252**	1490	238
471.omnetpp	16	680	147	**680**	**147**	679	147	16	680	147	**680**	**147**	679	147
473.astar	16	**920**	**122**	919	122	920	122	16	**823**	**136**	823	136	826	136
483.xalancbmk	16	515	214	**514**	**215**	513	215	16	515	214	**514**	**215**	513	215

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
    numactl was used to bind copies to the cores

General Notes

    This result was measured on the Servidor Itautec MX203.
    The Servidor Itautec MX203 and the Servidor Itautec MX223 are electronically equivalent.

Base Compiler Invocation

C benchmarks:

    icc

C++ benchmarks:

    icpc

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
    `icc`

401.bzip2: `/opt/intel/Compiler/11.0/081/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/081/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/081/bin/intel64/icc`

C++ benchmarks (except as noted below):
    `icpc`

473.astar: `/opt/intel/Compiler/11.0/081/bin/intel64/icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

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Peak Optimization Flags

C benchmarks:

 400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

 401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

 403.gcc: basepeak = yes

 429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch

 445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

 456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

 458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

 462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -opt-prefetch

 464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

 471.omnetpp: basepeak = yes

 473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap64

 483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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