SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY TX150 S6, Intel Xeon L3360, 2.83 GHz

SPECint®_rate2006 =   81.2

SPECint_rate_base2006 = 75.7

CPU2006 license: 22
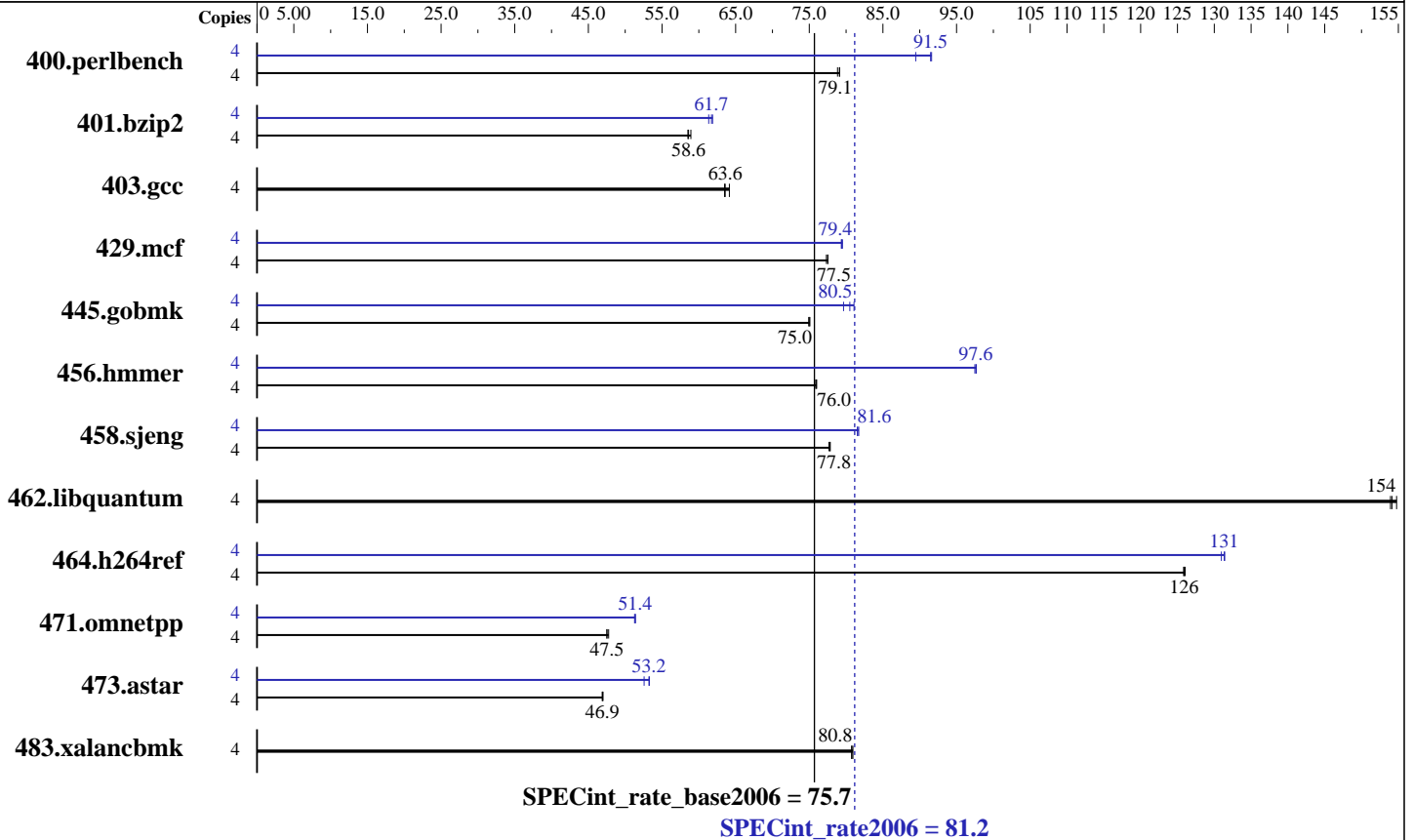
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2009

Hardware Availability: Apr-2009

Software Availability: Nov-2008

Copies

Benchmark	Copies	Peak	Base
400.perlbench	4	91.5	79.1
401.bzip2	4	61.7	58.6
403.gcc	4		63.6
429.mcf	4	79.4	77.5
445.gobmk	4	80.5	75.0
456.hmmer	4	97.6	76.0
458.sjeng	4	81.6	77.8
462.libquantum	4		154
464.h264ref	4	131	126
471.omnetpp	4	51.4	47.5
473.astar	4	53.2	46.9
483.xalancbmk	4		80.8

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Hardware

CPU Name: Intel Xeon L3360

CPU Characteristics: 1333 MHz system bus

CPU MHz: 2833

FPU: Integrated

CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip

CPU(s) orderable: 1 chip

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

L3 Cache: None

Other Cache: None

Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)

Disk Subsystem: 1x SATA, 160 GB, 7200 rpm

Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp

Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.066

Auto Parallel: No

File System: ext3

System State: Multi-User Run Level 3

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	4	496	78.8	494	79.1	**494**	**79.1**	4	437	89.5	**427**	**91.5**	427	91.6
401.bzip2	4	660	58.5	**659**	**58.6**	655	58.9	4	**625**	**61.7**	624	61.9	629	61.3
403.gcc	4	**506**	**63.6**	502	64.1	507	63.5	4	**506**	**63.6**	502	64.1	507	63.5
429.mcf	4	471	77.5	472	77.3	**471**	**77.5**	4	**459**	**79.4**	459	79.5	460	79.4
445.gobmk	4	559	75.1	**560**	**75.0**	560	74.9	4	527	79.7	518	81.1	**521**	**80.5**
456.hmmer	4	491	76.0	492	75.9	**491**	**76.0**	4	**382**	**97.6**	383	97.5	382	97.7
458.sjeng	4	**622**	**77.8**	623	77.7	622	77.8	4	594	81.5	**593**	**81.6**	592	81.7
462.libquantum	4	536	155	**538**	**154**	538	154	4	536	155	**538**	**154**	538	154
464.h264ref	4	702	126	703	126	**703**	**126**	4	674	131	**674**	**131**	676	131
471.omnetpp	4	526	47.5	**526**	**47.5**	524	47.8	4	487	51.4	**487**	**51.4**	488	51.3
473.astar	4	**598**	**46.9**	598	47.0	599	46.9	4	527	53.3	534	52.6	**528**	**53.2**
483.xalancbmk	4	341	80.9	**342**	**80.8**	342	80.7	4	341	80.9	**342**	**80.8**	342	80.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset has been used to bind processes to cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
    icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
    -opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
    -xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

    401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

    456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:
    icpc

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Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
        401.bzip2: -DSPEC_CPU_LP64
        456.hmmer: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                   -no-prec-div -static -ansi-alias -opt-prefetch

        401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                   -no-prec-div -static -opt-prefetch -ansi-alias

          403.gcc: basepeak = yes

          429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                   -no-prec-div -static -opt-prefetch

        445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
                   -no-prec-div -ansi-alias

        456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
                   -ansi-alias

        458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                   -no-prec-div -static -unroll4

    462.libquantum: basepeak = yes

       464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                   -no-prec-div -static -unroll2 -ansi-alias
```

C++ benchmarks:

```
       471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                   -no-prec-div -ansi-alias -opt-ra-region-strategy=block
                   -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

         473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                   -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
                   -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

    483.xalancbmk: basepeak = yes
```

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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