



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = 136

Servidor Itautec MX221 (Intel Xeon E5440)

SPECint_rate_base2006 = 123

CPU2006 license: 9001

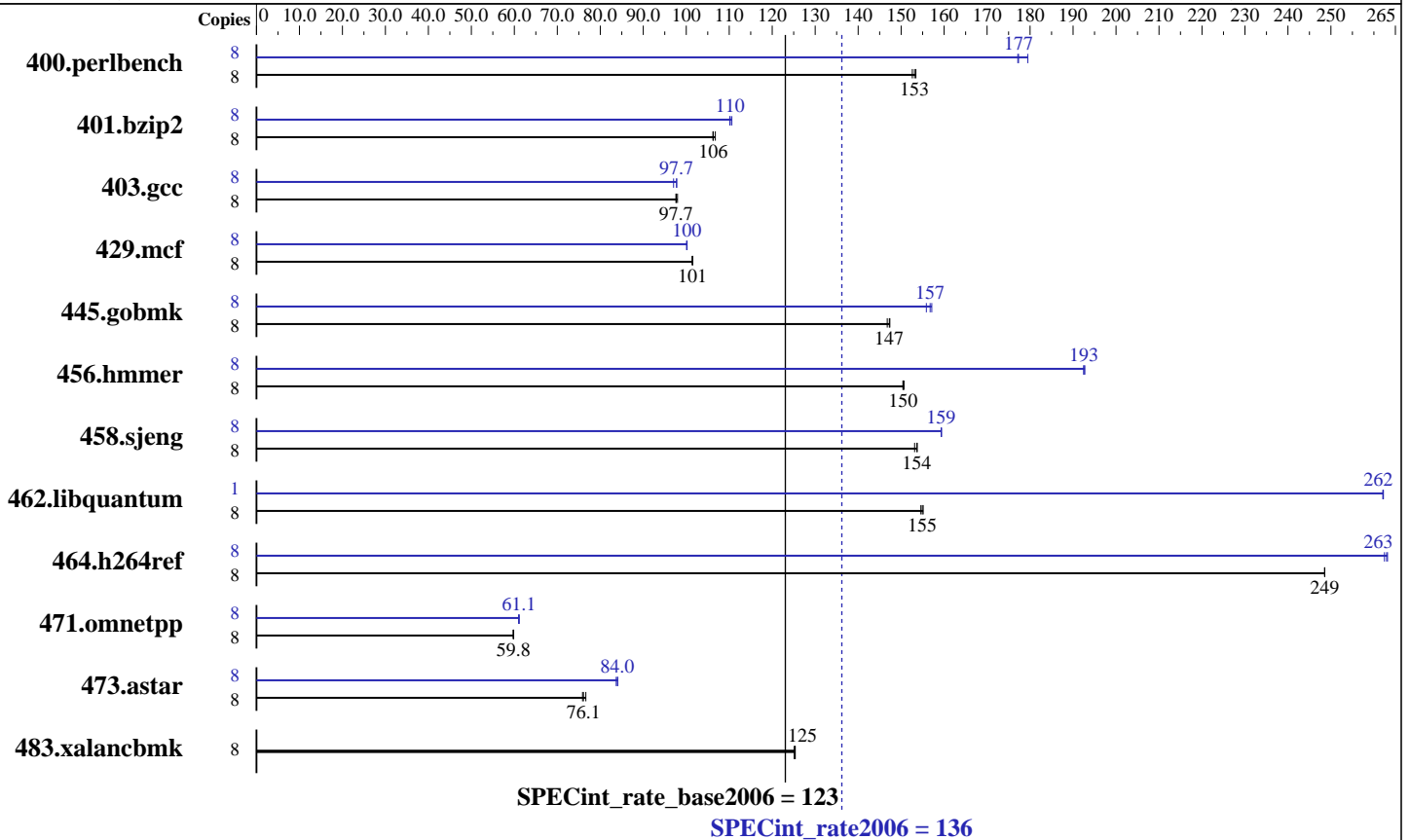
Test date: Jan-2009

Test sponsor: Itautec

Hardware Availability: Dec-2007

Tested by: Itautec

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5440
 CPU Characteristics:
 CPU MHz: 2830
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4 * 4 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SAS, 36GB, 15000RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20081105 Package ID: l_cproc_b_11.0.074
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502
 Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	510	153	512	153	509	153	8	441	177	441	177	435	179
401.bzip2	8	727	106	726	106	723	107	8	698	111	701	110	700	110
403.gcc	8	659	97.7	660	97.6	657	98.0	8	659	97.7	664	97.1	659	97.8
429.mcf	8	719	102	719	101	720	101	8	729	100	729	100	728	100
445.gobmk	8	572	147	569	147	570	147	8	534	157	535	157	538	156
456.hammer	8	496	150	496	150	495	151	8	388	193	387	193	388	192
458.sjeng	8	630	154	632	153	630	154	8	608	159	607	159	608	159
462.libquantum	8	1072	155	1070	155	1069	155	1	79.1	262	79.0	262	79.0	262
464.h264ref	8	712	249	712	249	712	249	8	673	263	675	262	673	263
471.omnetpp	8	838	59.7	836	59.8	835	59.8	8	818	61.1	819	61.1	820	61.0
473.astar	8	733	76.6	738	76.1	740	75.9	8	668	84.0	669	84.0	671	83.7
483.xalancbmk	8	441	125	440	125	440	125	8	441	125	440	125	440	125

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Submit Notes

The config file option 'submit' was used.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors,
except for 462.libquantum at peak.

Platform Notes

BIOS configuration:
Hardware Prefetch Enabled

General Notes

This result was measured on the Servidor Itaotec MX221.
The Servidor Itaotec MX221 and the Servidor Itaotec MX201 are electronically equivalent.



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/074/bin/intel64/icc
-L/opt/intel/Compiler/11.0/074/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/074/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/074/bin/intel64/icc
-L/opt/intel/Compiler/11.0/074/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/074/ipp/em64t/include

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias
403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3
429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias
456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4
462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

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Peak Optimization Flags (Continued)

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.html>
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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