



SPEC® CINT2006 Result

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ACTION S.A.

ACTINA SOLAR 222 X2 (Intel Xeon processor E5405,
2.0GHz)

SPECint_rate2006 = 81.0

SPECint_rate_base2006 = 74.7

CPU2006 license: 9008

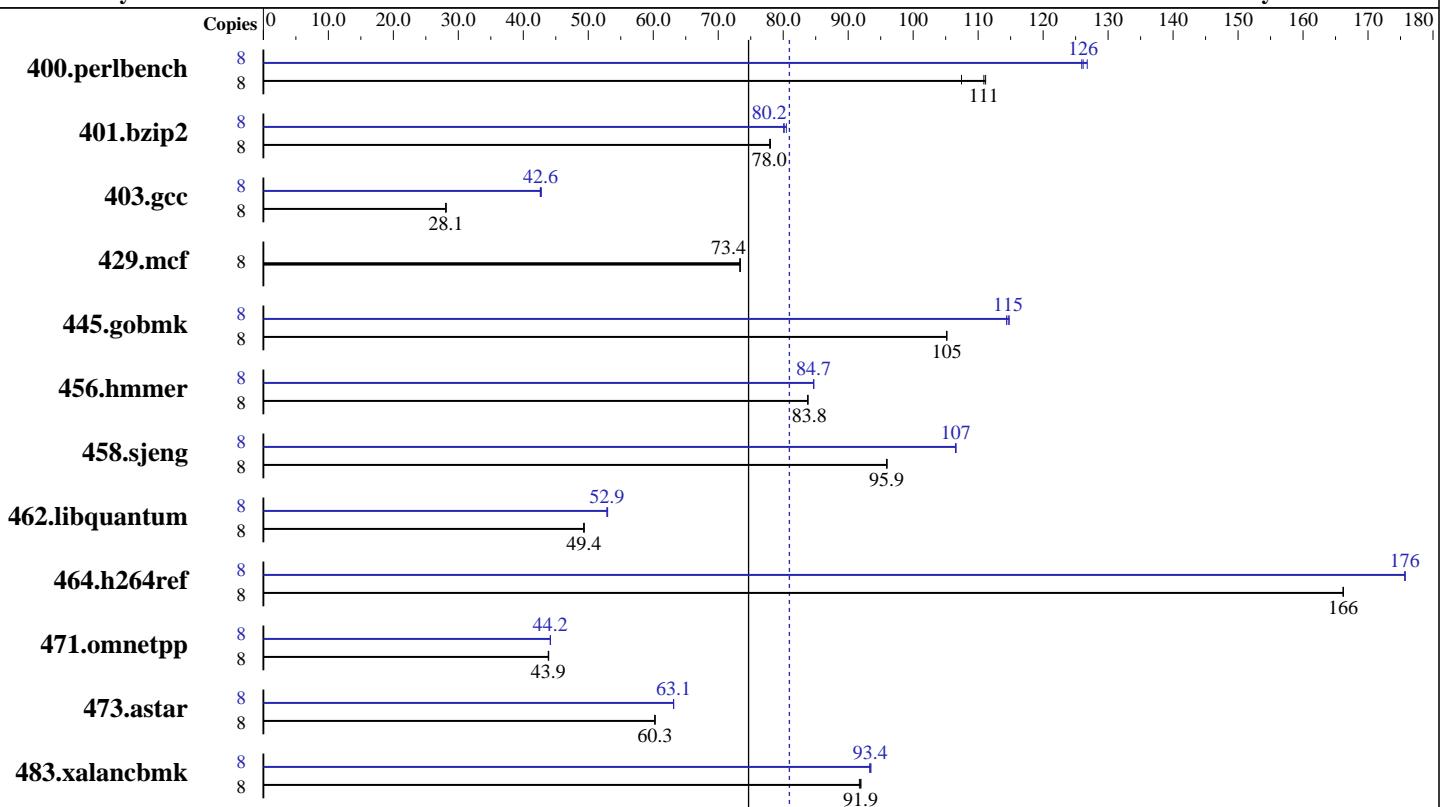
Test date: Jan-2008

Test sponsor: ACTION S.A.

Hardware Availability: Jan-2008

Tested by: ACTION S.A.

Software Availability: Jun-2007



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Hardware

CPU Name: Intel Xeon E5405
CPU Characteristics: 2.0 GHz, 12MB L2, 1333MHz bus
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
Disk Subsystem: RAID 5 (5X 147GB SAS 15K RPM)
Other Hardware: None

Software

Operating System: Windows 2003 Server Enterprise Edition (32-bit)
Service Pack 1
Compiler: Intel C++ Compiler for IA32 version 10.0
Build 20070426 Package ID: W_CC_P_10.0.025
Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|-------------|-------------|-------------|-------------|-------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 728 | 107 | 703 | 111 | 705 | 111 | 8 | 616 | 127 | 620 | 126 | 621 | 126 |
| 401.bzip2 | 8 | 989 | 78.0 | 991 | 77.9 | 990 | 78.0 | 8 | 959 | 80.5 | 963 | 80.2 | 964 | 80.1 |
| 403.gcc | 8 | 2290 | 28.1 | 2291 | 28.1 | 2295 | 28.1 | 8 | 1510 | 42.6 | 1511 | 42.6 | 1505 | 42.8 |
| 429.mcf | 8 | 995 | 73.4 | 994 | 73.4 | 995 | 73.4 | 8 | 995 | 73.4 | 994 | 73.4 | 995 | 73.4 |
| 445.gobmk | 8 | 798 | 105 | 798 | 105 | 798 | 105 | 8 | 731 | 115 | 734 | 114 | 732 | 115 |
| 456.hammer | 8 | 891 | 83.8 | 891 | 83.8 | 891 | 83.8 | 8 | 881 | 84.7 | 881 | 84.7 | 881 | 84.7 |
| 458.sjeng | 8 | 1009 | 96.0 | 1009 | 95.9 | 1009 | 95.9 | 8 | 908 | 107 | 909 | 107 | 908 | 107 |
| 462.libquantum | 8 | 3362 | 49.3 | 3359 | 49.4 | 3358 | 49.4 | 8 | 3133 | 52.9 | 3129 | 53.0 | 3134 | 52.9 |
| 464.h264ref | 8 | 1065 | 166 | 1065 | 166 | 1065 | 166 | 8 | 1008 | 176 | 1008 | 176 | 1008 | 176 |
| 471.omnetpp | 8 | 1140 | 43.9 | 1140 | 43.8 | 1139 | 43.9 | 8 | 1132 | 44.2 | 1132 | 44.2 | 1132 | 44.2 |
| 473.astar | 8 | 932 | 60.3 | 932 | 60.3 | 932 | 60.3 | 8 | 890 | 63.1 | 889 | 63.1 | 890 | 63.1 |
| 483.xalancbmk | 8 | 600 | 91.9 | 600 | 91.9 | 602 | 91.7 | 8 | 592 | 93.3 | 591 | 93.4 | 590 | 93.5 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Product description located as of 1/2008:

<http://www.actina.pl>

Binaries were built on Windows XP Professional SP2

Start command was used to bind processes to CPUs

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32



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Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias  
-Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE
```

```
403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000  
-link /FORCE:MULTIPLE
```

```
429.mcf: basepeak = yes
```

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Peak Optimization Flags (Continued)

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec_div -Qansi-alias /F512000000
-link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
-Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.

Report generated on Tue Jul 22 16:19:34 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 6 February 2008.