



# SPEC® CINT2006 Result

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## Acer Incorporated

### SPECint®\_rate2006 = 54.6

### Acer Altos G330 Mk2 (Intel Xeon X3210)

### SPECint\_rate\_base2006 = 47.1

CPU2006 license: 97

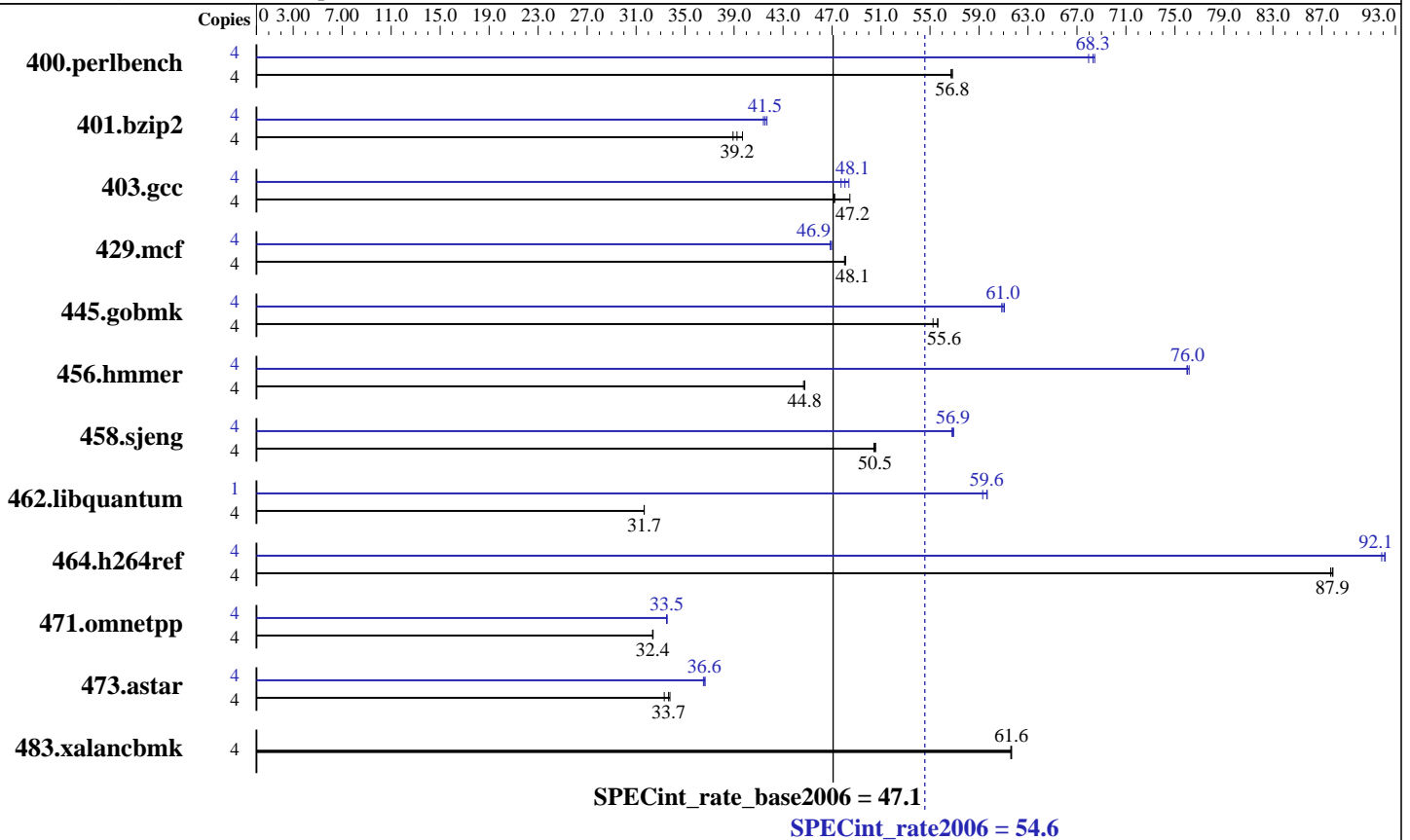
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007



#### Hardware

CPU Name: Intel Xeon X3210  
 CPU Characteristics: 1066 MHz system bus  
 CPU MHz: 2130  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)  
 Disk Subsystem: 1 x 160 GB 7200RPM SATA HDD  
 Other Hardware: None

#### Software

Operating System: SuSE Linux Enterprise Server 10 SP1 (x86\_64)  
 Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux  
 Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>688</b>	<b>56.8</b>	689	56.7	688	56.8	4	<b>572</b>	<b>68.3</b>	575	68.0	571	68.4
401.bzip2	4	<b>984</b>	<b>39.2</b>	992	38.9	973	39.7	4	932	41.4	926	41.7	<b>929</b>	<b>41.5</b>
403.gcc	4	<b>682</b>	<b>47.2</b>	665	48.5	683	47.2	4	666	48.4	<b>670</b>	<b>48.1</b>	675	47.7
429.mcf	4	758	48.1	760	48.0	<b>759</b>	<b>48.1</b>	4	<b>778</b>	<b>46.9</b>	778	46.9	777	47.0
445.gobmk	4	754	55.6	759	55.2	<b>754</b>	<b>55.6</b>	4	687	61.1	<b>688</b>	<b>61.0</b>	690	60.9
456.hmmmer	4	<b>834</b>	<b>44.8</b>	834	44.8	835	44.7	4	491	76.0	490	76.2	<b>491</b>	<b>76.0</b>
458.sjeng	4	957	50.6	960	50.4	<b>958</b>	<b>50.5</b>	4	<b>851</b>	<b>56.9</b>	850	56.9	852	56.8
462.libquantum	4	2619	31.7	2617	31.7	<b>2618</b>	<b>31.7</b>	1	349	59.3	<b>347</b>	<b>59.6</b>	347	59.7
464.h264ref	4	1007	87.9	1009	87.7	<b>1008</b>	<b>87.9</b>	4	960	92.2	<b>961</b>	<b>92.1</b>	963	91.9
471.omnetpp	4	<b>773</b>	<b>32.4</b>	773	32.4	772	32.4	4	745	33.5	<b>747</b>	<b>33.5</b>	747	33.5
473.astar	4	832	33.8	<b>834</b>	<b>33.7</b>	843	33.3	4	769	36.5	<b>768</b>	<b>36.6</b>	766	36.6
483.xalanbmk	4	448	61.7	448	61.6	<b>448</b>	<b>61.6</b>	4	448	61.7	448	61.6	<b>448</b>	<b>61.6</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode  
 "ulimit -s unlimited" was set for stacksize unlimited  
 All results used taskset to bind processes to cores.  
 OMP\_NUM\_THREADS set to number of cores

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.11.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.11.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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