



CFP2000 Result

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Acer Incorporated

Acer Altos R520 (2.66 GHz Intel Xeon Processor X5355)

SPECfp_rate2000 = 101

SPECfp_rate_base2000 = 99.9

SPEC license #: 97 Tested by: Acer Incorporated Test date: Dec-2006 Hardware Avail: Nov-2006 Software Avail: Nov-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	8	134	111	8	134	111
171.swim	8	545	52.8	8	545	52.8
172.mgrid	8	335	49.9	8	335	49.9
173.applu	8	364	53.6	8	364	53.6
177.mesa	8	59.3	219	8	59.3	219
178.galgel	8	81.4	331	8	81.4	331
179.art	8	98.2	246	8	98.2	246
183.quake	8	176	68.7	8	175	68.8
187.facerec	8	144	122	8	144	122
188.amp	8	169	121	8	142	144
189.lucas	8	318	58.4	8	318	58.4
191.fma3d	8	295	66.0	8	295	66.0
200.sixtrack	8	112	91.5	8	113	90.6
301.apsi	8	245	98.6	8	244	98.9

Hardware

CPU: Intel Xeon Processor X5355 (2.66 GHz, 8MB L2, 1333MHz system bus)
CPU MHz: 2666
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32KB(I) + 32KB(D) on chip, per core
Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 8 x 2048MB ECC FB-DIMM DDR2-667
Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1
Compiler: Intel C++ 9.1 Build 20060323Z for IA32 and Fortran Compiler 9.1 Build 20060323Z for IA32
Microsoft Visual Studio .Net 2003 (7.1.5091, for libraries)
shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
+FDO*: FDO (-QxP not used in PASS1)
Base tuning for all Fortran Programs : -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd +FDO shlw32M.lib
187.facerec: basepeak=yes
188.amp: -Oa -QxP
```



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Notes/Tuning Information (Continued)

189.lucas: -fast -Qprefetch- +FDO

191.fma3d: basepeak=yes

200.sixtrack: -Qipo -QxP +FDO

301.apsi: -fast +FDO

Enable hardware prefetching and disable adjacent cache line prefetch BIOS setting

This result was measured on the Acer Altos R520.

The Altos R720 and Altos R520 are electronically equivalent.