



CINT2000 Result

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Acer Incorporated
Acer Altos G530 (3.0 GHz Intel Xeon)

SPECint_rate2000 = 18.3
SPECint_rate_base2000 = 18.3

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Nov-2005 | Hardware Avail: Oct-2005 | Software Avail: Apr-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	210	15.4	2	210	15.4
175.vpr	2	274	11.8	2	271	12.0
176.gcc	2	107	23.9	2	107	23.9
181.mcf	2	238	17.5	2	238	17.5
186.crafty	2	179	12.9	2	179	12.9
197.parser	2	239	17.4	2	238	17.5
252.eon	2	142	21.3	2	142	21.3
253.perlbnk	2	216	19.3	2	219	19.1
254.gap	2	120	21.2	2	120	21.2
255.vortex	2	154	28.7	2	154	28.7
256.bzip2	2	220	15.8	2	221	15.8
300.twolf	2	331	21.0	2	330	21.1

Hardware

CPU: Intel Xeon (3.0 GHz, 2MB L2, 800MHz system bus)
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology Enable)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache: 2048KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 4 x 1GB ECC DDR2-400 SDRAM
Disk Subsystem: 1 x 146GB 10000RPM U320 SCSI HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler: Intel C++ Compiler 9.0 Build 20050430Z for IA32
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
252.eon: Approved stdcpp src.alt used
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlw32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
```



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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll1 +FDO
300.twolf: -fast +FDO shlw32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at <http://www.acer.com>