



# CINT2000 Result

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## SGI

### SGI Altix 330 (1500MHz, Itanium 2)

SPECint\_rate2000 = --

SPECint\_rate\_base2000 = 28.6

SPEC license #: 4 | Tested by: SGI | Test date: May-2005 | Hardware Avail: Jul-2005 | Software Avail: Jun-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	145	22.4			
175.vpr	2	143	22.7			
176.gcc	2	82.0	31.1			
181.mcf	2	120	34.9			
186.crafty	2	84.0	27.6			
197.parser	2	195	21.4			
252.eon	2	71.0	42.5			
253.perlbnk	2	150	27.9			
254.gap	2	125	20.4			
255.vortex	2	101	43.6			
256.bzip2	2	134	26.1			
300.twolf	2	212	32.9			

#### Hardware

CPU: Intel Itanium 2  
 CPU MHz: 1500  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 2 chips, 1 core/chip  
 CPU(s) orderable: 2-16  
 Parallel: No  
 Primary Cache: 16KBI + 16KBD on chip, per core  
 Secondary Cache: 256KB (I+D) on chip, per core  
 L3 Cache: 4.0MB (I+D) on chip, per core  
 Other Cache: N/A  
 Memory: 8 GB (8\*1GB PC2700 DIMMS per 2 core module)  
 Disk Subsystem: 1 x 73 GB SCSI (Seagate Cheetah 10k rpm)  
 Other Hardware: None

#### Software

Operating System: SGI ProPack(TM) v3.0 Service Pack 6  
 Compiler: Intel(R) C++ Itanium(R) Compiler for Linux 8.1 (Build 20041021)  
MicroQuill SmartHeap Library 7.01 (www.microquill.com)  
 File System: xfs  
 System State: Multi-user

### Notes/Tuning Information

+FDO: PASS1=-prof\_gen PASS2=-prof\_use

#### Baseline optimization flags:

C programs: -fast -auto\_ilp32 +FDO  
 C++ programs: -fast -ansi\_alias +FDO  
 Extra Libraries: libsmartheap64.a

#### Portability Flags:

176.gcc: -DSPEC\_CPU2000\_LP64 -Dalloca=\_builtin\_alloca  
 186.crafty: -DLINUX\_i386  
 252.eon: -DSPEC\_CPU2000\_LP64 -DHAS\_ERRLIST -DFMAX\_IS\_DOUBLE  
 253.perlbnk: -DSPEC\_CPU2000\_LP64 -DSPEC\_CPU2000\_NEED\_BOOL  
 -DSPEC\_CPU2000\_LINUX\_IA64 -DSPEC\_CPU2000\_GLIBC22  
 254.gap: -DSPEC\_CPU2000\_LP64 -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_IS\_USG  
 -DSYS\_HAS\_IOCTL\_PROTO -DSYS\_HAS\_TIME\_PROTO -DSYS\_HAS\_SIGNAL\_PROTO  
 255.vortex: -DSPEC\_CPU2000\_LP64 srcalt=closed\_files

Processes were bound to CPUs using runon.