



CINT2000 Result

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Hewlett-Packard Company
ProLiant DL360 G4 (3.6GHz, Intel Xeon)

SPECint_rate2000 = 17.0
SPECint_rate_base2000 = 16.6

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2004 Hardware Avail: Nov-2004 Software Avail: Aug-2004

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	1	121	13.4	1	120	13.6
175.vpr	1	161	10.1	1	155	10.5
176.gcc	1	62.7	20.4	1	62.7	20.4
181.mcf	1	177	11.8	1	177	11.8
186.crafty	1	75.7	15.3	1	75.8	15.3
197.parser	1	140	14.9	1	140	14.9
252.eon	1	73.6	20.5	1	61.6	24.5
253.perlbnk	1	101	20.8	1	97.8	21.3
254.gap	1	62.1	20.5	1	61.1	20.9
255.vortex	1	77.4	28.5	1	77.3	28.5
256.bzip2	1	151	11.5	1	146	11.9
300.twolf	1	172	20.2	1	170	20.4

Hardware

CPU: Intel Xeon (3.6GHz, 1MB L2, 800MHz bus)
 CPU MHz: 3600
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip (HT Technology disabled)
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 16K micro-ops I + 16KBD on chip
 Secondary Cache: 1024KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 2x1024MB PC2700 DDR-333
 Disk Subsystem: 1x230GB SATA
 Other Hardware:

Software

Operating System: Windows Server 2003 Standard (Build 3790)
 Compiler: Intel C++ Compiler 8.0 Build 20031211Z
 Microsoft Visual Studio .NET 7.0.9466 (for libraries)
 MicroQuill Smartheap Library 7.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
 +FDO*: FDO (-QxP not used in PASS1 to enable cross-compilation)

Base tuning for C programs: -QxP -fast +FDO* shlw32M.lib

Base tuning for C++ programs: -QxP -fast -GX -GR

Portability flags:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Peak tuning:

164.gzip: -QxP -fast -Oa +FDO ONESTEP=yes

175.vpr: -QxP -fast +FDO ONESTEP=yes

176.gcc: basepeak = 1

181.mcf: -QxP -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -QxP -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -QxP -fast +FDO ONESTEP=yes

252.eon: -QxP -O2 -Qipo +FDO*

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -QxP -fast +FDO ONESTEP=yes



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Notes/Tuning Information (Continued)

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255.vortex:  -QxP -fast          +FDO ONESTEP=yes shlw32M.lib
256.bzip2:   -fast -Oa -Qunroll1 +FDO ONESTEP=yes
300.twolf:   -QxP -fast -O3      +FDO* shlw32M.lib [-O3 not in Pass 1]
BIOS Configuration Notes
HT Technology disabled
Adjacent Sector Prefetch disabled
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Submitted_by: "Horton, Richard (Server Performance)" <richard.horton@hp.com>
Submitted: Mon Oct 4 18:32:37 2004
Submission: cpu2000-20041004-03422.sub