



CINT2000 Result

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SGI
SGI Altix 3000 (1000MHz, Itanium 2)

SPECint_rate2000 = 31.0
SPECint_rate_base2000 = 31.0

SPEC license #: 4 | Tested by: SGI | Test date: May-2003 | Hardware Avail: Feb-2003 | Software Avail: Apr-2003

| | | Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|----|----|-------------|-------------|--------------|------------|--------|---------|-------|
| 45 | 40 | 164.gzip | 4 | 230 | 28.3 | 4 | 230 | 28.3 |
| | 35 | 175.vpr | 4 | 217 | 30.0 | 4 | 217 | 30.0 |
| | 30 | 176.gcc | 4 | 133 | 38.3 | 4 | 133 | 38.3 |
| | 25 | 181.mcf | 4 | 358 | 23.4 | 4 | 358 | 23.4 |
| | 20 | 186.crafty | 4 | 144 | 32.2 | 4 | 144 | 32.2 |
| | 15 | 197.parser | 4 | 374 | 22.3 | 4 | 374 | 22.3 |
| | 10 | 252.eon | 4 | 156 | 38.7 | 4 | 156 | 38.7 |
| | 5 | 253.perlbnk | 4 | 266 | 31.5 | 4 | 266 | 31.5 |
| | | 254.gap | 4 | 206 | 24.7 | 4 | 206 | 24.7 |
| | | 255.vortex | 4 | 196 | 45.0 | 4 | 196 | 45.0 |
| | | 256.bzip2 | 4 | 231 | 30.1 | 4 | 231 | 30.1 |
| | | 300.twolf | 4 | 387 | 36.0 | 4 | 387 | 36.0 |

Hardware

CPU: Intel Itanium 2
 CPU MHz: 1000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 4 chips, 1 core/chip
 CPU(s) orderable: 4-64
 Parallel: No
 Primary Cache: 16KBI + 16KBD (on chip) per CPU
 Secondary Cache: 256KB (on chip) per CPU
 L3 Cache: 3.0MB (on chip) per CPU
 Other Cache: N/A
 Memory: 8 GB (16*512MB DIMMS per 4cpu module)
 Disk Subsystem: 1 x 36 GB SCSI (Seagate Cheetak 15k rpm)
 Other Hardware: None

Software

Operating System: SGI ProPack(TM) v2.1
 Compiler: Intel(R) C++ Compiler for Linux 7.1 (Build 20030327)
 File System: xfs
 System State: Single-user

Notes/Tuning Information

+FDO: PASS1=-prof_gen PASS2=-prof_use

Baseline optimization flags:

C programs: -ipo -O3 +FDO
 C++ programs: -ipo -O2 -ansi_alias +FDO

Portability Flags:

176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
 186.crafty: -DLINUX_i386
 252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST
 253.perlbnk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
 -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
 254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
 -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
 255.vortex: -DSPEC_CPU2000_LP64

Processes were bound to CPUs using dplace.

Peak flags same as baseline (basepeak=true set globally).