



# CINT2000 Result

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**Dell**  
**PowerEdge 1500SC (1.26 GHz PIII)**

SPECint\_rate2000 = 12.4

SPECint\_rate\_base2000 = 12.2

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Jan-2002 | Hardware Avail: Jan-2002 | Software Avail: Jun-2001

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	197	16.5	2	197	16.5
175.vpr	2	370	8.77	2	347	9.35
176.gcc	2	200	12.8	2	201	12.7
181.mcf	2	896	4.66	2	893	4.67
186.crafy	2	148	15.7	2	148	15.7
197.parser	2	404	10.3	2	404	10.3
252.eon	2	157	19.2	2	139	21.7
253.perlbnk	2	242	17.3	2	242	17.3
254.gap	2	268	9.52	2	267	9.55
255.vortex	2	198	22.2	2	193	22.8
256.bzip2	2	362	9.62	2	355	9.81
300.twolf	2	609	11.4	2	608	11.5

### Hardware

CPU: Intel Pentium III  
 CPU MHz: 1266  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 2 chips, 1 core/chip  
 CPU(s) orderable: 1, 2  
 Parallel: No  
 Primary Cache: 16KB(I) + 16KB(D) on chip  
 Secondary Cache: 512KB(I+D) on chip  
 L3 Cache: N/A  
 Other Cache: N/A  
 Memory: 1024MB SDRAM  
 Disk Subsystem: 1 x 9GB Quantum Atlas 10K II U160  
 Other Hardware:

### Software

Operating System: Windows 2000 Server (SP2 Build 2195)  
 Compiler: Intel C++ Compiler 5.0.1 (010727Z)  
 Microsoft Visual C++ 6.0 (SP5)  
 MicroQuill SmartHeap Library 5.0  
 File System: NTFS  
 System State: Default

## Notes/Tuning Information

### PORTABILITY FLAGS

176.gcc: -Dalloca=\_alloca /F10000000  
 186.crafy: -DNT\_i386  
 253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT  
 254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

### FEEDBACK-DIRECTED OPTIMIZATION

FDO: PASS1= -Qprof\_gen PASS2= -Qprof\_use

### BASE TUNING

C: -Qipo +FDO -QxK shlw32M.lib  
 C++: -Qipo -QxK -GX -GR

### PEAK TUNING

164.gzip: -Qipo +FDO -QxK shlw32M.lib  
 175.vpr: -Qipo +FDO -QxK  
 176.gcc: -Qipo +FDO -QxK shlw32M.lib  
 181.mcf: -Qipo +FDO -QxK shlw32M.lib  
 186.crafy: -Qipo +FDO -QxK shlw32M.lib  
 197.parser: -Qipo +FDO -QxK shlw32M.lib  
 252.eon: -Qipo +FDO -QxK  
 253.perlbnk: -Qipo +FDO -QxK shlw32M.lib  
 254.gap: -Qipo +FDO -QxK shlw32M.lib



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## Notes/Tuning Information (Continued)

```
255.vortex: -Qipo +FDO -QxK -Oa shlw32M.lib
256.bzip2:  -Qipo +FDO -QxK -Oa shlw32M.lib
300.twolf:  -Qipo +FDO -QxK -Oa shlw32M.lib
EXTRA LIBRARIES
shlw32M.lib: MicroQuill SmartHeap Library 5.0
              www.microquill.com
/numprocs=1 was added to c:\boot.ini file to disable additional CPUs on this system
```